

COMPUTER ORGANIZATION

Time : Three hours

Maximum : 100 marks

PART A — (6 × 5 = 30 marks)

Answer any SIX questions.

1. What is meant by Fetch cycle?
2. Draw the block diagram of the ADDER.
3. What is pipeline processing?
4. What is I/O bus? Explain.
5. What is ROM? Explain.
6. What is address sequencing?
7. Describe the process of asynchronous data transfer.
8. What is meant by handshaking?
9. What is an interrupt? Explain daisy chain priority interrupt.
10. List some control instructions.

PART B — (4 × 10 = 40 marks)

Answer any FOUR questions.

11. Explain DMA transfer with suitable examples.
12. Describe the process of addressing mapping.
13. Describe the working of a data communication processor.
14. Describe the role of program counter and other registers in addressing.
15. Describe LIFO and LRU page replacement algorithms.
16. Describe the working of ALU.

PART C — (2 × 15 = 30 marks)

Answer any TWO questions.

17. Explain with suitable diagrams arithmetic micro operations using parallel adder.
18. Describe the working RAM and ROM chips.
19. Write a note on
 - (a) Control Memory
 - (b) I/O interface
 - (c) DMA