

INFORMATION BROCHURE

C-DAC Common Admission Test (C-CAT) 2012
for admission to
Advanced Computing Training School (ACTS)
of
Centre for Development of Advanced Computing (C-DAC)
to be held on June 17, 2012 and July 1, 2012

1. Introduction

Centre for Development of Advanced Computing (C-DAC) is a Scientific Society of the Department of Information Technology (DeitY), Ministry of Communications and Information Technology (MC&IT), Government of India. Since its inception in the year 1988, C-DAC has pioneered the development of High Performance Computing (HPC). Towards fulfillment of this goal, C-DAC has advented the flexible and scalable parallel architecture, which has been realized in its celebrated 'PARAM' series of Supercomputers. C-DAC has now embarked upon the next stage in HPC with the national GRID initiative - Garuda Proof of Concept (PoC), all set to make available supercomputing resources to the entire scientific community across the country. Several other thematic areas of C-DAC research include Multilingual Computing, Professional Electronics, Software Technologies, Cyber Security, Health Informatics, and Ubiquitous Computing.

A major spin-off of the grand challenge expeditions into the various realms of state-of-the art technologies has been the emergence of a high quality intellectual resource base at CDAC in the form of an expert team with specialized knowledge of various facets of advanced computing. With such a resource base at its command, C-DAC has set up the Advanced Computing Training School (ACTS) to meet the ever-increasing skilled manpower requirements of the IT industry as well as supplement its intellectual resource base for cutting edge research and development.

C-DAC is committed to nation building through its Advanced Computing Training School (ACTS) and is the first Government agency that has expanded its training horizons globally, extending its high quality training services to countries like Mauritius, Ghana, Uzbekistan, Tajikistan, Dubai, Tanzania, Myanmar and Japan. Presently, ACTS is offering its courses through a network of over forty five training centres spread across the country wherein thousands of students and professionals are being trained to enhance their skills and equipped with the latest methodologies in advanced computing which enable them to make their mark in the Information and Communication Technology (ICT) industry.

C-DAC offers the following Post Graduate Diploma courses in areas of advanced computing:

- I. Post Graduate Diploma in Advanced Computing (PG-DAC)
- II. Post Graduate Diploma in Wireless & Mobile Computing (PG-DWiMC)
- III. Post Graduate Diploma in VLSI Design (PG-DVLSI)
- IV. Post Graduate Diploma in Embedded Systems Design (PG-DESD)
- V. Post Graduate Diploma in IT infrastructure, Systems and Security (PG-DITISS)
- VI. Post Graduate Diploma in Software Technology (PG-DST)
- VII. Post Graduate Diploma in Integrated VLSI and Embedded Systems (PG-DIVESD)
- VIII. Post Graduate Diploma in System Software Development (PG-DSSD)
- IX. Post Graduate Diploma in Healthcare Informatics (PG-DHI)
- X. Post Graduate Diploma in Geoinformatics (PG-DGi)
- XI. Post Graduate Diploma in Computational Linguistics and NLP (PG-DCLN)
- XII. Post Graduate Diploma in Automation & SCADA Systems(PG-DASS)

Dates of C-CAT 2012

**C-CAT 2012 will be held on June 17, 2012 and July 1, 2012.
A candidate can appear for any one C-CAT based on his/her suitability.**

2. General Information

(i) Note that from the current *academic year (2012-2013)*, admissions to all PG diploma courses will be made through C-DAC Common Admission Test (C-CAT 2012).

(ii) C-CAT 2012 will be conducted in the form of three test papers labeled as:

SECTION - A

English, Logical Reasoning and Quantitative Aptitude.

SECTION - B

C Programming, OOP and Operating Systems, Data Structures, Data Communications and Network.

SECTION - C

Computer Architecture, Digital Electronics and Microprocessors.

Each test paper will be of one hour duration, having 50 questions of 3 marks each (maximum 150 marks). Depending upon the programme(s) in which the candidate is seeking admission, he/she will have to either appear in just one test paper (SECTION A) or in two test papers (SECTION A and SECTION B) or in all three test papers (SECTION A, SECTION B and SECTION C).

(iii) Candidates who qualify in C-CAT 2012 will be offered admission to Post Graduate Diploma courses on the basis of their ranks, preferences and availability of seats. There is no age restriction to appear in C-CAT 2012.

(iv) C-CAT 2012 will be held on June 17, 2012 and July 1, 2012. Candidates may choose one of the dates as per their convenience while filling the application. The choice of date once made will not be altered unless approved in writing by C-DAC.

(v) To apply for admission to a desired programme, a candidate is required to qualify in the corresponding test paper(s) as indicated in Table 1 and also satisfy the minimum eligibility criteria of the respective academic programme.

(vi) The candidates who have either appeared or are due to appear in the final examination of their qualifying degree in 2012 are also eligible to appear in the test. By qualifying in C-CAT 2012, candidates can apply for provisional admission subject to the condition that:

(a) All parts of their final examination shall be completed by the date of registration in the programme.

(b) Proof of having passed the qualifying degree with required eligibility will be submitted by 30th September 2012.

(vii) The candidates will be provided ranks based on their performance in Section A, Section A+B and Section A+B+C. If a candidate appears in multiple sections, he/she shall be provided multiple ranks accordingly. For example if a candidate appears in Section A and Section B, he/she shall be provided two ranks, based on performance in Section A and based on performance in Section A and B. A candidate can appear only in those sections corresponding to the programmes which are chosen at the time of filling in the application. A candidate, who has not appeared for a particular section, will not get any position in the merit list, which spans over that section.

(viii) **Candidates should note that mere appearance in C-CAT 2012 or being in any of the merit list neither guarantees nor provides any automatic entitlement to admission.** Qualified candidates will have to apply for admission as per the prescribed procedure. Admissions shall be made in order of merit based on the choice exercised by the candidate and depending on the number of seats available in the programmes at the Admitting Centre(s).

(ix) With regard to the interpretation of the provisions of any matter not covered in this Information Brochure, the decision of the C-DAC shall be final and binding on all the parties concerned.

3. Details of Courses and Eligibility Criteria

The candidates appearing or have qualified in any of the qualifying degrees as listed below shall be eligible. The minimum marks in qualifying degree are specific to the programs are also mentioned below.

3.1 Post Graduate Diploma in Advanced Computing (PG-DAC): The Post Graduate Diploma in Advanced Computing (DAC) is the flagship programme of ACTS. The course is targeted towards engineers and IT professionals who wish to venture into the domain of advanced computing. The course aims to groom the students to enable them to work on current technology scenarios as well as prepare them to keep pace with the changing face of technology and the requirements of the growing IT industry. The course curriculum has been designed keeping in view the emerging trends in advanced computing as well as contemporary and futuristic human resource requirements of the ICT industry. The entire course syllabus, courseware, teaching methodology and the course delivery have been derived from the rich research and development background of C-DAC. The depth and width of the course is unique in the industry covering a wide spectrum of requirements of the ICT industry. Running successfully for more than fifteen years, the DAC course has yielded thousands of students, who are well positioned in the industry today.

Course Highlights:

- 24 weeks full-time Post-Graduate Course
- Well-equipped laboratory and can be accessed 24 hours.
- Live exercises relevant to the standards of industry.
- Placement assistance.
- Corporate / industry interaction
- Exposure to soft skills.
- Best Student award

Eligibility Criteria:

- Engg Graduate in any discipline or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./ AMIE/ AIETE/ DoEACC B level etc)
- PG in Engg. Sciences (Ex. MCA/M. Sc. Engg, M. Sc. (CS, IT, EE, Electronics, Instrumentation etc)
- Post Graduates in Math or allied areas (Ex. Statistics, Applied Math)
- PG in Physics or Computational Sciences/Post Graduates in Computing or Management are also eligible provided they hold graduation degree in Sciences.

Minimum Qualifying Marks: The candidates must have secured a minimum of 50% marks in their qualifying degree.

A candidate seeking admission to PG-DAC course must appear for SECTION A and B test papers of C-CAT 2012.

3.2 Post Graduate Diploma in Wireless and Mobile Computing (PG-DWiMC): The Post Graduate Diploma in Wireless and Mobile Computing (WiMC) is one of the flagship programmes of ACTS. The course is targeted towards engineers who wish to venture into the domain of mobile computing. The course aims to groom the students to enable them to work on current technology scenarios as well as prepare them to keep pace with the changing face of technology and requirements of an exponentially growing Mobile and Wireless industry. The Course curriculum has been designed keeping in view the emerging trends in wireless technologies as well as contemporary and futuristic human resource requirements of the Wireless and Mobile Industry. The entire course syllabus, courseware, teaching methodology and the course delivery have been derived from the rich research and development background of C-DAC. The depth and width of the course is unique in the industry covering a wide spectrum of requirements of the ICT industry.

Course Highlights:

- 24 weeks full-time Post-Graduate Course
- Well-equipped laboratory and can be accessed 24 hours
- Live exercises relevant to the standards of industry
- Corporate / industry interaction
- Exposure to soft skills
- Placement assistance

Eligibility Criteria:

- Graduate in Engg or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./ AMIE/ AIETE/ DoEACC B level etc) in Electronics/ Electrical/Computer Science or related areas.
- PG in Engg. Sciences (Ex. MCA/M. Sc. Engg, M. Sc. (CS, IT, EE, Electronics, Instrumentation etc)
- Post Graduates in Math or allied areas (Ex. Statistics, Applied Math)

Minimum Qualifying Marks: The candidates must have secured a minimum of 50% marks in their qualifying degree.

A candidate seeking admission to PG-DWiMC course must appear for SECTION A and B test papers of C-CAT 2012

3.3 Post Graduate Diploma in VLSI Design (PG-DVLSI): The Post Graduate Diploma in VLSI Design (DVLSI) is a pioneering course conducted by ACTS to assist engineers who wish to gain advanced knowledge in the domain of VLSI design. The course aims to impart the students with theoretical as well as practical knowledge in the field of VLSI design. It will also prepare them to keep pace with the changing trends of VLSI technology and the requirements of an ever-growing VLSI design industry. The entire course syllabus, courseware, teaching methodology and the course delivery have been derived from the rich research and development background of C-DAC, which has a legacy of designing the PARAM range of supercomputers over the last 18 years.

Course Highlights:

- 24 weeks full-time Post Graduate course
- Seven Modules plus Project
- Evaluation at the end of every module
- Curriculum designed by industry experts
- Well-equipped laboratory and can be accessed 24 hours.
- Well-equipped library
- Extensive hands on sessions
- Limited Internet access.
- Exposure to soft skills.
- Placement assistance.

Eligibility Criteria:

- Graduate in Engg or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./AMIE/ AIETE/ DoEACC B level etc.) in Electronics/ Electrical/Computer Science or related areas.
- Post Graduate in Engg. Sciences (Ex. MCA/M. Sc. Engg, M.Sc. (CS, IT, EE, Electronics, Instrumentation etc.)

Minimum Qualifying Marks: The candidates must have secured a minimum of 55% marks in their qualifying degree.

A candidate seeking admission to PG-DVLSI course must appear for SECTION A, B and C test papers of C-CAT 2012.

3.4 Post Graduate Diploma in Embedded Systems Design (PG-DESD): Realizing the growth of embedded systems in day-to-day life and the need for trained manpower in this promising area, C-DAC has launched Post Graduate Diploma in Embedded Systems Design (PG-DESD) for Engineers in computers, electronics and IT. Embedded Systems is a unique field, where engineers need to have sound

knowledge in hardware and software design. Keeping this aspect in view, C-DAC has designed the Diploma giving equal emphasis to hardware and software, enabling engineers to face challenges in the design and development of state-of-the-art embedded systems. The latest curriculum includes a module on wireless embedded systems design, specifically focusing on wireless technologies and Wireless Sensor Networks (WSN).

Course Highlights: Post Graduate Diploma in Embedded Systems Design (PG-DESD) is a 22 weeks full-time course consisting of seven modules, an industry relevant project and a seminar.

Eligibility Criteria:

- Graduate in Engg or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./AMIE/ AIETE/ DoEACC B level etc.) in Electronics/ Electrical/Computer Science or related areas.
- Post Graduate in Engg. Sciences (Ex. MCA/M. Sc. Engg, M.Sc. (CS, IT, EE, Electronics, Instrumentation etc.)

Minimum Qualifying Marks: The candidates must have secured a minimum of 55% marks in their qualifying degree.

A candidate seeking admission to PG-DESD course must appear for SECTION A, B and C test papers of C-CAT 2012.

3.5 Post Graduate Diploma in IT Infrastructure, Systems and Security (PG-DITISS): The Post Graduate Diploma in IT Infrastructure, Systems and Security (DITISS) is a 24 weeks full time programme targeted towards grooming students in the arena of human-computer interaction and Cyber & Network Security. The objective of this course is to enable the student to understand the concepts of network security and learn the techniques of detecting the attacks and securing a network from internal and external attacks. At the end of the course, the student will be able to understand a variety of generic security threats and vulnerabilities, understand the principles and practices of cryptographic techniques, identify and analyze particular security problems for a given application and apply appropriate security techniques to solve security problems.

Course Highlights:

- 24 weeks full-time Post-Graduate Diploma Course
- Thirteen modules followed by a project
- Well-equipped laboratory with 24 hours access
- Well-equipped library
- Extensive hands on sessions
- Exposure to Soft Skills

- Limited Internet Access
- Placement Assistance
- Corporate / Industry Interaction

Eligibility Criteria:

- Graduate in Engg or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./ AMIE/ AIETE/ DoEACC B level etc) in Electronics/ Electrical/Computer Science or related areas.
- PG in Engg. Sciences (Ex. MCA/M. Sc. Engg, M. Sc. (CS, IT, EE, Electronics, Instrumentation etc)
- Post Graduates in Math or allied areas (Ex. Statistics, Applied Math)

Minimum Qualifying Marks: The candidates must have secured a minimum of 55% marks in their qualifying degree.

A candidate seeking admission to PG-DITISS course must appear for SECTION A and B test papers of C-CAT 2012.

3.6 Post Graduate Diploma in Software technology (PG-DST)

The long-established 1-year full-time Post-Graduate Diploma in Software Technology (PG-DST) is designed for fresh or experienced graduates looking forward to pursue further education and training in software technology and development processes. The course comprises such modules as Mathematical Foundations of Computer Science, Programming Fundamentals (C, Java & Data Structures), Operating Systems, Computer Networks, DBMS, Web Technologies, Enterprise Computing, Object Oriented Analysis & Design, Software Engineering and Project, and the unique Machine Graded Programming Test.

Eligibility Criteria: Graduate in any subject from a recognized Board/University

Candidate seeking admission to PG-DST course must appear for SECTION A and B test papers of C-CAT 2012.

3.7 Post Graduate Diploma in Integrated VLSI and Embedded Systems Design (PG-DIVESD):

The Post Graduate Diploma in Integrated VLSI and Embedded System Design (PG-DIVESD) is a 24 weeks full time program targeted towards grooming students in the area of Embedded and VLSI designing.

Course Highlights:

- 24 weeks full time Post Graduate Diploma course
- 10 modules followed by project
- Evaluation at the end of the each module

- Well equipped laboratory which can be accessed 24 x 7
- Well equipped library
- Extensive hands on sessions
- Limited internet access
- Exposure to soft skills
- Placement assistance

Eligibility Criteria:

- Graduate in Engg or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./AMIE/ AIETE/ DoEACC B level etc.) in Electronics/ Electrical/Computer Science or related areas.
- Post Graduate in Engg. Sciences (Ex. MCA/M. Sc. Engg, M.Sc. (CS, IT, EE, Electronics, Instrumentation etc.)

Minimum Qualifying Marks: The candidates must have secured a minimum of 55% marks in their qualifying degree

A candidate seeking admission to PG-DIVESD course must appear for SECTION A, B and C test papers of C-CAT 2012.

3.8 Post Graduate Diploma in System Software Development (PG-DSSD): System Software is an important area of the computer system. It helps in managing and controlling the hardware and also provides convenient environment for the application software. Software like operating systems, device drivers, compilers, loaders, virtual machines, libraries etc comes under the category of system software, whereas software like computer games, online ticket booking, payroll software etc are classified as application software. System software has to be reliable, robust, fast performing, memory efficient and free from security issues like buffer overflow and format string vulnerabilities. Developing this kind of software is a challenging task as knowledge of system internals and quality (time, memory efficient and robust) programming skills are required. There is a gap between academia and industry requirements in this specific area of software development Manpower having these skills is scarce and there are not many practical oriented training courses. However, demand for these skill sets is fast growing in the development of software specific to domains like Telecommunications, Networking, Cyber Security, Operating Systems, Grid Computing, Ubiquitous Computing, Medical Equipments etc. Usage of wide range of operating systems and applications containing smaller operating system footprint in embedded devices is growing. Understanding the system call interface of operating system and operating system internals is also required in writing efficient application specific libraries and application programs. Knowledge on Operating system features like resource sharing and management, flexibility through separating the role of mechanisms and policies, buffer management, module level interfaces, robustness, performance, security and protection will also help in writing efficient application software. It is the most successful career oriented course “Diploma in System Software

Development” (DSSD) for Engineers in Electronics, Computer Science and Information Technology and also for Computer Science Post Graduates.

Course Highlights: Post Graduate Diploma in System Software Development (PG-DSSD) is a 22 weeks fulltime course consisting of 9 modules including an industry relevant project and a seminar. Daily session comprises of two hours of classroom lectures followed by four hours of lab work. Project work for one month will be assigned to the students during the course.

Eligibility Criteria:

- Graduate in Engg or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./AMIE/ AIETE/ DoEACC B level etc.) in Electronics/ Electrical/Computer Science or related areas.
- Post Graduate in Engg. Sciences (Ex. MCA/M. Sc. Engg, M.Sc. (CS, IT, EE, Electronics, Instrumentation etc.)

Minimum Qualifying Marks: The candidates must have secured a minimum of 55% marks in their qualifying degree.

A candidate seeking admission to PG-DSSD course must appear for SECTION A and B test papers of C-CAT 2012.

3.9 Post Graduate Diploma in Healthcare Informatics (PG-DHI): The Postgraduate Diploma in Healthcare Informatics (PG-DHI) is a 24 weeks full time program targeted towards grooming students in the area of Healthcare Informatics. The objective of this course is to enable the student to understand the art and applicable science of introducing, managing, and organizing information and technologies related to human healthcare, and making it useful for problem solving using latest state-of-art technologies. At the end of the course the student will be able to understand the concepts in Healthcare informatics, learn the technologies and skills required for and issues involved in aggregation and analysis of information regarding various factors interacting in healthcare. They will be able to apply appropriate techniques to solve problems in various application areas in healthcare informatics.

Course Highlights:

- 24 weeks full time Post Graduate Diploma course
- 9 modules followed by project
- Well equipped laboratory which can be accessed 24 x 7
- Well equipped library
- Extensive hands on sessions
- Limited internet access
- Exposure to soft skills

- Placement assistance
- Expert Faculty members with knowledge of interdisciplinary areas of Healthcare Informatics

Eligibility Criteria:

- Engg. Graduates in any discipline or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./ AMIE/ AIETE/ DoEACC B level etc) in Electronics/ PG in Engg. Sciences (Ex. MCA/M. Sc. Engg, M. Sc. (CS, IT, EE, Electronics, Instrumentation etc)
- Post Graduates in Math or allied areas (Ex. Statistics, Applied Math)
- PG in Physics or Computational Sciences/Post Graduates in Life Sciences /Chemistry or Graduates (Minimum 4 years) in Medicinal, Biomedical or Pharmaceutical Sciences are also eligible.

Minimum Qualifying Marks: The candidates must have secured a minimum of 50% marks in their qualifying degree.

A candidate seeking admission to PG-DHI course must appear for SECTION A test paper of C-CAT 2012

3.10 Post Graduate Diploma in Geoinformatics (PG-DGi): Geoinformatics or Geomatics is the synergy of multiple disciplines, namely, GIS, remote sensing, photogrammetry, cartography, GPS and geodesy. It is fundamental to all the disciplines, which use data identified by their locations. Geomatics deals with spatial and non-spatial data, their methods of acquisition, management, analysis, display, and dissemination. Applications of geomatics are mainly oriented to real world management problems pertaining to natural and man-made environments. The Post Graduate Diploma in Geoinformatics (PG-DGi) aims to provide conceptual knowledge on GIS, remote sensing and related fields, and hands-on training in GIS, remote sensing data interpretation, digital image processing, digital photogrammetry, digital cartography and GPS. There are also three electives i.e Geoinformatics Business & Project Management, GIS Development and RDBMS. The course contents have been designed keeping in view the emerging trends in the field of Geoinformatics and the increasing needs of skilled manpower.

Course Highlights: The Diploma in Geoinformatics is a 6 months full time course consisting of 9 modules (8 compulsory and one elective) and a dissertation project, with evaluation at the end of each module. The course curriculum demands full time attendance and commitment from the students. Each daily session comprises 3 hours of classroom lecture followed by at least 3 hours of lab work. Projects of 2 months duration are assigned to the students during the course. Application of Geoinformatics in various development contexts will be analysed through appropriate case studies. Tutorials and demonstrations are aimed to familiarise the students with emerging technologies in Geoinformatics.

Eligibility Criteria:

- Graduate in Engg or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./ AMIE/ AIETE/ DoEACC B level etc) in Electronics/ Electrical/Computer Science or related areas.
- PG in Engg. Sciences (Ex. MCA/M. Sc. Engg, M. Sc. (CS, IT, EE, Electronics, Instrumentation etc)
- Post Graduates in Math or allied areas (Ex. Statistics, Applied Math)
- PG in Physics or Computational Sciences/Post graduate in Applied Sciences/PG in Geography, Geology & allied areas are also eligible.

Minimum Qualifying Marks: The candidates must have secured a minimum of 50% marks in their qualifying degree.

A candidate seeking admission to PG-DGi course must appear for SECTION A test paper of C-CAT 2012.

- 3.11 Post Graduate Diploma in Computational Linguistics and NLP (PG-DCLN):** The Post Graduate Diploma in Computational Linguistics and NLP (PG-DCLN) is a 6-months full time program targeted towards grooming students in the arena of human-computer interaction and computational modeling as well as simulation of human language. Language is widely known as what makes us human and we instantly know its value in communication and development of ideas. Transferring these skills to computers is the challenge of Computational Linguistics. The PG-DCLN program aims to provide a self-contained introduction to the central issues of Natural Language Processing (NLP), various practical skills associated with the design and implementation of NLP systems, and to prepare students (software engineers and linguists) for computer modeling for the development language technology tools and applications. It combines elements of Computer Science, Linguistics, Software Engineering, Artificial Intelligence (AI) and Statistics that helps linguists and computer scientists to explore the emerging trends in NLP and AI as well as contemporary research in these fields. The indepth courseware, practical sessions prepare the students for a variety of positions in this rapidly growing industry.

Course Highlights:

- 24 weeks (900 Hrs) full-time Post-Graduate Diploma Course
- Six Modules followed by a project
- Well-equipped laboratory and can be accessed 24 hours
- Well-equipped library
- Extensive hands on sessions
- Live exercises relevant to the standards of industry
- Limited Internet access
- Exposure to soft skills

- Placement assistance

Eligibility Criteria:

- Graduate in Engg or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./ AMIE/ AIETE/ DoEACC B level etc) in Electronics/ Electrical/Computer Science or related areas.
- PG in Engg. Sciences (Ex. MCA/M. Sc. Engg, M. Sc. (CS, IT, EE, Electronics, Instrumentation etc)
- Post Graduates in Math or allied areas (Ex. Statistics, Applied Math)
- PG in Physics or Computational Sciences/Post graduates in Applied Sciences/ Linguistics/ Applied Linguistics /Language /Literature are also eligible.

Minimum Qualifying Marks:The candidates must have secured a minimum of 50% marks in their qualifying degree.

A candidate seeking admission to PG-DCLN course must appear for SECTION A test paper of C-CAT 2012.

3.12 Post Graduate Diploma in Automation SCADA Systems (PG-DASS): The Post Graduate Diploma in Automation SCADA Systems (PG-DASS) is a 24 weeks full time program targeted towards grooming students in the area of SCADA systems.

Course Highlights:

- 24 weeks full time Post Graduate Diploma course
- 10 modules followed by project
- Evaluation at the end of the each module
- Well equipped laboratory which can be accessed 24 x 7
- Well equipped library
- Extensive hands on sessions
- Limited internet access
- Exposure to soft skills
- Placement assistance

Eligibility Criteria:

- Graduate in Engg or equivalent (Ex. BE/B. Tech/4 years B. Sc Engg./ AMIE/ AIETE/ DoEACC B level etc) in Electronics/ Electrical/Computer Science or related areas.
- PG in Engg. Sciences (Ex. MCA/M. Sc. Engg, M. Sc. (CS, IT, EE, Electronics, Instrumentation etc)
- Post Graduates in Math or allied areas (Ex. Statistics, Applied Math)

Minimum Qualifying Marks: The candidates must have secured a minimum of 55% marks in their qualifying degree.

A candidate seeking admission to PG-DASS course must appear for SECTION A, B and C test papers of C-CAT 2012.

4. Important Points

(i) It will entirely be the responsibility of the candidate to prove that he/she satisfies the minimum eligibility requirements for the programme in which he/she is seeking admission.

(ii) C-DAC has the right to cancel, at any stage, the admission of a candidate who is found to have been admitted to a course to which he/she is not entitled, being unqualified or ineligible in accordance with the rules and regulations in force.

5. Pattern of Test Paper

C-CAT-2012 will be conducted in the form of three objective type test papers as following:

- Section A (English, Critical Reasoning and Quantitative Aptitude)
- Section B (Programming in C, Data Structures, Data Communications and Networking, OOP and Operating Systems)
- Section C (Computer Architecture, Digital Electronics and Microprocessors),

Each test paper shall be of one hour duration, having 50 questions of 3 marks each (maximum 150 marks). Depending upon the choice(s) of the programme(s) made by the candidate he/she will have to either appear in just one test paper (Section A) or in two test papers (Section A and Section B) or in all three test papers (Section A, B and C).

These test papers have to be answered in a machine-gradable Optical Mark Reader (OMR) by darkening appropriate bubbles using 2B pencil only. Since the OMR will be evaluated by electronic means, it is imperative that the instructions given on the question paper booklet and OMR are carefully read and followed by the candidates. The objective type questions in all the test papers will have four choices as possible answers, of which, only one will be correct. +3 marks will be given for every correct answer and -1 mark will be deducted for every wrong answer. Multiple answers to a question will be treated as a wrong answer. For each un-attempted question shall be awarded 0 (Zero) marks.

Note:

(a) Use of logarithmic tables/calculator of any kind/cellular phone/electronic gadgets is NOT permitted in the examination hall.

(b) The medium for all the test papers will be English only.

(c) Use of unfair means by a candidate in C-CAT 2012, whether detected at the time of test, evaluation or at any other stage, will lead to cancellation of his/her candidature as well as disqualification of the candidate from appearing in C-CAT 2012 in future.

(d) Disclosure of identity in any form, such as writing registration id or name in any place in the OMR other than the one designated for the purpose. Making any kind of distinguishing marks in the OMR may also lead to disqualification of the candidate.

Table 1: Courses and corresponding exam sections

Programme(s)	Test paper(s) to be taken
PG-DGi	Section A
PG-DHI	Section A
PG-DCLN	Section A
PG-DAC	Section A + Section B
PG-DWIMC	Section A + Section B
PG-DSSD	Section A + Section B
PG-DITISS	Section A + Section B
PG-DST	Section A + Section B
PG-DESD	Section A + Section B+ Section C
PG-DVLSI	Section A + Section B+ Section C
PG-DASS	Section A + Section B+ Section C
PG-DIVESD	Section A + Section B+ Section C

6. Test Schedule

The schedule for different test papers of C-CAT 2012 is given in Table 2.

Table 2: Test schedule for C-CAT 2012

Date	Time	Test Paper
June 17, 2012	10:00 AM–11:00 AM	SECTION A
	11:00 AM–12:00 AM	SECTION B
	12:00 Noon–01:00 PM	SECTION C
July 01, 2012	10:00 AM–11:00 AM	SECTION A
	11:00 AM–12:00 AM	SECTION B
	12:00 Noon–01:00 PM	SECTION C

The test schedule will not be changed under any circumstance.

7. Choice of Examination Centres

The location of test centres for C-CAT 2012 is given in **Table 3**. A centre once allotted will not normally be changed. In exceptional circumstances, a change of centre may be permitted if a request with a valid reason for the same is received in the office of C-DAC, **ACTS, 5th Floor, NSG IT Park, Aundh, Pune-411007, at least one week before the exam date.**

Table 3: Location of C-CAT Centres

State	Test Centres
Kerala	Thiruvanthapuram, Kochi
Tamil Naidu	Chennai, Coimbatore
Karnataka	Bengaluru
Andhra Pradesh	Hyderabad, Visakhapatnam
Odissa	Bhubaneshwar
Goa	Panaji
Maharashtra	Mumbai, Nasik, Nagpur, Thane, Pune, Solapur, Kolhapur, Karad
Gujarat	Ahmedabad
Rajasthan	Jaipur, Sikar
Chhattisgarh	Raipur
Madhya Pradesh	Indore, Bhopal, Gwalior
Assam	Guwahati,
Tripura	Agartala
Punjab	Mohali
Uttar Pradesh	Noida, Allahabad, Lucknow
NCR	New Delhi
Bihar	Patna
Jharkhand	Dhanbad
West Bengal	Kolkata

8. Application Procedure for C-CAT 2012

Candidates may apply for C-CAT 2012 through an “online” procedure at www.cdac.in or acts.cdac.in. While filling the application form, candidate needs to upload his/her passport size photograph. Candidates are advised to download and take print out of their filled application forms for future reference.

a) Application Fee: The basic application fee would be Rs.500/- and thereafter Rs.100/- for every programme opted. Hence the application fee for one programme will be Rs.600/- and Rs.700/- for two programmes and so on. Candidates paying through DD have to pay Rs. 25 extra in addition to the total application fee.

b) Payment Mode:

- **Online Mode:** The application fee can be paid on both Credit and Debit Card through the payment gateway provided, along with the online application form.
- **Demand Draft Mode:** Demand Draft Mode: The application fee can be paid by a Demand Draft drawn in favor of '**C-DAC Training**', on any Nationalized Bank, payable at Hyderabad. After electronically filling his/her personal data and information pertaining to the Application Form on the website, the applicant can take a print out of the application slip generated online and enclose it along with the DD and send it to the following address:

C-CAT Coordinator
C-DAC, Nalanda Building, No. 1 Shivabagh
Satyam Theatre Road
Ameerpet, Hyderabad - 500016
Andhra Pradesh (India)
Phones: +91-40-23737127/28
Fax: +91-040-23743382

Note: Please note that the candidates should mention their Name, Form No. mail ID/contact number on the reverse side of the Demand Draft.

9. Admit Card

The Admit Cards can be downloaded from the website from 14th June 2012 for C-CAT 2012 scheduled on June 17, 2012 and for C-CAT 2012 scheduled on July 1, 2012 the admit cards can be downloaded from the website from June 27, 2012 onwards. The candidates will not be able to download their Admit Cards, if their DDs have not been received at the C-DAC centre. The candidate should carefully examine the Admit Card received by him/her for all the entries made therein. In case of any discrepancy, the candidate should inform C-DAC immediately. If the candidate is not able to download the online admit card, then C-DAC may be contacted through Phone/Fax/Email, giving the Application Form Number and Name. No candidate will be permitted to appear in C-CAT 2012 test without a valid Admit Card. The Admit Card should be presented to the invigilators/C-CAT 2012 officials for verification.

The Admit Card of C-CAT 2012 must be carefully preserved by the candidate and produced at the time of admission/registration, if required by the Admitting Centre. The C-DAC may withdraw the permission granted to a candidate to appear in C-CAT-2012, if found that he/she is not eligible to appear in the Test even though an Admit Card has been issued and is produced by the candidate before the Presiding Officer of the test centre.

10. Training Centres

The list of training centres and Authorized Training centres where the courses would be conducted are as listed below in Table 4(a) and Table 4(b).

Table 4 (a): Training Centres

Name of the Centre	Course Name
C-DAC Bengaluru	PG-DAC, PG-DWiMC, PG-DSSD, PG-DESD, PG-DASS, PG-DST, PG-DITISS
C-DAC Chennai	PG-DAC, PG-DSSD, PG-DESD
C-DAC, Hyderabad	PG-DAC, PG-DSSD, PG-DESD
C-DAC, Kolkata	PG-DWiMC, PG-DESD
C-DAC, Mohali	PG-DSSD, PG-DITISS, PG-DESD
C-DAC Mumbai	PG-DAC, PG-DWiMC, PG-DST
C-DAC Noida	PG-DGI, PG-DAC, PG-DWiMC, PG-DITISS, PG-DESD, PG-DIVESD
C-DAC Pune	PG-DAC, PG-DWiMC, PG-DITISS, PG-DESD, PG-DVLSI, PG-DGI, PG-DCLN, PG-DHI
C-DAC Thiruvananthapuram	PG-DESD

**Table 4 (b): C-DAC Satellite Centres and
Authorised Training Centres**

Name of the Centre	Course Name
<u>Satellite Centres</u>	
National Institute of Technology, Agartala	PG-DAC
Motilal Nehru National Institute of Technology, Allahabad	PG-DAC
<u>Authorised Training Centres</u>	
Gateway Education and Training, Ahmedabad	PG-DAC
Vimarshana Technology Solutions Pvt. Ltd., Bengaluru	PG-DAC, PG-DWiMC
Lakshya, Bhubaneswar	PG-DAC
Zip2Zenith Learning Pvt. Ltd., Gurgaon	PG-DAC
Mastishka Advanced Computing School, Indore	PG-DAC
Sunbeam Institute of Information Technology, Karad	PG-DAC
Global Infotech Learning, Kolhapur	PG-DAC
Mumbai Educational Trust, Mumbai	PG-DAC
YCP Academy of Information Technology, Mumbai	PG-DAC
Vidyanidhi Info Tech Academy, Mumbai	PG-DAC
Thakur Institute of Career Advancement, Mumbai	PG-DIVESD, PG-DVLSI
Soft Polynomials, Nagpur	PG-DAC, PG-DVLSI, PG-DIVESD
MET IIT Bhujbal Knowledge City, Nashik	PG-DAC
Bytes Softech Pvt. Ltd., Delhi	PG-DAC, PG-DITISS
Career Foresight, Patna	PG-DAC
Institute of Advanced Computing and Software Development, Pune	PG-DAC
Infoway Technologies Pvt. Ltd., Pune	PG-DAC
Sunbeam Institute of Information Technology, Pune	PG-DAC, PG-DWiMC, PG-DIVESD
Institute of Emerging Technologies, Pune	PG-DAC
Knowledge IT, Pune	PG-DAC
NETCOM, Sikar	PG-DAC
Ashwath Infotech Pvt. Ltd., Solapur	PG-DAC
NIELIT Aurangabad	PG-DESD

11. Important Dates

Sl. No.	Activity	Date	
1	Online Application Form	Start Date	May 7, 2012
		End Date for C-CAT 2012 -17 th June 2012	12 th June 2012
		End Date for C-CAT 2012 – 1 st July 2012	25 th June, 2012
2	Last Date of Receipt of DD at C-DAC Centres for C-CAT 2012 scheduled on June 17, 2012	12th June 2012	
	Last Date of Receipt of DD at C-DAC centres for C-CAT 2012 scheduled on July 1 2012	25 th June, 2012	
3.	Start date for downloading Admit Cards for C-CAT scheduled on 17 th June 2012	14 th June 2012	
	Start date for downloading Admit Cards for C-CAT scheduled on 1 st July 2012	27 th June 2012	
4.	Date of C-CAT 2012	1 st C-CAT 2012	17th June 2012
		2nd C-CAT 2012	1st July 2012

IMPORTANT NOTE

In all matters concerning C-CAT 2012, the decision of C-DAC will be final and binding on all the applicants.