### GAUTAM BUDDH TECHNICAL UNIVERSITY, LUCKNOW

### SCHEDULE FOR M. TECH.( MODULAR) EXAMINATION 2010-11 M. TECH. (DIGITAL COMMUNICATION)-III- SEMESTER

Date	Day	Timing	Paper Code	Subject
1	2	3	4	5
08-01-2011	Saturday	10.00 AM to 1.00 PM	DCE-42	Satellite Communication
09-01-2011	Sunday	GAP		
10-01-2011	Monday	10.00 AM to 1.00 PM	DCE-33	Radar Engineering

**Controller of Examination** 

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# GAUTAM BUDDH TECHNICAL UNIVERSITY, LUCKNOW SCHEDULE FOR M. TECH.( MODULAR) EXAMINATION 2010-11 M. TECH. (MECHANICAL ENGINEERING)-III- SEMESTER

Day	Timing	Paper Code	Subject
2	3	4	5
Saturday	10.00 AM to 1.00 PM	MME-306	Advanced thermal engineering
Sunday	GAP		
Monday	10.00 AM to 1.00 PM	OEL-309	Environmental Pollution & Its control
	Saturday	Saturday 10.00 AM to 1.00 PM  Sunday  Monday 10.00 AM to 1.00 PM	2 3 4  Saturday 10.00 AM to 1.00 PM MME-306  Sunday GAP  Monday 10.00 AM to 1.00 PM OEL-309

**Controller of Examination** 

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#### GAUTAM BUDDH TECHNICAL UNIVERSITY, LUCKNOW

#### SCHEDULE FOR M. TECH.( MODULAR) EXAMINATION 2010-11

#### M. TECH. (Computer Science & Engg.)-III- SEMESTER

Date	Day	Timing	Paper Code	Subject
1	2	3	4	5
08-01-2011	Saturday	10.00 AM to 1.00 PM	CS/IT-72 CS/IT-42	Cryptography Data Warehousing
09-01-2011	Sunday	GAP		
10-01-2011	Monday	10.00 AM to 1.00 PM	CS/IT-22 CS/IT-23	Object oriented programming Object oriented systems

Controller of Examination

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#### GAUTAM BUDDH TECHNICAL UNIVERSITY, LUCKNOW

#### SCHEDULE FOR M. TECH.( MODULAR) EXAMINATION 2010-11

#### M. TECH. (Industrial Electronics & Control)-III- SEMESTER

Date	Day	Timing	Paper Code	Subject
1	2	3	4	5
08-01-2011	Saturday	10.00 AM to 1.00 PM	IEC-931	Control Techniques in industrial Electronics
09-01-2011	Sunday	GAP		
10-01-2011	Monday	10.00 AM to 1.00 PM	IEC-932	Modern Control System
11-01-2011	Tuesday	GAP		
12-01-2011	Wednesday	10.00 AM to 1.00 PM	IEC-933	Advanced Digital Signal processing
13-01-2011	Thursday	10.00 AM to 1.00 PM	IEC-9344	Opto-Electronics Instrumentation

Controller of Examination

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## GAUTAM BUDDH TECHNICAL UNIVERSITY, LUCKNOW SCHEDULE FOR M. TECH.( MODULAR) EXAMINATION 2010-11 M. TECH. (VLSI DESIGN )-III- SEMESTER

Date	Day	Timing	Paper Code	Subject
1	2	3	4	5
08-01-2011	Saturday	10.00 AM to 1.00 PM	VD-301	VLSI Test & Testability
09-01-2011	Sunday	GAP		
10-01-2011	Monday	10.00 AM to 1.00 PM	VD-03	Elective -III

Controller of Examination

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