



B.Sc III Year - Electronics

PAPER – III Digital Electronics and Microprocessor (90 hours)

UNIT-I (23 HOURS)

Introduction to number systems, Logic gates OR, AND, NOT, XOR, NAND, NOR gates - Truth tables – Positive and negative logic – Logic families and their characteristics – RTL, DTL, ECL, TTL and CMOS.– Universal building blocks NAND and NOR gates. Laws of Boolean algebra De Morgan's Theorems – Boolean identities – Simplification of Boolean expressions – Karnaugh Maps – Sum of products (SOP) and Product of sums (POS).

UNIT-II (22 HOURS)

Combinational and Sequential circuits: Multiplexer and De-Multiplexer – Decoder, Half adder, Full adder and Parallel adder circuits. Flip flops – RS, D, JK

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and JK Master-Slave (working and truth tables) –Registers, Shift registers, Serial in-Serial out, Serial in – parallel out, parallel in- Serial out and Parallel in Parallel out registers, - Synchronous and asynchronous binary counters, Up/Down counters- Decade counter (7490) - working, truth tables and timing diagrams. Semiconductor memories – RAM, ROM, PROM, EPROM, EEPROM.

UNIT-III (23 HOURS)

Introduction to Microcomputer and Microprocessor: Intel 8085 Microprocessor – central processing unit CPU – arithmetic and logic unit ALU – timing and control unit – register organization – address, data and control buses- pin configuration of 8085 and its description. Timing diagrams- Instruction cycle, machine cycle, fetch and execute cycles.

Instruction set of 8085, instruction and data formats - classification of instructions –addressing modes. Assembly language programming examples of 8 and 16-bit addition, subtraction, multiplication and division. Finding the largest and smallest in a data array. Programming examples using stacks and subroutines.

UNIT-IV (22 HOURS)

Interfacing peripherals and applications: Organization of memory and its working -Memory interfacing concepts- Keyboard interfacing[8279] - Programmable peripheral interface (8255) - D/A and A/D converters and their interfacing to the Microprocessor. Stepper motor control- seven segment LED.

(NOTE: Solve related problems in all the Units)

Reference Books:

1. Digital Principles and Applications- Malvino & Leach- TMH
2. Digital Fundamentals – F.Loyd & Jain- Pearson Education
3. Modern Digital Electronics- R.P Jain-TMH
4. Fundamentals of Digital Circuits- Anand Kumar- PHI
5. Digital Systems – Rajkamal- Pearson Education
6. Digital Electronic Principles and Integrated Circuits- Maini- Willey India
7. Digital Electronics- Gothman-
8. Digital Electronics –J.W. Bignel & Robert Donova- Thomson Publishers (Indian 5th Ed)
9. Microprocessor Architecture and Programming – Ramesh S. Goanker- Penram
10. Introduction to Microprocessor – Aditya. P. Mathur- TMH



11. Microprocessors and Microcontrollers Hardware and Interfacing- Mathivannan- PHI
12. Fundamentals of Microprocessors and Microcontrollers – B. Ram-Dhanpat Rai & Sons.
13. Advanced Microprocessor and Peripherals, Architecture, Programming and Interface- A.K.Ray and K.N. Bhurchandi- TMH
14. Microprocessor Lab Premier- K.A. Krishna Murthy



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PRACTICAL PAPER-III (90 hours – 30 sessions)

Digital Electronics and Microprocessor Lab

A) Digital Experiments

1. Verification of truth tables of OR, AND, NOT, NAND, NOR, EX-OR gates (By using 7400-series)
2. Construction of gates using NAND, NOR gates.
3. Construction of Half and Full adders and verifying their truth tables.
4. Operation and verifying truth tables of flip- flops- RS, D, and JK using ICs.
5. Construction of Binary counters (74193).
6. Driving Stepper motor using JK flip-flop
7. Simulation experiments using appropriate electronic circuit simulation.
 - a) 4-bit parallel adder using combinational circuits.
 - b) Decade counter using JK flip flops.
 - c) Up/Down counter using JK flip flop.
 - d) Up/Down counter using 74193.
 - e) Multiplexer/Demultiplexer
 - f) Encoder

B) MICROPROCESSOR (Software)

1. Binary addition & subtraction. (8-bit & 16-bit)
2. Multiplication & division.
3. Picking up largest/smallest number.
4. Arranging –ascending/descending order.
5. Decimal addition (DAA) & Subtraction.
6. Time delay generation

C) MICROPROCESSOR (Hardware)

1. Interfacing R-2R Ladder network (DAC) (4 bits) to generate waveforms.
2. Interfacing a stepper motor and rotating it clockwise/anti clockwise through a known angle.
3. Interfacing a seven segment display.
4. Interfacing ADC for temperature measurement.

STUDENTS should be ENCOURAGED TO DO A SMALL PROJECT DURING THIRD YEAR



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Elective Paper–IV(A): Embedded Systems and Applications (90 hours)

Unit– I (22 Hours)

The 8051 Microcontroller

Introduction to microcontrollers and embedded systems: Block diagram approach of Embedded systems – Classification of embedded systems.

Overview and block diagram of 8051. Architecture of 8051. Program counter and memory organization. Data types and directives, Flag bits and PSW Register, Register banks and Stack; Pin diagram, Port organization, I/O Programming, Bit manipulation. Interrupts and Timer/Counter Modules.

Unit–II (23 Hours)

Addressing modes, instruction set and assembly language programming of 8051:

Addressing modes and accessing memory using various addressing modes. Instruction set: Arithmetic, Logical, Single Bit, Jump, Loop and Call Instructions and their usage. Time Delay Generation and Calculation;

Programming examples: Addition, multiplication, subtraction, division, arranging a given set of numbers in ascending / descending order, picking the smallest /

largest number among a given set of numbers, Accessing a specified port terminal and generating a rectangular waveform.

Unit – III (22 Hours)

Interfacing of peripherals to Microcontroller

Interfacing of parallel ports, Interrupt priority controller, DAC, ADC. Serial communication - modes and protocols .

Unit – IV (23 Hours)

Applications of Embedded Systems

Temperature measurement, displaying information on a LCD, Control of a Stepper Motor, Interfacing a keyboard and generation of different types of waveforms.

Reference Books:

1. The 8051 Microcontrollers and Embedded Systems – By Muhammad Ali Mazidi and Janice Gillispie Mazidi- Pearson Education Asia, 4th Reprint, 2002
2. Microcontrollers – Theory and applications by Ajay V. Deshmukh-Tata McGraw-Hill
3. The 8051 Microcontroller - architecture, programming & applications By Kenneth J. Ayala- Penram International Publishing, 1995
4. Programming and Customizing the 8051 Microcontroller – By Myke Predko-TMH, 2003
5. Design with Microcontrollers By - J B Peatman- TMH.
6. The 8051 Microcontroller - Programming, interfacing and applications by Howard Boyet and Ron Katz - (MII) Microprocessors Training Inc.
7. The concepts & features of Microcontrollers by Rajkamal - Wheeler Pub.

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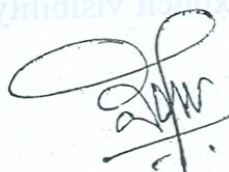
Elective Paper – IV (A): PRACTICALS (90 Hours- 30 Sessions)

Embedded Systems and Applications Lab

Microcontroller Experiments using 8051 kit

1. Multiplication of two numbers using MUL command (later using counter method for repeated addition)
2. Division of two numbers using DIV command (later using counter method for repeated subtraction)
3. Pick the smallest number among a given set of numbers
4. Pick the largest number among a given set of numbers
5. Arrange 'n' numbers in ascending order
6. Arrange 'n' numbers in descending order
7. Generate a specified time delay using timer/ counter
8. Interface a ADC and a temperature sensor to measure temperature
9. Interface a DAC & Generate a stair case wave form – with step duration and no. of steps as variables
10. Flash a LED connected at a specified out put port terminal
11. Interface a stepper motor – and rotate it clock wise or anti clock wise through given angle steps
12. Using Keil software write a program to pick the smallest among a given set of numbers
13. Using Keil software write a program to pick the largest among a given set of numbers
14. Using Keil software write a program to arrange a given set of numbers in ascending order
15. Using Keil software write a program to arrange a given set of numbers in descending order
16. Using Keil software write a program to generate a rectangular wave form at a specified port terminal

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Elective Paper – IV (B) : Digital Design Using VHDL (90 Hours)

UNIT – I (22 Hours)

Introduction & Behavioural Modeling

Introduction to HDLs: Difference between HDL and other software languages – Different HDLs in vogue. Overview of digital system design using HDL

Basic VHDL Language Elements: Identifiers, Data objects, scalar and composite data types, Operators

Behavioural Modeling with examples: Entity declaration, Architecture body, Process statement and sequential statements. Inertial and transport delay models, creating signal waveforms, signal drivers, effect of transport and inertial delays on signal drivers.

UNIT – II (23 Hours)

Data Flow and Structural Modeling

Data Flow Modeling with examples: Concurrent signal assignment statement, Concurrent versus sequential signal assignment, Delta delays, Multiple drivers, Conditional signal assignment statement, selected signal assignment statement, concurrent assertion statement.

Structural Modeling with examples: Component declaration, Component instantiation and examples, Direct instantiation of component.

UNIT – III (23 Hours)

Subprograms and Packages

Subprograms and Overloading: Functions and procedures with simple examples - subprogram overloading, Operator overloading.

Packages and Libraries: Package declaration, package body, design file, design libraries, order of analysis, implicit visibility, explicit visibility, library clause and use clause.



Advanced Features: Entity statements, Generate statements, Attributes, Aggregate targets, ports and their behaviour.

UNIT – IV (22 Hours)

Simulation and Hardware modeling

Model Simulation: Simulation – Writing a Test Bench for a Half and a Full adder.

Hardware Modeling Examples: Modeling entity interfaces, Modeling simple elements, Different styles of modeling, Modeling regular structures, Modeling delays, Modeling conditional operations, Modeling a clock divider and a pulse counter.

Reference Books


1. A VHDL Primer - By J.Bhasker ., 3rd edition - PHI, New Delhi, 2007
2. Circuit design with VHDL by Volnei . Pedroni – PHI, New Delhi, 2007
3. Digital Systems Design using VHDL by Charles H.Roth Jr.- PWS Pub.,1998
4. Introductory VHDL : From Simulation to Synthesis – by Sudhakar Yalamanchili.- Pearson Education Asia., 2001
5. VHDL Programming by Example – By Douglas L.Perry.- 4th Ed - TMH., 2002
6. Fundamentals of Digital Logic with VHDL Design – by Stephen Brown & Zvonko Vranesic - TMH. 2002
7. VHDL – Analysis & Modeling of Digital Systems – By Zainalabedin Navabi- 2nd Ed - TMH, 1998
8. The Designer's Guide to VHDL - By Peter J. Ashenden -2nd Ed., 1st Indian Reprint- Harcourt India Pvt. Ltd., 2001.

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Elective Paper – IV (B): PRACTICALS (90 Hours- 30 Sessions)
Digital design Using VHDL Lab

VHDL –Program entry, simulation & implementation (CPLD/ FPGA) using appropriate HDL Software for the following circuits.



1. All types of logic gates (Data Flow)
2. Half adder (Data flow, Structural and Schematic)
3. Full adder (Data flow, Structural and Schematic)
4. Half subtractor (Data flow, Structural and Schematic)
5. Full subtractor (Data flow, Structural and Schematic)
6. Two control input Mux – using case
7. Two control input Mux – using conditional signal assignment
8. Two control input Mux – using selected signal assignment
9. Two control input Demux - using case
10. BCD to seven segment decoder (schematic)
11. Modeling a RS-FF with assertion, report & different levels of severity (Behavioural)
12. Modeling a BCD Counter (Top level behavioural)
13. Writing a Test Bench for a Half adder
14. Writing a Test bench for Full Adder

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