GATE CS - 2006

Q.1 - Q.20 Carry One Mark Each

1. Consider the polynomial $p(x) = a0 + a1x + a2x2$ number of multiplications needed to evaluate pon	
(A) 3	
(B) 4	
(C) 6	
(D) 9	(7)
2. Let X,Y,Z be sets of sizes x, y and z respectively	
of all subsets of W. The number of functions from	Z to E is:
(A) <i>Z2XY</i>	
(B) Zx2	
(C) <i>Z2XY</i>	
(D) 2XYZ	60

- 3. The set {1,2,3,5,7,8,91 under multiplication modulo 10 is not a group. Given below are four plausible reasons. Which one of them is false?
- (A) It is not closed
- (B) 2 does not have an inverse
- (C) 3 does not have an inverse
- (D) 8 does not have an inverse
- 4. A relation R is defined on ordered pairs of integers as follows:

(x,y)R(u,v)if x < u and y > v. Then R is:

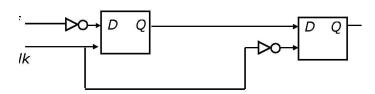
- (A) Neither a Partial Order nor an Equivalence Relation
- (B) A Partial Order but not a Total Order
- (C) A Total Order
- (D) An Equivalence Relation
- 5. For which one of the following reasons does Internet Protocol (IP) use the time-to-live (TTL) field in the IP datagram header?
- (A) Ensure packets reach destination within that time
- (B) Discard packets that reach later than that time
- (C) Prevent packets from looping indefinitely
- (D) Limit the time for which a packet gets queued in intermediate routers.
- 6. Consider three CPU-intensive processes, which require 10, 20 and 30 time units and arrive at times 0, 2 and 6, respectively. How many context switches are needed if the operating system implements a shortest remaining time first scheduling algorithm? Do not count the context switches at time zero and at the end.
- (A) 1
- (B) 2
- (C) 3
- (D) 4
- 7. Consider the following grammar.

Consider the following LR(0) items corresponding to the grammar above.

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Given the items above, which two of them will appear in the same set in the canonical sets-ofitems for the grammar?

- (A) (i) and (ii)
- (B) (ii) and (iii)
- (C) (i) and (iii)
- (D) None of the above
- 8. You are given a free running clock with a duty cycle of 50% and a digital waveform f which changes only at the negative edge of the clock. Which one of the following circuits (using clocked D flip-flops) will delay the phase of f by 1800?



- 9. A CPU has 24-bit instructions. A program starts at address 300 (in decimal). Which one of the following is a legal program counter (all values in decimal)?
- (A) 400
- (B) 500
- (C) 600
- (D) 700
- 10. In a binary max heap containing n numbers, the smallest element can be found in time
- (A) *0(n)*
- (B) O(logn)
- (C) 0(loglogn)
- (D) 0(1)
- 11. Consider a weighted complete graph Gon the vertex set $\{v1,v2,v\}$ such that the weight of the edge (v_i,v) is 2i-j. The weight of a minimum spanning tree of G is:
- (A) n-i
- (B) 2n-2
- (C)
- (D) 2
- 12. To implement Dijkstra's shortest path algorithm on unweighted graphs so that it runs in linear time, the data structure to be used is:
- (A) Queue
- (B) Stack
- (C) Heap
- (D) B-Tree
- 13. A scheme for storing binary trees in an array X is as follows. Indexing of X starts at 1 instead of 0. the root is stored at X[1]. For a node stored at X[i], the left child, if any, is stored

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in X[2i] and the right child, if any, in X[2i+1]. To be able to store any binary tree on nvertices the minimum size of X should be (A) $\log 2 n$ (B) n (C) $2n+1$ (D) $2-1$
14. Which one of the following in place sorting algorithms needs the minimum number of
swaps? (A) Quick sort (B) Insertion sort (C) Selection sort (D) Heap sort
15. Consider the following C-program fragment in which i,j and nare integer variables. for $(i = n, j = 0; i > 0; i = 2, j + = i);$
Let val(j)denote the value stored in the variable jafter termination of the <i>for</i> loop. Which one of the following is true? (A) $val(j) = 8(logn)$ (B) $vaI(j) = 8(fi)$ (C) $val(j) = 8(nlogn)$ (D) $val(j) = 8(nlogn)$
16. Let S be an NP-complete problem and Q and R be two other problems not known to be in NP. Q is polynomial time reducible to S and S is polynomial-time reducible to R. Which one of the following statements is true? (A) R is NP-complete (B) R is NP-hard (C) Q is NP-complete (D) Q is NP-hard
17. An element in an array X is called a leader if it is greater than all elements to the right of it in X. The best algorithm to find all leaders in an array (A) Solves it in linear time using a left to right pass of the array (B) Solves it in linear time using a right to left pass of the array (C) Solves it using divide and conquer in time $8(nlogn)$ (D) Solves it in time $8(n2)$
18. We are given a set X $\{x1\ x\}$ where x , = 2'. A sample S c Xis drawn by selecting each x ,independently with probability p, =The expected value of the smallest number in sample S is: (A) (B) 2 (C) (D) n
 19. Let L1 {OnminOmnm>O1L {Onmin+mOmnm>O} and i = {jt n rnjn mflm 01. Which of these languages are NOT context free? (A) L1 only (B) L3 only

- (C) L1 and L2
- (D) L2 and L3
- 20. Consider the following log sequence of two transactions on a bank account, with initial balance 12000, that transfer 2000 to a mortgage payment and then apply a 5% interest.
- 1. T1 start
- 2. T1 B old= 1200 new= 10000
- 3. T1 M old=0 new=2000
- 4. T1 commit
- 5. T2start
- 6. T2 B old= 10000 new= 10500
- 7. T2 commit

Suppose the database system crashes just before log record 7 is written. When the system is restarted, which one statement is true of the recovery procedure?

- (A) We must redo log record 6 to set B to 10500
- (B) We must undo log record 6 to set B to 10000 and then redo log records 2 and 3
- (C) We need not redo log records 2 and 3 because transaction Ti has committed
- (D) We can apply redo and undo operations in arbitrary order because they are idempotent.

Q.21 — Q.75 Carry Two Marks Each

- 21. For each element in a set of size 2n, an unbiased coin is tossed. The 2ncoin tosses are independent. An element is chosen if the corresponding coin toss were head. The probability that exactly n elements are chosen is:
- (A) 2n
- 22. Let E, F and G be finite sets.

Let X=(EfIF)-(FfIG) and $Y=(E_{(EfIG)})_{(E_{F})}$.

Which one of the following is true?

- (A) XcY
- (B) XDY
- (C) X=Y
- (D) $X-Y\neq\emptyset$ and $Y-X\neq\emptyset$
- 23. F is an nxn real matrix. b is an nxi real vector. Suppose there are two nxivectors, u and vsuch that u v, and Fu = b, Fv = b. Which one of the following statements is false?
- (A) Determinant of F is zero
- (B) There are an infinite number of solutions to Fx = b
- (C) There is an x Osuch that Fx = 0
- (D) F must have two identical rows
- 24. integers S, and ,r(S) is the set of integers obtained by applying permutation rto each element of S?
- (A) (n-AUBAHB
- (B) (A2 + B2)n2
- (C) n!
- (D) A fl B2/[A U B
- 25. Let $S = \{1,2,3,m\}, m>3$. Let X1 X be subsets of S each of size 3. Define a function ffrom Sto the set of natural numbers as, f(i) the number of sets Xthat Visit http://groups.yahoo.com/group/OneStopGATE/ for joining the club of GATE Aspirants

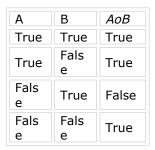
contain the element *I*. That is $f(I) = \{jIE \times 7\}$.

Then f(i)is:

(A) 3m (B) <i>3n</i>
(C) 2m+1
(D) 2n+1
26. Which one of the first order predicate calculus statements given below correctly expresses the following English statement?
Tigers and lions attack if they are hungry or threatened.
(A) $vx[(tiger(x) \mathbf{A} lion(x)) - \{(hungry(x) v threatened(x)) - attacks(x))1\}$
(B) Vx [(tiger (x) V lion (x)) — {(hungry (x) V threatened (x)) A attacks (x)j
(C) $Vx[(tiger(x) \ v \ lion(x)) - \{attacks(x) - (hungry(x) \ v \ threatened(x)))1$
(D) $vx[(tiger(x) \ v \ lion(x)) - \{(hungry(x) \ v \ threatened(x)) - attacks(x))1$
27. Consider the following propositional statements:
PI:((AAB)C))((AC)A(BC))
P2:((AvB)C))((A-C)v(B_C)) Which are of the following in true?
Which one of the following is true? (A) P1 is a tautology, but not P2
(B) P2 is a tautology, but not P1
(C) P1 and P2 are both tautologies
(D) Both P1 and P2 are not tautologies
28. A logical binary relation a, is defined as follows:
Let be the unary negation (NOT) operator, with higher precedence then o. Which one of the
following is equivalent to A A B?
(A) ('-'.'AOB)
(B) -'.'(AO''B)
(C) ".'(".'Ao".'B) (D) AOB)
(D) AOD)
29. If s is a string over $(0 + 1)^*$ then let $n0$ (s) denote the number of 0's in
s and $n1$ (s)the number of I's in s . Which one of the following languages is not
regular?
(A) $L = \{s (0 + 1)*n0 (s) \text{ is a 3-digit prime } \}$
(B) $L = \{s \in (0+1)^* \text{ for every prefix } s' \text{ of } s, \text{ fl0 } (s') - n1 \ (s') \ 2\}$
(C) $L = \{sE(0+1)*nO(s)_nI(s)4\}$
(D) $L = \{s \in (0 + 1) \text{ j } n0 \text{ (s) } \text{mod } 7 = n1 \text{ (s) } \text{mod } 5 = 0\}$
30. For SE $(0+1)$ *let d(s)denote the decimal value of $s(e.g.d(101)=5)$. Let $L = \{s \in (0+1) \mid d(s) \mod 5 = 2 \text{ and } d(s) \mod 7 = 4\}$
Which one of the following statements is true?
(A) L is recursively enumerable, but not recursive
(B) L is recursive, but not context-free
(C) L is context-free, but not regular
(D) L is regular
31. Let SHAM3 be the problem of finding a Hamiltonian cycle in a graph $G = (V,E)$ with V
divisible by 3 and DHAM3 be the problem of determining if a Hamiltonian cycle exists in such



graphs. Which one of the following is true?

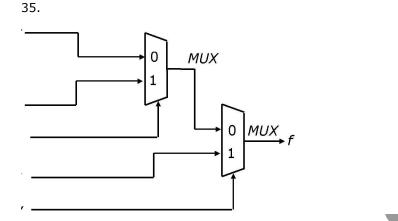


- (A) Both DHAM3 and SHAM3 are NP-hard
- (B) SHAM3 is NP-hard, but DHAM3 is not
- (C) DHAM3 is NP-hard, but SHAM3 is not
- (D) Neither DHAM3 nor SHAM3 is NP-hard
- 32. Consider the following statements about the context free grammar $G = \{S SS, S ab, S ba, S \}$
- I. G is ambiguous
- II. G produces all strings with equal number of a's and b's
- III. G can be accepted by a deterministic PDA.

Which combination below expresses all the true statements about G?

- (A) I only
- (B) I and III only
- (C) II and III only
- (D) I, II and III
- 33. Let *L1* be a regular language, *L2* be a deterministic context-free language and *L3* a recursively enumerable, but not recursive, language. Which one of the following statements is false?
- (A) L1 fl L is a deterministic CFL
- (B) L3 fl L is recursive
- (C) L1 U L2 is context free
- (D) L1 fl L fl L3 is recursively enumerable
- 34. Consider the regular language $L = (111+11111)^*$. The minimum number of states in any DFA accepting this languages is:
- (A) 3
- (B) 5
- (C) 8
- (D) 9





Consider the circuit above. Which one of the following options correctly represents f(x,y,z)?

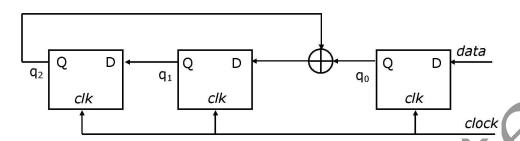
- (A) xz+xy+yz
- (B) xz+xy+yz
- (C) xz+xy+yz
- (D) xz+xy+yz
- 36. Given two three bit numbers a2a1a0 and b2b1b0 and c, the carry in, the function that represents the *carry* generate function when these two numbers are added is:
- $(A) \ a2b2 + \ a2a1b1 + \ a2a1a0b0 + \ a2a0b1b0 + \ a1b2b1 + \ a1a0b2b0 + \ a0b2b1b0$
- (B) a2b2 + a2b1b0 + a2a1b1b0 + a1a0b2b1 + a1a0b2 + a1a0b2b0 + a2a0b1b0
- (C) a2 +b2 + (a2 \$b2)(a1 +b1 + (a1 \$b1) (a0 +b0))
- (D) a2b2 + a2a1b1 + a2 a1a0b0 + a2 a0 b1b0 + a1 b2 b1 + a1a0 b2b0 + a0 b2b1b0
- 37. Consider the circuit in the diagram. The \$ operator represents Ex-OR. The D flip- flops are initialized to zeroes (cleared).

The following data: 100110000 is supplied to the "data" terminal in nine clock cycles. After that the values of q2q1q0 are:

- (A) 000
- (B) 001
- (C) 010
- (D) 101
- 38. Consider a Boolean function f(w,x,y,z), suppose that exactly one of its inputs is allowed to change at a time. If the function happens to be true for two input vectors i = (w1,x1,y1,z1) and '2 (w2,x2,y2,z2), we would like the function to remain true as the input changes from

i to '2 (' and '2 differ in exactly one bit position), without becoming false





momentarily. Let f(w,x,y,z) = (5,7,11,12,13,15). Which of the following cube covers of f will ensure that the required property is satisfied?

- (A) wxz, wxy, xyz, xyz, wyz
- (B) wxy, wxz, wyz
- (C) wxyz,xz,wxyz
- (D) wzy, wyz, wxz, wxz, xyz, xyz
- 39. We consider the addition of two 2's complement numbers b 1b 2 ,b0 and a 1a 2....a0. A binary adder for adding unsigned binary numbers is used to add the two numbers. The sum is denoted by c 1c 2....c0 and the carry-out by cQLJ. Which one of the following options correctly identifies the overflow condition?
- (A) *c0(a1sb1)*
- (B) a 1b 1c+a 1b 1C
- (C) *cQLJ* 1
- (D) a1\$b1\$c1
- 40. Consider numbers represented in 4-bit gray code. Let h3h2h1h0 be the gray code representation of a number nand let g3g2g1g0be the gray code of (n+1) (modulo 16) value of the number. Which one of the following functions is correct?
- (A) g0(h3h2h1h0)=(1,2,3,6,10,13,14,15)
- (B) g1(h3h2h1h0) = (4,9,10,11,12,13,14,15)
- (C) g2(h3h2h1h0)=(2,4,5,6,7,12,13,15)
- (D) g3(h3h2h1h0)=(0,1,6,7,10,11,12,13)
- 41. A CPU has a cache with block size 64 bytes. The main memory has kbanks, each bank being cbytes wide. Consecutive c—byte chunks are mapped on consecutive banks with wraparound. All the kbanks can be accessed in parallel, but two accesses to the same bank must be serialized. A cache block access may involve multiple iterations of parallel bank accesses depending on the amount of data obtained by accessing all the kbanks in parallel. Each iteration requires decoding the bank numbers to be accessed in parallel and this takes --ns. The latency of one bank access is 80 ns. If c = 2 and k = 24, the latency of retrieving a cache block starting at address zero from main memory is:
- (A) 92 ns
- (B) 104 ns
- (C) 172 ns
- (D) 184 ns

- 42. A CPU has a five-stage pipeline and runs at 1 GHz frequency. Instruction fetch happens in the first stage of the pipeline. A conditional branch instruction computes the target address and evaluates the condition in the third stage of the pipeline. The processor stops fetching new instructions following a conditional branch until the branch outcome is known. A program executes io instructions out of which 20% are conditional branches. If each instruction takes one cycle to complete on average, the total execution time of the program is:
- (A) 1.0 second
- (B) 1.2 seconds
- (C) 1.4 seconds
- (D) 1.6 seconds
- 43. Consider a new instruction named branch-on-bit-set (mnemonic bbs). The instruction "bbs reg, pos, label" jumps to label if bit in position pos of register operand reg is one. A register is 32 bits wide and the bits are numbered 0 to 31, bit in position 0 being the least significant. Consider the following emulation of this instruction on a processor that does not have bbs implemented.

temp - reg & mask

Branch to *label* if temp is non-zero.

The variable *temp* is a temporary register. For correct emulation, the variable mask must be generated by

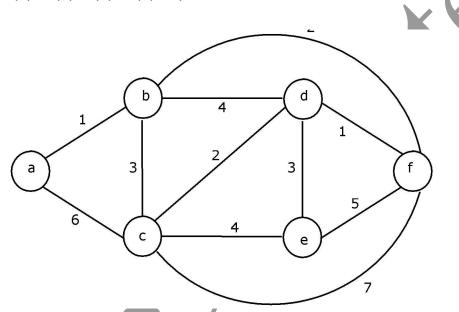
- (A) mask -0x1 < cpos
- (B) mask f— 0 x rrrrrrr >> pos
- (C) mask—pos
- (D) mask 0xf
- 44. Station A uses 32 byte packets to transmit messages to Station B using a sliding window protocol. The round trip delay between A and B is 80 milliseconds and the bottleneck bandwidth on the path between A and B is 128 kbps. What is the optimal window size that A should use?
- (A) 20
- (B) 40
- (C) 160
- (D) 320
- 45. Two computers Cl and C2 are configured as follows. Cl has IP address 203.197.2.53 and netmask 255.255.128.0. C2 has IP address 203.197.75.201 and netmask 255.255.192.0. which one of the following statements is true?
- (A) CI and C2 both assume they are on the same network
- (B) C2 assumes Cl is on same network, but Cl assumes C2 is on a different network
- (C) Classumes C2 is on same network, but C2 assumes Cl is on a different network
- (D) Cl and C2 both assume they are on different networks.
- 46. Station A needs to send a message consisting of 9 packets to Station B using a sliding window (window size 3) and go-back-n error control strategy. All packets are ready and immediately available for transmission. If every 5th packet that A transmits gets lost (but no acks from B ever get lost), then what is the number of packets that A will transmit for sending the message to B?
- (A) 12
- (B) 14

- (C) 16
- (D) 18

47. Consider the following graph:

Which one of the following cannot be the sequence of edges added, in that order, to a minimum spanning tree using Kruskal's algorithm?

- (A) (a-b), (d-f), (b-f), (d-c), (d-e)
- (B) (a-b),(d-f),(d-c),(b-f),(d-e)
- (C) (d-f),(a-b),(d-c),(b-f),(d-e)
- (D) (d-f),(a-b),(b-f),(d-e),(d-c)



- 48. Let T be a depth first search tree in an undirected graph G. Vertices u and v are leaves of this tree T. The degrees of both u and v in G are at least 2. which one of the following statements is true?
- (A) There must exist a vertex w adjacent to both u and v in G
- (B) There must exist a vertex w whose removal disconnects u and v in G
- (C) There must exist a cycle in G containing u and v
- (D) There must exist a cycle in G containing uand all its neighbours in G.

49. An implementation of a queue Q, using two stacks Si and S2, is given below:

void insert (Q, x) push (Si, x);

void delete (Q)

if (stack-empty(52)) then

if (stack-empty(Si)) then

print("Q is empty");

return;

else while (! (stack-empty (Si)))

x=pop (Si);

push(52,x)

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x = pop(S2);
Let flinsert and m(n) delete operations be performed in an arbitrary order on an empty queue
Q. Let x and y be the number of push and pop operations performed respectively in the
process. Which one of the following is true for all m and n?
(A) n+m x<2n and 2m< y< n+m
(B) n+m x<2n and 2m<y<2n
(C) 2m \times 2n and 2m < y < n + m
(D) 2m \times 2n and 2m < y < 2n
50. A set X can be represented by an array x[n] as follows:
Ii ifx[i1]=0
otherwise
Consider the following algorithm in which x,y and z are Boolean arrays of size n:
algorithm zzz(x[ 1, y[ 1, z [
mt i;
for (i=0; i<n; ++i)
z[i] = (x[i] Ay[i]) V (x[i] A y[i])
The set Z computed by the algorithm is:
(A) (XUY)
(B) (xflY)
(C) (X-Y)fI(Y-X)
(D) (x-Y)U(Y-x)
51. Consider the following recurrence:
T(n)=2T(r*i1)+1,T(1)=1
Which one of the following is true?
(A) T(n) = e(loglogn)
(B) T(n) = e(log n)
(C) T(n) = 8(sJ)
(D) T(n)rz8(n)
52. The median of n elements can be found in O(n)time. Which one of the following is correct
about the complexity of quick sort, in which median is selected as pivot?
(A) 8(n)
(B) e(nlogn)
(C) 8(n2)
(D) 8(n3)
53. Consider the following C-function in which a[nl and b[mlare two sorted integer arrays and
c(n + mibe) another integer array.
void xyz(int a[], mt b [1, mt c []){
mt i,j,k;
i=j=k=0;
while ((i<n) && (j<m))
if (a[i] < b[j]) c[k++] = a[i++];
else c[k++] =
Which of the following condition(s) hold(s) after the termination of the while loop?
(i) j < m, k = n + j - 1, and a \lceil n - i1 < b \lceil j \rceil if i = n
(ii) i < n, k = m + i - 1, and b \lceil m - i1a \rceil i1 if j = m
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(A) only (i)

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(B) only (ii)
(C) either (i) or (ii) but not both
(D) neither (i) nor (ii)
54. Given two arrays of numbers a1,...,a and b1,...,b where each number is 0 or 1, the fastest
algorithm to find the largest span (i,j)such that
a_1 + a_2 + a_3 + a_4 + a_5 
(A) Takes Q(3n) and c(2)time if hashing is permitted
(B) Takes O(n3) and c(n25)time in the key comparison model
(C) Takes e(n)time and space
(D) Takes o(J) time only if the sum of the 2n elements is an even number
55. Consider these two functions and two statements Si and S2 about them.
mt workl(\pmnt *a, mt \pm, mt j) mt work2(\pmnt *a, mt \pm, mt j)
mt x = a[i+2]; mt ti =
a[j] = x+1; mt t2 = a[t];
return a[i+2] - 3; a[j] = t2+1;
return t2 - 3;
Si: The transformation form worki to work2 is valid, i.e., for any program state and input
arguments, work2 will compute the same output and have the same effect on program state
as worki
S2: All the transformations applied to work to get work will always improve the
performance (i.e reduce CPU time) of work2 compared to worki
(A) Si is false and S2 is false
(B) Si is false and S2 is true
(C) Si is true and S2 is false
(D) Si is true and S2 is true
56. Consider the following code written in a pass-by-reference language like
FORTRAN and these statements about the code.
subroutine swap(ix,iy)
it = ix
Li: ix=iy
L2: iy=it
end
ia = 3
ib = 8
call swap (ia, ib+5)
print , ia, ib
end '
Si: The compiler will generate code to allocate a temporary nameless cell, initialize it to i3,
and pass the address of the cell swap
S2: On execution the code will generate a runtime error on line Li
S3: On execution the code will generate a runtime error on line L2
S4: The program will print i3 and 8
S5: The program will print i3 and -2
Exactly the following set of statement(s) is correct:
(A) Si and S2
(B) Si and S4
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(C) S3
(D) Si and S5
57. Consider this C code to swap two integers and these five statements: the code
void swap (mt *px, mt *py) {
*px = *px - *py;
*py = *px + *py;
*px = *py - *px;
Si: will generate a compilation error
S2: may generate a segmentation fault at runtime depending on the arguments passed
S3: correctly implements the swap procedure for all input pointers referring to integers stored
in memory locations accessible to the process
S4: implements the swap procedure correctly for some but not all valid input pointers
S5: may add or subtract integers and pointers.
(A) Si
(B) S2 and S3
(C) S2 and S4
(D) S2 and S5
58. Consider the following grammar:
S - FR
R - *5k.
F - Id
In the predictive parser table, M, of the grammar the entries M[S,id] and M[R,\$1] respectively.
(A) \{S-FR\} and \{R-s\}
(B) \{S-FR\} and \{\}
(C) \{S-FR\} and \{R *S\}
(D) \{F - id\} and \{R - s\}
59. Consider the following translation scheme.
S - ER
R *E{print('*');}Rs
E − F+E{print(+);}F
F - (S)id{print(id.value);}
Here Id is a token that represents an integer and id. value represents the corresponding
integer value. For an input 2*3+4,this translation scheme prints
(A) 2*3+4
(B) 2*+34
(C) 2 3*4
(D) 2 3 4*
60. Consider the following C code segment.
for (\pm - 0, i < n; i++)
for (j=0; j<n; j++)
if(\pm\%2)
x += (4*j + 5*\pm);
y += (7 + 4*j);
Which one of the following is false?
(A) The code contains loop invariant computation
```



- (B) There is scope of common sub-expression elimination in this code
- (C) There is scope of strength reduction in this code
- (D) There is scope of dead code elimination in this code
- 61. The atomic fetch-and-set x, y instruction unconditionally sets the memory location x to 1 and fetches the old value of x n y without allowing any intervening access to the memory location x. consider the following implementation of P and V functions on a binary semaphore S.

```
void P (binary_semaphore *s)
unsigned y;
unsigned * = &(s—>value);
do
fetch—and—set x, y;
while (y)
void V (binary_semaphore *s)
S—>value = 0;
Which one of the following is true?
```

- (A) The implementation may not work if context switching is disabled in
- (B) Instead of using fetch-and —set, a pair of normal load/store can be used
- (C) The implementation of V is wrong
- (D) The code does not implement a binary semaphore
- 62. A CPU generates 32-bit virtual addresses. The page size is 4 KB. The processor has a translation look-aside buffer (TLB) which can hold a total of 128 page table entries and is 4-way set associative. The minimum size of the TLB tag is:
- (A) 11 bits
- (B) 13 bits
- (C) 15 bits
- (D) 20 bits
- 63. A computer system supports 32-bit virtual addresses as well as 32-bit physical addresses. Since the virtual address space is of the same size as the physical address space, the operating system designers decide to get rid of the virtual memory entirely. Which one of the following is true?
- (A) Efficient implementation of multi-user support is no longer possible
- (B) The processor cache organization can be made more efficient now
- (C) Hardware support for memory management is no longer needed
- (D) CPU scheduling can be made more efficient now
- 64. Consider three processes (process id 0, 1, 2 respectively) with compute time bursts 2, 4 and 8 time units. All processes arrive at time zero. Consider the longest remaining time first (LRTF) scheduling algorithm. In LRTF ties are broken by giving priority to the process with the lowest process id. The average turn around time is:
- (A) 13 units
- (B) 14 units
- (C) 15 units
- (D) 16 units
- 65. Consider three processes, all arriving at time zero, with total execution time of 10, 20 and 30 units, respectively. Each process spends the first 20% of execution time doing Visit http://groups.yahoo.com/group/OneStopGATE/ for joining the club of GATE Aspirants

- I/O, the next 70°h of time doing computation, and the last 10°h of time doing I/O again. The operating system uses a shortest remaining compute time first scheduling algorithm and schedules a new process either when the running process gets blocked on I/O or when the running process finishes its compute burst. Assume that all I/O operations can be overlapped as much as possible. For what percentage of time does the CPU remain idle?
- (A) 0%
- (B) 10.6°h
- (C) 30.0°h
- (D) 89.4°h
- 66. Consider the following snapshot of a system running n processes. Process i is holding x,instances of a resource R, 1 n. currently, all instances of R are occupied. Further, for all I, process i has placed a request for an additional y, instances while holding the x, instances it already has. There are exactly two processes p and q such that = Yq = 0. Which one of the following can serve as a necessary condition to guarantee that the system is not approaching a deadlock?
- (A) min(xp,xq) < max Yk
- (B) X + Xq mink pq Yk
- (C) max(xp,xq)>1
- (D) min(xp,xq)>1
- 67. Consider the relation account (customer, balance) where customer is a primary key and there are no null values. We would like to rank customers according to decreasing balance. The customer with the largest balance gets rank 1, ties are

not broke but ranks are skipped: if exactly two customers have the largest balance they each get rank 1 and rank 2 is not assigned.

select A.customer, count(B.customer)

Queryl: from account A, account B

where A.balance <=B.balance

group by A.customer

select A.customer, 1+count(B.customer)

Query2: from account A, account B

where A.balance < B.balance

group by A.customer

Consider these statements about Queryl and Query2.

- Ouervl will produce the same row set as Ouerv2 for some but not all databases.
- 2. Both Queryl and Query2 are correct implementation of the specification
- 3. Queryl is a correct implementation of the specification but Query2 is not
- 4. Neither Queryl nor Query2 is a correct implementation of the specification
- 5. Assigning rank with a pure relational query takes less time than scanning in decreasing balance order assigning ranks using ODBC.

Which two of the above statements are correct?

- (A) 2 and 5
- (B) 1 and 3
- (C) 1 and 4
- (D) 3 and 5
- 68. Consider the relation enrolled (student, course) in which (student, course) is the primary key, and the relation paid (student, amount) where student is the primary key. Assume no null values and no foreign keys or integrity constraints. Given the following four queries:

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Queryl:select student from enrolled where student in (select student from paid) Query2:select student from paid where student in (select student from enrolled) Query3:select E.student from enrolled E, paid P where E.student = P.student Query4:select student from paid where exists

(select * from enrolled where enrolled.student = paid.student)

Which one of the following statements is correct?

- (A) All queries return identical row sets for any database
- (B) Query2 and Query4 return identical row sets for all databases but there exist databases for which Query1 and Query2 return different row sets.
- (C) There exist databases for which Query3 returns strictly fewer rows than Query2
- (D) There exist databases for which Query4 will encounter an integrity violation at runtime.
- 69. Consider the relation enrolled (student, course) in which (student, course) is the primary key, and the relation paid (student, amount) where student is the primary key. Assume no null values and no foreign keys or integrity constraints. Assume that amounts 6000, 7000, 8000, 9000 and 10000 were each paid by 20% of the students. Consider these query plans (Plan 1 on left, Plan 2 on right) to "list all courses taken by students who have paid more than x"

enrolled paid enrolled paid

1'1'

Probe index Sequential on student scan, select

amount > x

A disk seek takes 4ms, disk data transfer bandwidth is 300 MB/s and checking a tuple to see if amount is greater than x takes IOps. Which of the following statements is correct?

- (A) Plan 1 and Plan 2 will not output identical row sets for all databases
- (B) A course may be listed more than once in the output of Plan 1 for some data bases
- (C) For x = 5000, Plan 1 executes faster than Plan 2 for all databases
- (D) For x = 9000, Plan I executes slower than Plan 2 for all databases.

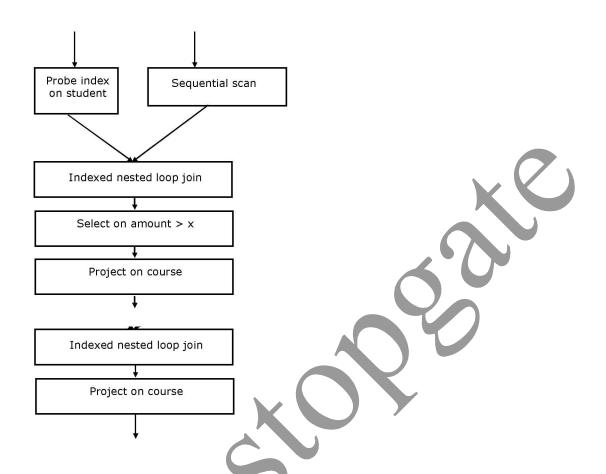
70. The following functional dependencies are given:

AB -* CD,AF -* D,DE -* F,C -* G,F -* E,G -* A.

Which one of the following options is false?

- (A) $\{CF\} = \{ACDEFG\}$
- (B) $\{BG\} = \{ABCDG\}$
- (C) $\{AF\} = \{ACDEFG\}$
- (D) $\{AB\} = \{ABCDFG\}$





Common Data Questions:

Common Data for Questions 71, 72, 73:

The 2 vertices of a graph G corresponds to all subsets of a set of size n, for n < 6. Two vertices of G are adjacent if and only if the corresponding sets intersect in exactly two elements.

71. The number of vertices of degree zero in G is:

- (A) 1
- (B) n
- (C) n+1
- (D)2

72. The maximum degree of a vertex in G is:

- (A) 2J
- (B) 2 2
- (C) 23x3
- (D) 2 1

73. The number of connected components in G is:

- (A) n
- (B) n+2
- (C) 2/2
- (D)

Common Data for Questions 74, 75:

Consider two cache organizations: The first one is 32 KB 2-way set associative with 32- byte block size. The second one is of the same size but direct mapped. The size of an address is 32 bits in both cases. A 2-to-i multiplexer has a latency of 0.6 ns while a *kbit* comparator has a latency of k/10 ns. The hit latency of the set associative organization is k/10 hit latency of the direct mapped one is k/10.

74. The value of h1is:

- (A) 2.4 ns
- (B) 2.3 ns
- (C) 1.8 ns
- (D) 1.7 ns

75. The value of h2 is:

- (A) 2.4 ns
- (B) 2.3 ns
- (C) 1.8 ns
- (D) 1.7 ns

Linked Answer Questions: Q.76 to Q85 Carry Two Marks Each

Statement for Linked Answer Questions 76 & 77:

A 3-ary max heap is like a binary max heap, but instead of 2 children, nodes have 3 children. A 3-ary heap can be represented by an array as follows: The root is stored in the first location, a[0], nodes in the next level, from left to right, is stored from a[1] to a[3]. The nodes from the second level of the tree from left to right are stored from a[4] location onward. An item x can be inserted into a 3-ary heap containing n items by placing x in the location a[n] and pushing it up the tree to satisfy the heap property.

76. Which one of the following is a valid sequence of elements in an array representing 3-ary max heap?

- (A) 1, 3, 5, 6, 8, 9
- (B) 9, 6, 3, 1, 8, 5
- (C) 9, 3, 6, 8, 5, 1
- (D) 9, 5, 6, 8, 3, 1

77. Suppose the elements 7, 2, 10 and 4 are inserted, in that order, into the valid 3- ary max heap found in the above question, Q.76. Which one of the following is the sequence of items in the array representing the resultant heap?

- (A) 10, 7, 9, 8, 3, 1, 5, 2, 6, 4
- (B) 10, 9, 8, 7, 6, 5, 4, 3, 2, 1
- (C) 10, 9, 4, 5, 7, 6, 8, 2, 1, 3



(D) 10, 8, 6, 9, 7, 2, 3, 4, 1, 5

Statement for Linked Answer Questions 78 & 79:

Barrier is a synchronization construct where a set of processes synchronizes globally i.e. each process in the set arrives at the barrier and waits for all others to arrive and then all processes leave the barrier. Let the number of processes in the set be three and S be a binary semaphore with the usual P and V functions. Consider the following C implementation of a barrier with line numbers shown on left.

```
void barrier (void)
1: P(S);
2: process_arrived++;
3. V(S);
4: while (process_arrived !=3);
5: P(S);
6: process_left++;
7: if (process_left==3)
8: process_arrived = 0;
9: process_left = 0;
10:
11: V(S);
```

The variables process_arrived and process_left are shared among all processes and are initialized to zero. In a concurrent program all the three processes call the barrier function when they need to synchronize globally.

78. The above implementation of barrier is incorrect. Which one of the following is true?

- (A) The barrier implementation is wrong due to the use of binary semaphore S
- (B) The barrier implementation may lead to a deadlock if two barrier in invocations are used in immediate succession.
- (C) Lines 6 to 10 need not be inside a critical section
- (D) The barrier implementation is correct if there are only two processes instead of three.

79. Which one of the following rectifies the problem in the implementation?

- (A) Lines 6 to 10 are simply replaced by process_arrived--
- (B) At the beginning of the barrier the first process to enter the barrier waits until process arrived becomes zero before proceeding to execute P(S).
- (C) Context switch is disabled at the beginning of the barrier and re-enabled at the end.
- (D) The variable process left is made private instead of shared

Statement for Linked Answer Questions 80 & 81:

A CPU has a 32 KB direct mapped cache with 128-byte block size. Suppose A is a two-dimensional array of size 512x512 with elements that occupy 8-bytes each. Consider the following two C code segments, P1 and P2.

```
P1: for (i=0; i<512; i++)
for (j=0; j<512; j++)
x +=A[i] [j];
P2: for (i=0; i<512; i++)
for (j=0; j<512; j++)
```

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x +=A[j] [±1;

P1 and P2 are executed independently with the same initial state, namely, the array A is not in the cache and i,j,x are in registers. Let the number of cache misses experienced by P1 be M1 and that for P2 be 142.

80. The value of M1 is:

- (A) 0
- (B) 2048
- (C) 16384
- (D) 262144

81. The value of the ratio

- (A) 0
- (B)16
- (C)
- (D) 16

Statement for Linked Answer Questions 82 & 83:

Consider the diagram shown below where a number of LAN5 are connected by (transparent) bridges. In order to avoid packets looping through circuits in the graph, the bridges organize themselves in a spanning tree. First, the root bridge is identified as the bridge with the least serial number. Next, the root sends out (one or more) data units to enable the setting up of the spanning tree of shortest paths from the root bridge to each bridge.

Each bridge identifies a port (the root port) through which it will forward frames to the root bridge. Port conflicts are always resolved in favour of the port with the lower index value. When there is a possibility of multiple bridges forwarding to the same LAN (but not through the root port), ties are broken as follows: bridges closest to the root get preference and between such bridges, the one with the lowest serial number is preferred.

82. For the given connection of LANs by bridges, which one of the following choices represents the depth first traversal of the spanning tree of bridges?

- (A) Bi, B5, B3, B4, B2
- (B) Bi, B3, B5, B2, B4
- (C) Bi, B5, B2, B3, B4
- (D) Bi, B3, B4, B5, B2

83. Consider the correct spanning tree for the previous question. Let host Hi send out a broadcast ping packet. Which of the following options represents the correct forwarding table on B3?

(A)

Hosts

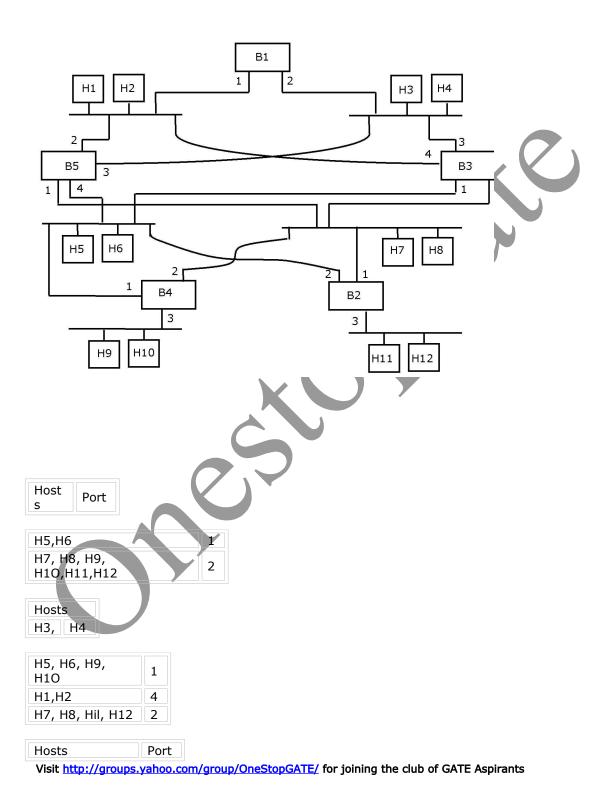
Hi, H2, H3, H4

(B)

Ii 12

H5, H6, H9, HiO







Hi, H2, H3, H4	3
H5, H7, H9, HiO	1
H7, H8, Hil, H12	4

(C) H7, H8, Hil, H12 2

Statement for Linked Answer Questions 84 & 85:

- 84. Which one of the following grammars generates the language $L = \{a''bi'\}$?
- (A) S ACCB
- (B) S— $aSSbab\ C$ —aCbab
- (C) S-ACCB S-ACCB
- (D) C—aCbE C—aCbH
- 85. In the correct grammar above, what is the length of the derivation (number of steps starring from S) to generate the string a/bm with I < m?
- (A) max(I,m)+2
- (B) I+m+2
- (C) I+m+3
- (D) max(I,m)+3