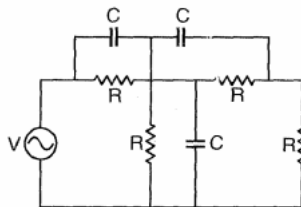


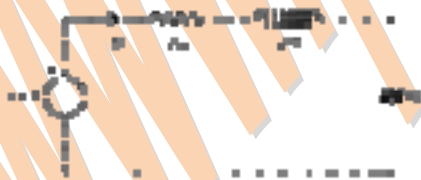
## ELECTRONICS &amp; COMMUNICATION ENGINEERING

## ONE MARK QUESTIONS

1. The minimum number of equations required to analyze the circuit shown in the figure is



- (a.) 3  
 (b.) 4  
 (c.) 6  
 (d.) 7
2. A source of angular frequency 1 rad/sec has a source impedance consisting of  $1\Omega$  resistance in series with  $1H$  inductance. The load that will obtain the maximum power transfer is
- (a.)  $1\Omega$  resistance  
 (b.)  $1\Omega$  resistance in parallel with  $1H$  inductance  
 (c.)  $1\Omega$  resistance in series with  $1F$  capacitor  
 (d.)  $1\Omega$  resistance in parallel with  $1F$  capacitor
3. A series RLC circuit has a resonance frequency of  $1\text{ kHz}$  and a quality factor  $Q = 100$ . If each of  $R$ ,  $L$  and  $C$  is doubled from its original value, the new  $Q$  of the circuit is
- (a.) 25  
 (b.) 50  
 (c.) 100  
 (d.) 200
4. The differential equation for the current  $i(t)$  in the circuit of the figure is



- (a.)  $2 \frac{d^2 i}{dt^2} + 2 \frac{di}{dt} + i(t) = \sin t$   
 (b.)  $\frac{d^2 i}{dt^2} + 2 \frac{di}{dt} + 2i(t) = \cos t$

(c.)  $2 \frac{d^2i}{dt^2} + 2 \frac{di}{dt} + i(t) = \cos t$

(d.)  $\frac{d^2i}{dt^2} + 2 \frac{di}{dt} + 2i(t) = \sin t$

5. n-type silicon is obtained by doping silicon with
- Germanium
  - Aluminium
  - Boron
  - Phosphorus
6. The bandgap of silicon at 300 K is
- 1.36 eV
  - 1.10 eV
  - 0.80 eV
  - 0.67 eV
7. The intrinsic carrier concentration of silicon sample at 300 K is  $1.5 \times 10^{16} /m^3$ . If after doping, the number of majority carriers is  $5 \times 10^{20} /m^3$  the minority carrier density is
- $4.50 \times 10^{11}/m^3$
  - $3.33 \times 10^4/m^3$
  - $5.00 \times 10^{20}/m^3$
  - $3.00 \times 10^5/m^3$
8. Choose proper substitutes for X and Y to make the following statement correct Tunnel diode and Avalanche photodiode are operated in X bias and Y bias respectively.
- X: reverse, Y: reverse
  - X: reverse, Y: forward
  - X: forward, Y: reverse
  - X: forward. Y: forward
9. For an n-channel enhancement type MOSFET, if the source is connected at a higher potential than that of the bulk (i.e.  $V_{SB} > 0$ ), the threshold voltage  $V_T$  of the MOSFET will
- remain unchanged
  - decrease
  - change polarity
  - increase
10. Choose the correct match for input resistance of various amplifier configurations shown below
- Configuration**
- CB: Common Base
- CC: Common Collector
- CE: Common Emitter

**Input resistance**

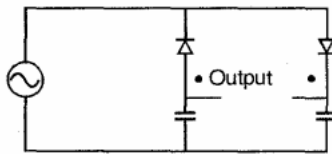
MO: Moderate

HI: High

LO: Low

- (a.) CB-LO, CC-MO, CE-HI
- (b.) CB-LO, CC-HI, CE-MO
- (c.) CB-MO, CC-HI, CE-LO
- (d.) CB-HI, CC-LO, CE-MO

11. The circuit shown in the figure is best described as



- (a.) bridge rectifier
  - (b.) ring modulator
  - (c.) frequency discriminator
  - (d.) voltage doubler
12. If the input to the ideal comparator shown in the figure is a sinusoidal signal of 8V (peak to peak) without any DC component, then the output of the comparator has a duty cycle of



- (a.) 1/2
  - (b.) 1/3
  - (c.) 1/6
  - (d.) 1/12
13. If the differential voltage gain and the common mode voltage gain of a differential amplifier are 48 dB and 2 dB respectively, then its common mode rejection ratio is
- (a.) 23 dB
  - (b.) 25 dB
  - (c.) 46 dB
  - (d.) 50 dB
14. Generally, the gain of a transistor amplifier falls at high frequencies due to the
- (a.) internal capacitances of the device
  - (b.) coupling capacitor at the input
  - (c.) skin effect
  - (d.) coupling capacitor at the output

15. The number of distinct Boolean expressions of 4 variables is
- (a.) 16
  - (b.) 256
  - (c.) 1024
  - (d.) 65536
16. The minimum number of comparators required to build an 8 bit flash ADC is
- (a.) 8
  - (b.) 63
  - (c.) 255
  - (d.) 256
17. The output of the 74 ser GATE of TTL gates is taken from a BJT in
- (a.) totem pole and common collector configuration
  - (b.) either totem pole or open collector configuration
  - (c.) common base configuration
  - (d.) common collector configuration
18. Without any additional circuitry, an 8:1 MUX can be used to obtain
- (a.) some but not all Boolean functions of 3 variables
  - (b.) all functions of 3 variables but none of 4 variables
  - (c.) all functions of 3 variables and some but not all of 4 variables
  - (d.) all functions of 4 variables
19. A 0 to 6 counter consists of 3 flip flops and a combination circuit of 2 input gate(s). The combination circuit consists of
- (a.) one AND gate
  - (b.) one OR gate
  - (c.) one AND gate and one OR gate
  - (d.) two AND gates
20. The Laplace transform of  $i(t)$  is given by
- $$I(s) = \frac{2}{s(1+s)}$$
- As  $t \rightarrow \infty$ , the value of  $i(t)$  tends to
- (a.) 0
  - (b.) 1
  - (c.) 2
  - (d.)  $\infty$

21. The Fourier series expansion of a real periodic signal with fundamental frequency  $f_0$  is given by

$$g_p(t) = \sum_{n=-\infty}^{\infty} c_n e^{j2\pi n f_0 t} \text{ it is given that } c_3 = 3 + j5. \text{ Then } c_{-3} \text{ is}$$

- (a.)  $5 + j3$   
 (b.)  $-3 - j5$   
 (c.)  $-5 + j3$   
 (d.)  $3 - j5$
22. Let  $x(t)$  be the input to a linear, time-invariant system. The required output is  $4x(t - 2)$ . The transfer function of the system should be

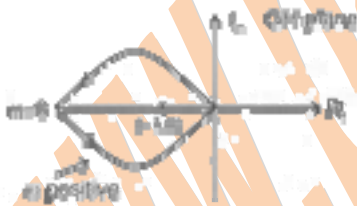
- (a.)  $4e^{j4\pi f}$   
 (b.)  $2e^{-j8\pi f}$   
 (c.)  $4e^{-j4\pi f}$   
 (d.)  $2e^{j8\pi f}$

23. A sequence  $x(n)$  with the z-transform  $X(z) = z^4 + z^2 - 2z + 2 - 3z$  is applied as an input to a linear, time-invariant system with the impulse response  $h(n) = 2\delta(n - 3)$  where

$$\delta(n) = \begin{cases} 1, & n = 0 \\ 0, & \text{otherwise} \end{cases}$$

The output at  $n = 4$  is

- (a.) -6  
 (b.) Zero  
 (c.) 2  
 (d.) -4
24. In the figure, the Nyquist plot of the open-loop transfer function  $G(s)H(s)$  of a system. If  $G(s)H(s)$  has one right-hand pole, the closed-loop system is



- (a.) always stable  
 (b.) unstable with one closed-loop right hand pole  
 (c.) unstable with two closed-loop right hand poles  
 (d.) unstable with three closed-loop right hand poles
25. A PD controller is used to compensate a system. Compared to the uncompensated system, the compensated system has
- (a.) a higher type number  
 (b.) higher noise amplification

- (c.) reduced damping  
(d.) larger transient overshoot
26. The input to a coherent detector is DSB-SC signal plus noise. The noise at the detector output is  
(a.) the in-phase component  
(b.) the quadrature-component  
(c.) zero  
(d.) the envelope
27. The noise at the input to an ideal frequency detector is white. The detector is operating above threshold. The power spectral density of the noise at the output is  
(a.) raised-cosine  
(b.) flat  
(c.) parabolic  
(d.) Gaussian
28. At a given probability of error, binary coherent FSK is inferior to binary coherent PSK, by  
(a.) 6 dB  
(b.) 3 dB  
(c.) 2 dB  
(d.) 0 dB
29. The unit of  $\nabla \times H$  is  
(a.) Ampere  
(b.) Ampere/meter  
(c.) Ampere/meter<sup>2</sup>  
(d.) Ampere-meter
30. The depth of penetration of electromagnetic wave in a medium having conductivity at a frequency of 1 MHz is 25cm. The depth of penetration at a frequency of 4 MHz will be  
(a.) 6.25 cm  
(b.) 12.50 cm  
(c.) 50.00 cm  
(d.) 100.00 cm

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**TWO MARKS QUESTIONS**

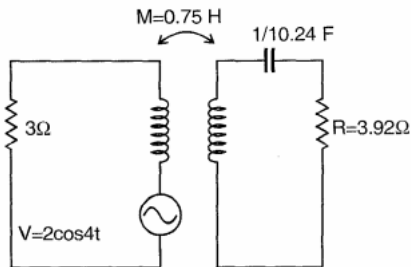
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31. Twelve  $\Omega$  resistance are used as edges to form a cube. The resistance between two diagonally opposite corners of the cube is  
(a.)  $\frac{5}{6} \Omega$   
(b.)  $1 \Omega$

(c.)  $\frac{6}{5} \Omega$

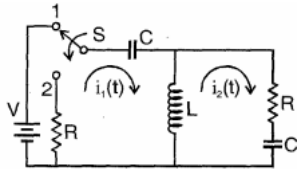
(d.)  $\frac{3}{2} \Omega$

32. The current flowing through the resistance  $R$  in the circuit in the figure has the form  $P \cos 4t$ , where  $P$  is



- (a.)  $(0.18 + j 0.72)$   
 (b.)  $(0.46 + j 1.90)$   
 (c.)  $-(0.18 + j 1.90)$   
 (d.)  $-(0.192 + j 0.144)$
33. The circuit for is given. Assume that the switch  $S$  is in position 1 for a long time and thrown to position 2 at  $t = 0$ .

At  $t = 0^+$ , the current  $i_1$  is



- (a.)  $\frac{-V}{2R}$   
 (b.)  $\frac{-V}{R}$   
 (c.)  $\frac{-V}{4R}$   
 (d.) Zero
34. The circuit for is given. Assume that the switch  $S$  is in position 1 for a long time and thrown to position 2 at  $t = 0$ .

$I_1(s)$  and  $I_2(s)$  are the Laplace transforms of  $i_1(t)$  and  $i_2(t)$  respectively. The equations for the loop currents  $I_1(s)$  and  $I_2(s)$  for the circuit shown in the figure, after the switch is brought from position 1 to position 2 at  $t = 0$ , are



$$(a.) \begin{bmatrix} R+Ls+\frac{1}{Cs} & -Ls \\ -Ls & R+\frac{1}{Cs} \end{bmatrix} \begin{bmatrix} I_1(s) \\ I_2(s) \end{bmatrix} = \begin{bmatrix} \frac{V}{s} \\ 0 \end{bmatrix}$$

$$(b.) \begin{bmatrix} R+Ls+\frac{1}{Cs} & -Ls \\ -Ls & R+\frac{1}{Cs} \end{bmatrix} \begin{bmatrix} I_1(s) \\ I_2(s) \end{bmatrix} = \begin{bmatrix} -\frac{V}{s} \\ 0 \end{bmatrix}$$

$$(c.) \begin{bmatrix} R+Ls+\frac{1}{Cs} & -Ls \\ -Ls & R+Ls+\frac{1}{Cs} \end{bmatrix} \begin{bmatrix} I_1(s) \\ I_2(s) \end{bmatrix} = \begin{bmatrix} -\frac{V}{s} \\ 0 \end{bmatrix}$$

$$(d.) \begin{bmatrix} R+Ls+\frac{1}{Cs} & -Ls \\ -Ls & R+Ls+\frac{1}{Cs} \end{bmatrix} \begin{bmatrix} I_1(s) \\ I_2(s) \end{bmatrix} = \begin{bmatrix} \frac{V}{s} \\ 0 \end{bmatrix}$$

35. An input voltage  $v(t) = 10\sqrt{2} \cos(t+10^\circ) + 10\sqrt{5} \cos(2t+10^\circ) V$  is applied to a series combination of resistance  $R = 1\Omega$  and an inductance  $L = 1H$ . The resulting steady state current  $i(t)$  in ampere is

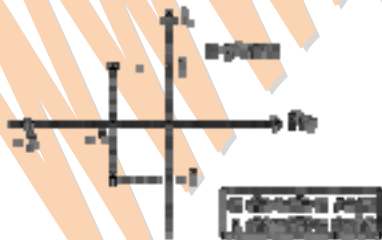
(a.)  $10\cos(t+55^\circ) + 10 \cos(2t+10^\circ + \tan^{-1}2)$

(b.)  $10\cos(t+55^\circ) + \sqrt{\frac{3}{2}} 10 \cos(2t+55^\circ)$

(c.)  $10\cos(t-35^\circ) + 10\cos(2t+10^\circ - \tan^{-1}2)$

(d.)  $10\cos(t-35^\circ) + 10\sqrt{\frac{3}{2}} \cos(2t-35^\circ)$

36. The driving-point impedance  $Z(s)$  of a network has the pole-zero locations are shown in the figure. If  $Z(0) = 3$ , then  $Z(s)$  is



(a.)  $\frac{3(s+3)}{s^2+2s+3}$

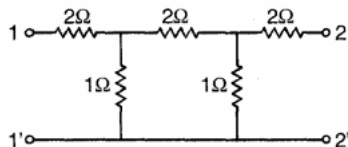


(b.)  $\frac{2(s+3)}{s^2 + 2s + 2}$

(c.)  $\frac{3(s-3)}{s^2 - 2s - 2}$

(d.)  $\frac{2(s-3)}{s^2 - 2s - 3}$

37. The impedance parameters
- $Z_{11}$
- and
- $Z_{12}$
- of the two-port network in the figure are



- (a.)  $Z_{11} = 2.75\Omega$  and  $Z_{12} = 0.25\Omega$
- (b.)  $Z_{11} = 3\Omega$  and  $Z_{12} = 0.25\Omega$
- (c.)  $Z_{11} = 3\Omega$  and  $Z_{12} = 0.25\Omega$
- (d.)  $Z_{11} = 2.25\Omega$  and  $Z_{12} = 0.5\Omega$
38. An n-type silicon bar 0.1cm long and  $100\mu\text{m}^2$  in cross-sectional area has a majority carrier concentration of  $5 \times 10^{20}/\text{m}^3$  and the carrier mobility is  $0.13\text{ m}^2/\text{V}\cdot\text{s}$  at 300K. If the charge of an electron is  $1.6 \times 10^{-19}$  coulomb, then the resistance of the bar is
- (a.)  $10^6$  ohm
- (b.)  $10^4$  ohm
- (c.)  $10^{-1}$  ohm
- (d.)  $10^{-4}$  ohm
39. The electron concentration in a sample of uniformly doped n-type silicon at 300 K varies linearly from  $10^{17}/\text{cm}^3$  at  $x=0$  to  $6 \times 10^{16}/\text{cm}^3$  at  $x=2\mu\text{m}$ . Assume a situation that electrons are supplied to keep this concentration gradient constant with time. If electronic charge is  $1.6 \times 10^{-19}$  coulomb and the diffusion constant  $D_n = 35\text{ cm}^2/\text{s}$ , the current density in the silicon, if no electric field is present, is
- (a.) zero
- (b.)  $-1120\text{ A}/\text{cm}^2$
- (c.)  $-560\text{ A}/\text{cm}^2$
- (d.)  $+1120\text{ A}/\text{cm}^2$
40. Match items in Group 1 with items in Group 2, most suitably.
- Group I
- A. LED
- B. Avalanche Photodiode
- C. Tunnel diode
- D. LASER
- Group 2

1. Heavy (loping)
2. Coherent radiation
3. Spontaneous emission
4. Current gain

Codes:

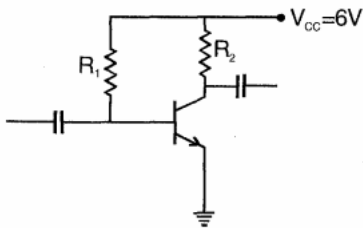
	A	B	C	D
(a.)	1	2	4	3
(b.)	2	3	1	4
(c.)	3	4	1	2
(d.)	2	1	4	3

41. At 300 K, for a diode current of 1 mA, a certain germanium diode requires a forward bias of 0.1435V, whereas a certain silicon diode requires a forward bias of 0.718 V. Under the conditions stated above, the closest approximation of the ratio of reverse saturation current in germanium diode to that in silicon diode is
- (a.) 1
  - (b.) 5
  - (c.)  $4 \times 10^3$
  - (d.)  $8 \times 10^3$
42. A particular green LED emits light of wavelength  $5490 \text{ \AA}$ . The energy bandgap of the semiconductor material used is (Planck's constant =  $6.626 \times 10^{-34} \text{ J-s}$ )
- (a.) 2.26 eV
  - (b.) 1.98 eV
  - (c.) 1.17 eV
  - (d.) 0.74 eV
43. When the gate to-source voltage ( $V_{GS}$ ) of a MOSFET is with threshold voltage of 400 mV, working in saturation is 901 mV the drain current is observed to be 1mA. Neglecting the channel width modulation effect and assuming that the MOSFET is operating at saturation, the drain current for an applied  $V_{GS}$  of 1400 mV is
- (a.) 0.5 mA
  - (b.) 2.0 mA
  - (c.) 3.5 mA
  - (d.) 4.0 mA
44. If P is Passivation, Q is n-well implant, R is metallization and S is source/drain diffusion, then the order in which they are called out in a standard n-well CMOS fabrication process, is
- (a.) P-Q-R-S
  - (b.) Q-S-R-P
  - (c.) R-P-S-Q
  - (d.) S-R-Q-P

45. An amplifier without feedback has a voltage gain of 50, input resistance of  $1\text{K}\Omega$  and output resistance of  $2.5\text{K}\Omega$ . The input resistance of the current-shunt negative feedback amplifier using the above amplifier with a feedback factor of 0.2 is

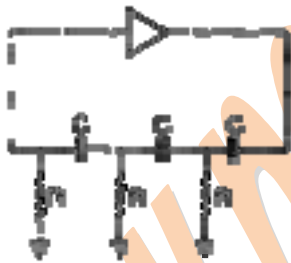
- (a.)  $1/11\text{K}\Omega$   
 (b.)  $1/5\text{K}\Omega$   
 (c.)  $5\text{K}\Omega$   
 (d.)  $11\text{K}\Omega$

46. In the amplifier circuit shown in the figure, the values of  $R_1$  and  $R_2$  are such that the transistor is operating at  $V_{CE} = 3\text{V}$  and  $I_C = 1.5\text{mA}$  when its  $\beta$  is 150. For a transistor with  $\beta$  of 200, the operating point ( $V_{CE}$ ,  $I_C$ ) is



- (a.) (2V, 2mA)  
 (b.) (3V, 2mA)  
 (c.) (4V, 2mA)  
 (d.) (4V, 1mA)

47. The oscillator circuit shown in the figure has an ideal inverting amplifier. Its frequency of oscillation (in Hz) is

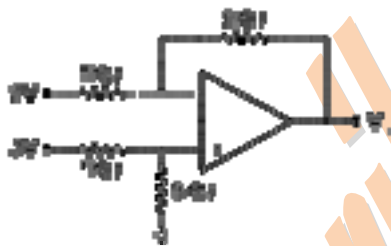


- (a.)  $\frac{1}{(2\pi\sqrt{6}RC)}$   
 (b.)  $\frac{1}{(2\pi RC)}$   
 (c.)  $\frac{1}{(\sqrt{6}RC)}$   
 (d.)  $\frac{1}{\sqrt{6}(2\pi RC)}$

48. The output voltage of the regulated power supply shown in the figure is



- (a.) 3V  
 (b.) 6V  
 (c.) 9V  
 (d.) 12V
49. The action of a JFET in its equivalent circuit can best be represented as a
- (a.) Current Controlled Current Source  
 (b.) Current Controlled Voltage Source  
 (c.) Voltage Controlled Voltage Source  
 (d.) Voltage Controlled Current Source
50. If the op-amp in the figure is idea, the output voltage  $V_{out}$  will be equal to



- (a.) 1 V  
 (b.) 6 V  
 (c.) 14 V  
 (d.) 17 V
51. Three identical amplifiers with each one having a voltage gain of 50, input resistance of  $1K\Omega$  and output resistance of  $250\Omega$ , are cascaded. The open circuit voltage gain of the combined amplifier is
- (a.) 49 dB  
 (b.) 51 dB  
 (c.) 98 dB  
 (d.) 102 dB
52. An ideal sawtooth voltage waveform of frequency 500 Hz and amplitude 3 V is generated by charging a capacitor of  $2\mu F$  in every cycle. The charging requires
- (a.) constant voltage source of 3 V for 1 ms  
 (b.) constant voltage source of 3 V for 2 ms  
 (c.) constant current source of mA for 1 ms  
 (d.) constant current source of 3 mA for 2 ms

53. The circuit shown in the figure has 4 boxes each described by inputs P, Q, R and outputs Y, Z with



The circuit acts as a

- (a.) 4 bit adder giving  $P + Q$   
 (b.) 4 bit subtractor giving  $P - Q$   
 (c.) 4 bit subtractor giving  $Q - P$   
 (d.) 4 bit adder giving  $P + Q + R$
54. If the functions W, X, Y and Z are as follows

$$W = R + \bar{P}Q + \bar{R}S \quad X = PQ\bar{R}S + \bar{P}Q\bar{R}S + PQ\bar{R}\bar{S}$$

$$Y = RS + \bar{P}R + \bar{P}Q + \bar{P}\bar{Q} \quad Z = R + S + \bar{P}Q + \bar{P}\bar{Q}\bar{R} + \bar{P}\bar{Q}S$$

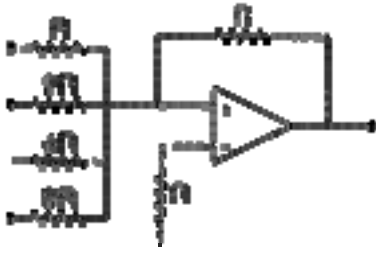
Then

- (a.)  $W = Z, X = \bar{Z}$   
 (b.)  $W = Z, X = Y$   
 (c.)  $W = Y$   
 (d.)  $W = Y = \bar{Z}$
55. A 4 bit ripple counter and a 4 bit synchronous counter are made using flip flops having a propagation delay of 10 ns each. If the worst case delay in the ripple counter and the synchronous counter be R and S respectively, then
- (a.)  $R = 10\text{ns}, S = 40\text{ns}$   
 (b.)  $R = 40\text{ns}, S = 10\text{ns}$   
 (c.)  $R = 10\text{ns}, S = 30\text{ns}$   
 (d.)  $R = 30\text{ns}, S = 10\text{ns}$
56. The DTL, TTL, ECL and CMOS families of digital ICs are compared in the following 4 columns

(P)	(Q)	(R)	(S)	
Fanout is minimum	DTL	DTL	TTL	CMOS
Power consumption is minimum	TTL	CMOS	ECL	DTL
Propagation delay is minimum	CMOS	ECL	TTL	TTL

The correct column is

- (a.) P  
 (b.) Q  
 (c.) R  
 (d.) S
57. The circuit shown in the figure is a 4 bit DAC



The input bits 0 and 1 are represented by 0 and 5 V respectively. The OP AMP is ideal, but all the resistances and the 5 V inputs have a tolerance of  $\pm 10\%$ . The specification (rounded to the nearest multiple of 5%) for the tolerance of the DAC is

- (a.)  $\pm 35\%$
- (b.)  $\pm 20\%$
- (c.)  $\pm 10\%$
- (d.)  $\pm 5\%$

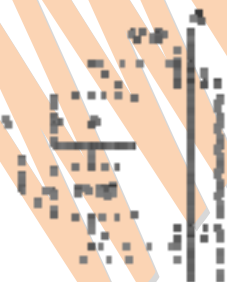
58. The circuit shown in the figure converts



- (a.) BCD to binary code
- (b.) Binary to excess - 3 code
- (c.) Excess - 3 to Gray code
- (d.) Gray to Binary code

59. In the circuit shown in the figure, A is parallel-in, parallel-out 4 bit register, which loads at the rising edge of the clock C. The input lines are connected to a 4 bit bus, W. Its output acts as the input to a 16x 4 ROM whose output is floating when the enable input E is 0. A partial table of the contents of the ROM is as follows:

Address	Y <sub>3</sub>	Y <sub>2</sub>	Y <sub>1</sub>	Y <sub>0</sub>
0000	0	0	0	0
0001	0	0	0	1
0010	0	0	1	0
0011	0	0	1	1
0100	0	1	0	0
0101	0	1	0	1
0110	0	1	1	0
0111	0	1	1	1
1000	1	0	0	0
1001	1	0	0	1
1010	1	0	1	0
1011	1	0	1	1
1100	1	1	0	0
1101	1	1	0	1
1110	1	1	1	0
1111	1	1	1	1





The clock to the register is shown, and the data on the W bus at time  $t_1$  is 0110. The data on the bus at time  $t_2$  is

- (a.) 1111
- (b.) 1011
- (c.) 1000
- (d.) 0010

60. Let P be linearity, Q be time-invariance, R be causality and S be stability. A discrete-time system has the input-output relationship,

$$y(n) = \begin{cases} x(n) & n \geq 1 \\ 0, & n = 0 \\ x(n+1) & n \leq -1 \end{cases}$$

where  $x(n)$  is the input and  $y(n)$  is the output. The above system has the properties

- (a.) P, S but not Q, R
- (b.) P, Q, S but not R
- (c.) P, Q, R, S
- (d.) Q, R, S but not P

**Data given below for two questions. Solve the problem and choose the correct answer.**

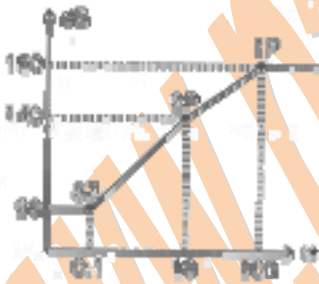
The system under consideration is an RC low-pass filter (RC-LPF) With  $R = 1.0 \leq 2k\Omega$  and  $C = 1.0\mu F$

61. Let  $H(f)$  denote the frequency response of the RC-LPF. Let  $f_1$  be the highest frequency such that  $0 \leq |f| \leq f_1$   $\frac{|H(f_1)|}{H(0)} \geq 0.95$ . Then  $f_1$  (in Hz) is
- (a.) 327.8
  - (b.) 163.9
  - (c.) 52.2
  - (d.) 104.4
62. Let  $t_g(f)$  be the group delay function of the given RC – LPF and  $f_2 = 100$  Hz. Then  $t_g(f_2)$  in ms, is
- (a.) 0.717
  - (b.) 7.17
  - (c.) 71.7
  - (d.) 4.505

63. The signal flow graph of a system is shown in the figure. The transfer function  $\frac{C(s)}{R(s)}$  of the system is



- (a.)  $\frac{6}{s^2 + 29s + 6}$
- (b.)  $\frac{6s}{s^2 + 29s + 6}$
- (c.)  $\frac{s(s+2)}{s^2 + 29s + 6}$
- (d.)  $\frac{s(s+27)}{s^2 + 29s + 6}$
64. The root locus of the system  $G(s)H(s) = \frac{K}{s(s+2)(s+3)}$  has the break-away point located at
- (a.) (-0.5, 0)
- (b.) (-2.548, 0)
- (c.) (-4, 0)
- (d.) (-0.784, 0)
65. The approximate Bode magnitude plot of a minimum-phase system is shown in the figure. The transfer function of the system is



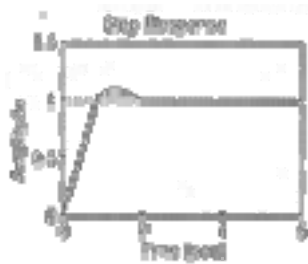
- (a.)  $10^8 \frac{(s+0.1)^3}{(s+10)^2 (s+100)}$
- (b.)  $10^7 \frac{(s+0.1)^3}{(s+10)(s+100)}$
- (c.)  $10^8 \frac{(s+0.1)^2}{(s+10)^2 (s+100)}$



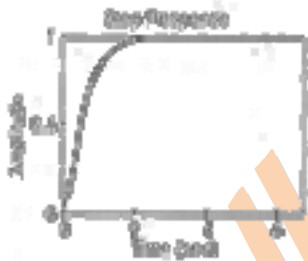
(d.)  $10^9 \frac{(s+0.1)^3}{(s+10)(s+100)^2}$

66. A second-order system has the transfer function  $\frac{C(s)}{R(s)} = \frac{4}{s^2 + 4s + 4}$ . With  $r(t)$  as the unit-step function, the response  $c(t)$  of the system is represented by

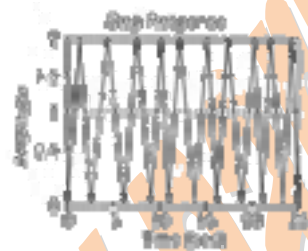
(a.)



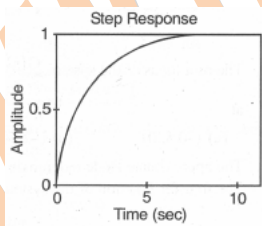
(b.)



(c.)



(d.)



67. The gain margin and the phase margin of a feedback system with  $G(s)H(s) = \frac{s}{(s+100)^3}$  are

(a.) 0 dB,  $0^\circ$

(b.)  $\infty$ ,  $\infty$

(c.)  $\infty$ ,  $0^\circ$

(d.) 88.5 dB,  $\infty$

68. The zero-input response of a system given by the state-space equation  $\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 1 & 0 \end{bmatrix}$  and

$$\begin{bmatrix} x_1(0) \\ x_2(0) \end{bmatrix} = \begin{bmatrix} 1 \\ 0 \end{bmatrix} \text{ is}$$

(a.)  $\begin{bmatrix} te^t \\ t \end{bmatrix}$

(b.)  $\begin{bmatrix} e^t \\ t \end{bmatrix}$

(c.)  $\begin{bmatrix} e^t \\ te^t \end{bmatrix}$

(d.)  $\begin{bmatrix} t \\ te^t \end{bmatrix}$

69. Let X and Y be two statistically independent random variables uniformly distributed in the ranges (-1, 1) and (-2, 1) respectively. Let Z = X + Y. Then the probability that (Z ≤ -2) is

(a.) Zero

(b.)  $\frac{1}{6}$

(c.)  $\frac{1}{3}$

(d.)  $\frac{1}{12}$

**Common Data for Question 70 and 71:**

Let X be the Gaussian random variable obtained by sampling the process at  $t = t_i$  and let

$$Q(\alpha) = \int_{\alpha}^{\infty} \frac{1}{\sqrt{2\pi}} e^{-\frac{x^2}{2}} dx. \text{ Auto correlation function } R_{xx}(\tau) = 4(e^{-0.2|\tau|} + 1) \text{ and mean} = 0$$

70. The probability that  $[x \leq 1]$  is

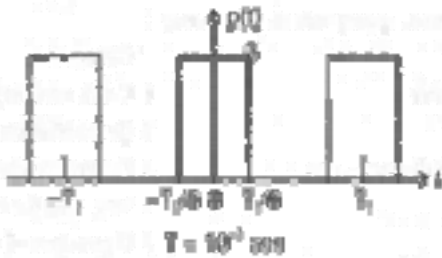
(a.)  $1 - Q(0.5)$

(b.)  $Q(0.5)$

(c.)  $Q\left(\frac{1}{2\sqrt{2}}\right)$

(d.)  $1 - Q\left(\frac{1}{2\sqrt{2}}\right)$

71. Let  $Y$  and  $Z$  be the random variables obtained by sampling  $X(t)$  at  $t = 2$  and  $t = 4$  respectively. Let  $W = Y - Z$ . The variance of  $W$  is
- 13.36
  - 9.36
  - 2.64
  - 8.00
72. Let  $x(t) = 2\cos(800\pi t) + \cos(1400\pi t)$ .  $x(t)$  is sampled with the rectangular pulse train shown in the figure. The only spectral components (in kHz) present in the sampled signal in the frequency range 2.5 kHz to 3.5 kHz are



- 2.7, 3.4
  - 3.3, 3.6
  - 2.6, 2.7, 3.3, 3.4, 3.6
  - 2.7, 3.3
73. A DSB-SC signal is to be generated with a carrier frequency  $f_c = 1$  MHz using a non-linear device with the input-output characteristic

$$V_o = a_0 v_i + a_1 v_i^3$$

where  $a_0$  and  $a_1$  are constants. The output of the non-linear device can be filtered by an appropriate band-pass filter.

Let  $v_i = v_i = A_c^i \cos(2\pi f_c^i t) + m(t)$  where  $m(t)$  is the message signal. Then the value of  $f_c^i$  (in MHz) is

- 1.0
- 0.333
- 0.5
- 3.0

**Common Data for Questions 74 and 75:**

Let  $m(t) = \cos\left[(4\pi \times 10^3)t\right]$  be the message signal and  $c(t) = 5\cos\left[(2\pi \times 10^6)t\right]$  be the carrier.

74.  $c(t)$  and  $m(t)$  are used to generate an AM signal. The modulation index of the generated AM signal is 0.5. Then the quantity  $\frac{\text{Total sideband power}}{\text{Carrier power}}$  is

(a.)  $\frac{1}{2}$

(b.)  $\frac{1}{4}$

(c.)  $\frac{1}{3}$

(d.)  $\frac{1}{8}$

75.  $c(t)$  and  $m(t)$  are used to generate an FM signal. If the peak frequency deviation, of the generated FM signal is three times the transmission bandwidth of the AM signal, then the coefficient of the term  $\cos[2\pi(1008 \times 10^3 t)]$  in the FM signal (in terms of the Bessel coefficients) is

(a.)  $5J_4(3)$

(b.)  $\frac{5}{2}J_8(3)$

(c.)  $\frac{5}{2}J_8(4)$

(d.)  $5J_4(6)$

76. Choose the correct one from among the alternative A, B, C and D after matching an item in Group 1 with the most appropriate item in Group 2.

**Group 1**

- A. Ring modulator
- B. VCO
- C. Foster-Seely discriminator
- D. Mixer

**Group 2**

1. Clock recovery
2. Demodulation of FM
3. Frequency conversion
4. Summing the two inputs
5. Generation of FM
6. Generation of DSB-Sc

(a.) A—1; B—3; C—2; D—4

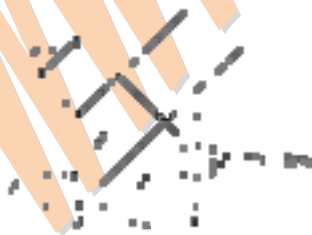
(b.) A—6; B—5; C—2; D—3

(c.) A—6; B—1; C—3; D—2

(d.) A—5; B—6; C—1; D—3

77. A super heterodyne receiver is to operate in the frequency range 550 kHz-1650 kHz, with the intermediate frequency of 450 kHz. Let  $R = \frac{C_{\max}}{C_{\min}}$  denote the required capacitance ratio of the local oscillator and I denote the image frequency (in kHz) of the incoming signal. If the receiver is tuned to 700 kHz, then
- $R = 4.41, I = 1600$
  - $R = 2.10, I = 1150$
  - $R = 3.0, I = 1600$
  - $R = 9.0, I = 1150$
78. A sinusoidal signal with peak-to-peak amplitude of 1.536 V is quantized into 128 levels using a mid-rise uniform quantizer. The quantization-noise power is
- 0.768 V
  - $48 \times 10^{-6} \text{ V}^2$
  - $12 \times 10^{-6} \text{ V}^2$
  - 3.072 V
79. If  $E_b$ , the energy per bit of a binary digital signal, is  $10^{-5}$  watt-sec and the one-sided power spectral density of the white noise,  $N_0 = 10^{-6}$  W/Hz, then the output SNR of the matched filter is
- 26 dB
  - 10 dB
  - 20 dB
  - 13 dB
80. The input to a linear delta modulator having a step-size  $\Delta = 0.628$  is a sine wave with frequency  $f_m$  and peak amplitude  $E_m$ . If the sampling frequency  $f_s = 40$  kHz the combination of the sine-wave frequency and the peak amplitude, where slope overload will take place is
- | $E_m$      | $f_m$ |
|------------|-------|
| (a.) 0.3 V | 8 kHz |
| (b.) 1.5 V | 4 kHz |
| (c.) 1.5 V | 2 kHz |
| (d.) 3.0 V | 1 kHz |
81. If S represents the carrier synchronization at the receiver and  $\rho$  represents the bandwidth efficiency, then the correct statement for the coherent binary PSK is
- $\rho = 0.5$ , S is required
  - $\rho = 1.0$ , S is required
  - $\rho = 0.5$ , S is not required
  - $\rho = 1.0$ , S is not required
82. A signal is sampled at 8 kHz and is quantized using 8-bit uniform quantizer. Assuming  $\text{SNR}_q$  for a sinusoidal signal, the correct statement for PCM signal with a bit rate of R is
- $R = 32$  kbps,  $\text{SNR}_q = 25.8$  dB

- (b.)  $R = 64$  kbps,  $\text{SNR}_q = 55.8\text{dB}$   
 (c.)  $R = 64$  kbps,  $\text{SNR}_q = 49.8\text{dB}$   
 (d.)  $R = 32$  kbps,  $\text{SNR}_q = 49.8\text{dB}$
83. Medium 1 has the electrical permittivity  $\epsilon_1 = 1.5 \epsilon_0$  farad/m and occupies the region to the left of  $x = 0$  plane. Medium 2 has the electrical permittivity  $\epsilon_2 = 2.5\epsilon_0$  farad/m and occupies the region to the right of  $x = 0$  plane. If  $E_1$  in medium 1 is  $E_1 = (2u_x - 3u_y + 1u_z)$  volt/m, then  $E_2$  in medium 2 is  
 (a.)  $(2.0u_x - 7.5u_y + 2.5u_z)$  volt/m  
 (b.)  $(2.0u_x - 2.0u_y + 0.6u_z)$  volt/m  
 (c.)  $(1.2u_x - 3.0u_y + 1.0u_z)$  volt/m  
 (d.)  $(1.2u_x - 2.0u_y + 0.6u_z)$  volt/m
84. If the electric field intensity is given by  $E = (xu_x + yu_y + zu_z)$  volt/m the potential difference between X (2, 0, 0) and Y (1, 2, 3) is  
 (a.) +1 volt  
 (b.) -1 volt  
 (c.) +5 volt  
 (d.) +6 volt
85. A uniform plane wave travelling in air is incident on the plane boundary between air and another dielectric medium with  $\epsilon_r = 4$ . The reflection coefficient for the normal incidence, is  
 (a.) Zero  
 (b.)  $0.5 \angle 180^\circ$   
 (c.)  $0.333 \angle 0^\circ$   
 (d.)  $0.333 \angle 180^\circ$
86. If the electric field intensity associated with a uniform plane electromagnetic wave travelling in a perfect dielectric medium is given by  $E(z, t) = 10\cos(2\pi \times 10^7 t - 0.1\pi z)$  volt/m, the velocity of the travelling wave is  
 (a.)  $3.00 \times 10^8$  m/sec  
 (b.)  $2.00 \times 10^8$  m/sec  
 (c.)  $6.28 \times 10^7$  m/sec  
 (d.)  $2.00 \times 10^7$  m/sec
87. A short-circuited stub is shunt connected to a transmission line as shown in the figure. If  $Z_0 = 50$  ohm, the admittance  $Y$  seen at the junction of the stub and the transmission line is



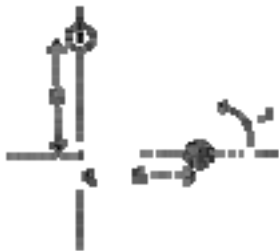
- (a.)  $(0.01 - j0.02)$  mho

- (b.)  $(0.02 - j0.01)$  mho
- (c.)  $(0.04 - j0.02)$  mho
- (d.)  $(0.02 + j0)$  mho

88. A rectangular metal wave guide filled with a dielectric material of relative permittivity  $\epsilon_r = 4$  has the inside dimensions  $3.0\text{cm} \times 1.2\text{cm}$ . The cut-off frequency for the dominant mode is

- (a.) 2.5 GHz
- (b.) 5.0 GHz
- (c.) 10.0 GHz
- (d.) 12.5 GHz

89. Two identical antennas are placed in the  $\theta = \pi/2$  plane as shown in the figure. The elements have equal amplitude excitation with  $180^\circ$  polarity difference, operating at wavelength  $\lambda$ . The correct value of the magnitude of the far-zone resultant electric field strength normalized with that of a single element, both computed for  $\phi = 0$ , is



- (a.)  $2 \cos\left(\frac{2\pi s}{\lambda}\right)$
- (b.)  $2 \sin\left(\frac{2\pi s}{\lambda}\right)$
- (c.)  $2 \cos\left(\frac{\pi s}{\lambda}\right)$
- (d.)  $2 \sin\left(\frac{\pi s}{\lambda}\right)$

90. In an 8085 microprocessor, the instruction CMP B has been executed while the content of the accumulator is less than that of register B. As a result

- (a.) Carry flag will be set but Zero flag will be reset
- (b.) Carry flag will be reset but Zero flag will be set
- (c.) Both Carry flag and Zero flag will be reset
- (d.) Both Carry flag and Zero flag will be set