

Page:

1 of 13

SET - B

ELECTRONICS AND COMMUNICATION ENGINEERING

- Interrupt latency is the time clapsed between:
 - a) Occurrence of an interrupt and its detection by the CPU
 - Assertion of an interrupt and the start of the associated ISR
 - Assertion of an interrupt and the completion of the associated ISR 1
 - Start and completion of associated ISR
- Inverse Fourier transform of signal

 $X(j\omega)=2\pi\delta(\omega)+\pi\delta(\omega-4\pi)+\pi\delta(\omega+4\pi)$

a) 1+cos4xt

b) $\pi(1-\cos 4\pi t)$

c) 2\pi (1-\cos4\pi t)

d) $2\pi(1+\cos 4\pi t)$

Determine the discrete-time Fourier Transform for the signal 3 x[n]=[-2, -1, 0, 1, 2]

$$\stackrel{\uparrow}{\underset{\mathbf{n}=0}{\longleftarrow}}$$

- a) $2j(2\sin 2\omega + \sin \omega)$ b) $-2j(2\sin 2\omega + \sin \omega)$ c) $2(2\cos 2\omega \cos \omega)$ d) $-2(2\cos 2\omega \cos \omega)$
- Auto-correlation of a sinusoid $s(t)=A \sin(\omega t)$ is

 - a) $(A^2/2) \sin(\omega \tau)$ b) $(A/2) \sin(\omega \tau)$
- c) $(A^2/2) \cos(\omega \tau)$ d) $(A/2) \cos(\omega \tau)$
- A source produces 4 symbols with probabilities 1/2,1/4,1/8,1/8. For this source, a practical coding scheme has an average codeword length of 2 bits/symbol. The efficiency of the code is c) 1/2 d) 1/4 b) 7/8
- When signal frequency is 2000 KHz and IF is 455 KHz, the image frequency could be d) 2090 KHz c) 2455 KHz a) 2910 KHz b) 1645 KHz
- The Q of RLC tuned circuit with source voltage V in the figure is 100 at resonant frequency of 100 KHz. What is the voltage Vi across inductor at resonance?

NAN TIME

41 50V

6) V. - V.

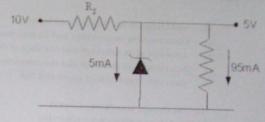
c) 100V

2009

ELECTRONICS AND COMMUNICATION ENGINEERING

SET - B

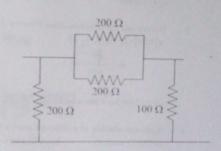
A 5V reference is drawn from the circuit shown in the figure. Zener diode of 400mW and 5V with firing current of 5mA is used. The value of Re is



- a) 50 ohms
- b) 500 ohms
- c) 75 ohms

d) 470 ohms

Find the equivalent resistance R for the network shown in the figure.



- a) 200 ohms
- b) 101 ohms
- c) 220 ohms

d) 100 ohms

16

R

The Voltage across last resistor is V. All resistors are unity. Then Vs is given by d) None of these #1 13 V c) 4 V 6) 8 V

Natural frequency of a unity feedback control system of transfer function

$$G(s) = \frac{10}{s(s+1)}$$

- at 0.5 radice by 3.16 rad/cre
- c) 4.6 rad ser
- d) None of these

Page:

3 of 13

d)

a) 8 times

#17 times

R = 0, G = 0

ELECTRONICS AND COMMUNICATION ENGINEERING

Very low frequency range (R >> ωL, G >> ωC) Very high frequency range (R << ωL, G << ωC).

Which of the following conditions will not guarantee a distortionless transmission line?

SET - B

13 For a	line of charac	teristic impedance Z ₀	terminated in a load Z _R suc	h that Z ₀ > Z ₀ , the		
Voltag	Voltage Standing Wave Ratio (VSWR) is given by					
a) Z _R /2	Lo	b) Z ₀	c) Z _R	d) Z ₀ / Z _R		
14 The be	amwidth of a	reflector antenna of	diameter = 70 cm at 30 GH:	z is		
a) 3.7 d	legree	b) 0.5 degree	c) 1.1 degree	d) I degree		
15 T, = r	$/(1+r^2+r^4)$					
$\sum_{i}^{k}T_{i}=$						
a) 1/4		b) 1	c) None of the above	d) ½		
			He tries his keys at random placed after every attempt)			
a) n/2		b) n	c) √n	d) None of the above		
If a coun	ter having 10	FFs is initially at 0, v	what count will it hold after	2060 pulses?		
a) 000 000 1100 b) 000 001 1100						
c) 000 00	1 1000		d) 000 000 1110			
A memo	ry system of s	ize 16 K bytes is requ	tired to be designed using n	nemory chips which		
have 12	address lines :	and 4 data lines each.	Then the number of such	chips required to design		
	ory system is					
a) 2		b) 4	c) 8	d) 16		
Conside	r the followin	g program for 8085		10.15		
	XRAA			" ","		
	LXI B, 00071	1	10.00	321		
LOOP:	DCX B					
	JNZ LOOP			and the second		
The loop	p will be execu	ited		*		

h) once

d) infinite times

SET - B

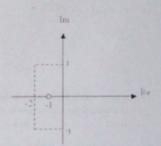
2009

- 20 A certain JK FF has t_{pa}=12 ns. The largest MOD counter that can be constructed from such FFs and still operate up to 10 MHz is
 a) 16 b) 256 c) 8 d) 128
- Convolution of x(t+5) with impulse function $\delta(t-7)$ is equal to
 - a) x(t-2)

b) x(t+12)

c) x(t-12)

- d) x(t+2)
- 22 The purpose of a Cassegrain feed in a parabolic reflector antenna is to
 - a) Achieve higher antenna gain
 - b) Reduce the antenna size
 - c) Reduce the beamwidth
 - (A) Ease of locating the feed at convenient point
- 23 A transfer function G(s) has pole-zero plot as shown in the figure. Given that the steady state function gain is 2, the transfer function G(s) will be given by



- a) $\frac{5(s+1)}{s^2+4s+4}$
- c) $\frac{10(s+1)}{s^2+4s+5}$

- b) $\frac{2(s+1)}{s^2+4s+5}$
- d) $\frac{10(s+1)}{(s+2)}$
- 24 The impedance of a series RLC circuit is
 - a) $\sqrt{R^2 + X_1^2 + X_2^2}$
 - e) $\sqrt{R^2 + (X_1 + X_2)^2}$

- b) $\sqrt{R^2 + X_1^2 X_2^2}$ d) $\sqrt{R^2 + (X_1 - X_2)}$
- 25 The advantage of n/4 QPSK compared to conventional QPSK is ?
 - (A) #/4 QPSK requires less bandwidth compared to conventional QPSK
 - b) z/4 QPSK produces lower envelope variation compared to conventional QPSK
 - e) n/4 QPSK receiver design is simple compared to conventional QPSK
 - dy There is no advantage

2009

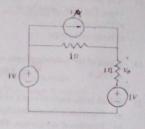
ELECTRONICS AND COMMUNICATION ENGINEERING

- A moving coil iron ammeter may be compensated for frequency errors by a) Series inductance

b) Shunt resistance

e) Series resistor

- A) Shunt capacitor
- Find the value of Vo in the figure.



a) 1/2 V

- 28 What is the approximate skin depth in copper at 100 MHz?
- a) 0.1 micron
- b) 10 microns c) 10 mm
- d) 100 mm
- While designing a low noise amplifier, what is the importance of the noise resistance of a 29 transistor?
 - a) It gives the effective impedance offered by the input with respect to noise current
 - It defines the criterion for conjugate matching of input impedance
 - It tells us the resistance that would generate the same amount of noise at room temperature
 - It tells us how rapidly the noise figure increases as we move away from the optimum source impedance
- 30 Unattenuated radiation field at the surface of the earth of a quarter-wave monopole will exist if the earth surface is
 - a) Lossy dielectric

b) Perfect insulator

c) Perfectly conducting

d) None of these

Given that

$$A = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 2 & 0 \\ 0 & 0 & 3 \end{bmatrix}$$

as Null matrix

b) Identity matrix

CT - 85

d) None of the above

300

Page 6 OF 13

2009

lim x log sin(x) is $x \rightarrow 0$

ELECTRONICS AND COMMUNICATION ENGINEERING

•	201.0 0		
250	100	•	m

be

	4,0						
33	A 12 bit ADC is	s operating with a 1 µs o	lock period and the tota	d conversion time is seen to			
	14 µs. The ADC must be of						
	a) Flash type		b) Counting ty	pe			
	c) Integrating t	ype	d) Successive A	approximation type			
34	Which of the following/types of devices is not field programmable?						
	a) FPGA	b) ASIC	c) CPLD	d) PLD			
35	Which is the correct order of different process steps for a typical FPGA design?						
	a) Functional simulation, Synthesis, Place & Route, Timing Verification						
	b) Functional simulation, Timing Verification, Synthesis, Place & Route						
	c) Timing Verification, Synthesis, Functional simulation, Place & Route						
	d) Synthesis, Functional simulation, Timing Verification, Place & Route						
36	The theoretical	dividing line between F	teduced Instruction Set	computing (RISC)			
	microprocessor and Complex Instructions Set Computing (CISC) microprocessor is						
	a) Instruction execution rate to be one instruction per clock cycle						
	b) Number of	f address and data lines					
	c) Number o	f pins in the chip					
	d) None of th	e above					
37	In a pulsed rada other radar pars a factor of	r, the peak radar transi ameters unchanged. The	nitter power is increased maximum range capab	I by a factor of 81 keeping a ility of the radar increases b			
	a) 81	b) 9	e) 27	d) 3			
38		tter has a PRF of 1500 I	PPS and a pulse width of	I micro second. Its duty eye			
	a) 0.0075	b) 0.015	c) 0.0015	d) 0.00075			
39	Which of the following technology results in least power dissipation?						
	a) CMOS	b) ECL	c) TTL	d) NMOS			
40	A power MOSFET is a						
	a) Current controlled device		b) Frequency controlled device				
	e) Voltage controlled device		d) None of the above				

e) Voltage controlled device

Page: 7 OF 13

SET - B

2009

ELECTRONICS AND COMMUNICATION ENGINEERING

- 41 The advantage of differential PSK (DPSK) over coherent PSK is a) DPSK requires less bandwidth compared coherent PSK
 - b) DPSK receiver design is simple compared to coherent PSK
 - c) DPSK Bit Error rate is lower than coherent PSK
 - d) For same bandwidth, DPSK bit rate is higher compared to coherent PSK
- 42 An ideal current meter should have
 - a) Zero resistance
 - c) Infinite resistance

- b) Finite resistance
- d) Very large resistance
- 43 For the network shown in the figure V_i(t) is the input and i(t) is the output. The transfer function

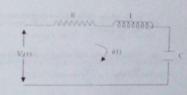
$$\frac{I(s)}{Vi(s)}$$

of the network is

a)
$$\frac{C}{R(s^2 + L(s+1))}$$

c)
$$Cs$$

$$RCs^2 + LCs + 1$$



b)
$$C$$

$$LCx^2 + RCx + 1$$

$$\frac{Cs}{LCs^2 + RCs + 1}$$

eyele

ig all

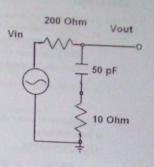
- be

- 44 The length of a lossy shorted stub is 1/32 λ. What will be the nature of its input impedance?
 - a) A low inductive reactance in series with a low resistance
 - b) A low capacitive reactance in shunt with a high resistance
 - e) A high capacitive reactance in series with a low resistance
 - d) A high inductive reactance in series with a low resistance

ELECTRONICS AND COMMUNICATION ENGINEERING

SET - B

45 Refer to the circuit in the figure. Which of the following statements is NOT true about this circuit?



- a) It is a low pass filter
- c) Its phase shift varies monotonically with the frequency
- b) It has a finite attenuation at infinite frequency
- d) It has one pole and one zero
- 46 Which of the following is the correct continuity equation:

$$\nabla \bullet J = \frac{\partial \rho}{\partial t}$$

- d) $\nabla \bullet J = -\frac{\partial E}{\partial t}$
- 47 α, β are the roots of the equation $x^2 + x + 1 = 0$. Then $\alpha^{3n} + \beta^{3n}$ is

 a) 1 b) 2 c) 0 d) 3
- A point moving in the complex plane satisfies the following relation
 z² + z*² = 8, where z* stands for the complex conjugate of z.
 The difference of the distances of the moving point from (2√2,0) and (-2√2,0) is
 a) 8
 b) 2
 c) 4
 d) 6
- 49 The greatest negative number which can be stored in a computer that has 8-bit word length and uses 2's complement arithmetic is

 a) -256 b) -255 c) -128 d) -127
- 50 Which of the following ADCs uses over sampling in its operation
 a) Sigma delta ADC
 b) Counter ramp convertor
 e) Successive Approximation Register ADC
 d) Flash Convertor



Page:

9 OF 13

SET - B

2009

ELECTRONICS AND COMMUNICATION ENGINEERING

- A mealy state machine's output depends on 53
 - a) State and outputs

- b) Inputs
- d) State and inputs e) State
- The characteristic equation of the T-FF is given by

a)
$$Q^* = TQ$$

c) $Q^* = TQ$

b)
$$Q^* = TQ$$

b)
$$Q^* = T\overline{Q}$$

d) $Q' = T\overline{Q} + Q\overline{T}$

- Percentage modulation of an AM wave having a power content of 8 KW at carrier frequency and 2 KW in each of its side bands is
 - a) 60 %

b) 70 %

- c) 100 %
- d) 80 %
- Approximate equivalent noise temperature (deg. K) of an amplifier with a noise factor of 1.04
 - a) 301.6

b) 11.6

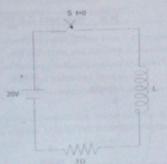
c) 278.4

- d) 5.8
- A differential amplifier is used in the input stage of Op-Amps to achieve very high 55 a) open -loop gain b) bandwidth c) slew rate

- Which two-port parameters are best suited for analyzing a series-shunt feedback circuit? 56
 - a) Z-Parameters
- b) H-Parameters
- c) Y-Parameters
- d) S-Parameters

57

MIL



For the circuit shown in the figure, the switch is closed at t=0. After some time when the current in the inductor was 6A, the rate of change of current through the inductor was £A/sec. The value of the inductor is

** 0 5 11

6) 1.5 11

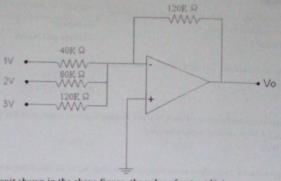
c) 1.0 H

d) Indeterminate

ELECTRONICS AND COMMUNICATION ENGINEERING

SET - B

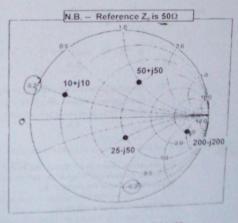
58



In the circuit shown in the above figure, the value of output Vo is

59 The characteristics equation of a closed loop control system is given as $s^2 + 4s + 16 = 0$. The resonant frequency in radian/sec of the system is

60 Which point in the Smith Chart is labeled with the WRONG impedance?



#1 16-j10

b) 50+150

c) 200-j200

d) 25-150

The same

INDIAN SPACE RESEARCH ORGANISATION

Page:

11 OF 13

2009

exerier is still NW

ELECTRONICS AND COMMUNICATION ENGINEERING

SET - B

2"

WARLIES.

63	If a line is terminated in an open circuit, the VSWR is					
	a) =0	6) 0	c) 1	d)-1		
62	Unit of relative per	meability is				
	a) Henry/meter	b) Henry	c) Henry-meter	d) It is dimensionles		
63	$f(x) = [tan^2x]$					
	(stands for greate	est integer function)				
	a) f(x) continuos	is at x = 0				
	b) limit f(x) does	not exist as x tend to	0			
	c) \(\Gamma(0) = 1 \)					
	d) f(x) not deriva	ble at x = 0				
64	Number of real value	nes of $(a+ib)^{1/a}+(a+ib)^{1/a}$	a-ib) 10 is			
	2)0	b) 1	c) n	d) None of the above		
65	A 5 DR DAC has a c	turrent output. For a	digital input of 10100, an	output current of 10 mA i		
			ent for a digital input of 11	101?		
	a) 14.5 mA	b) = 10 mA	c) = 100 mA	d) Not possible to		
				calculate		
66	The ethernet protoc	col uses				
	a) CSMA/CA	b) SCPC	c) CSMA/CD	A Charlet Office		
			C) CSMACD	d) Slotted ALOHA		
67	A micro controller	differs from a microp	processor in terms of			
*	a) 1/0 interfaces :	and instruction decod	ing	C.C		
	(b) Memory config	guration and I/O inter		and the second		
	c) Data bus widtl	and clock speed	rat	The The		
		guration and instructi	on decoding	HALL STORY		
48	An 8-bit microcontr	oller has an external t	CAM with the memory map	from \$000H to BETTH		
	The number of bytes	this RAM can store i	s	itom soont to strill.		
	a) 8193	b) 8192	c) 8191	d) 8000		
69	An audio frances	of 15 Villa is for				
	resulting bandwidth	is to Kitz is trequenc	y modulated with a deviation	on of 75 KHz. The		
	a) 180 KHz		e) 210 KHz	d) 240 KHz		

A IkW carrier is Amplitude Modulated to a depth of 60%. The total power in the modulated

411.6 kW

5) 1.56 kW

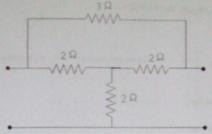


Page:

12 OF 13 SET - B

ELECTRONICS AND COMMUNICATION ENGINEERING

71



The ver parameter of the network shown in the given figure will be

2) 1/2

b) - 1/

- d)- 1/
- An inductance of 1 H is realized using air core with 100 turns. What will be the inductance if the number of turns are doubled?

- b) 0.5 H

- d) None of these
- An inductor of inductance 0.1 H, carrying current of 6 Amps will store energy of
 - a) 6 Joules
- b) 36 Joules
- c) 1.8 Joules
- d) 3.6 Joules
- A coil has self resistance of 5Ω and inductance of 1 μH. The value of Q at 1 MHz frequency is.
 - a) 200
- b) 100

c) 40

- d) None of these
- The closed loop transfer function of a unity feedback control system is $G(s) = \frac{1}{(s+2)^2}$. The closed loop transfer function will have poles at
 - a) -2. ±1
- b) -2,-1

ein

- d) -2,2
- The dominant mode of propagation in microstrip line in low frequency approximation is a) TM₁₁ b) Quasi-TEM c) TEM d) TE₀₁ 76
- Consider a transmission line of characteristic impedance 50 ohms and the line is terminated at one end by +j50 ohms, the VSWR produced in the transmission line will be d) infinity b) zero 167 + 3
 - The divergence of magnetic flux density is Bi.o



Page:

13 OF 13

2009

ELECTRONICS AND COMMUNICATION ENGINEERING

SET - B

A straight line and a circle of radius a are given. A chord is drawn at random to this circle parallel to the given line. Expected length of the chord is

a) Za

b) πa

c) ma/2

d) None of the above

S0 There is a function f(x), such that f(0) = 1 and f'(0) = -1 and f(x) is positive for all values of x. Then,

- a) f"(x) < 0 for all x
- b) $-1 < f^{**}(x) < 0$ for all x

