Questions Q1. to Q20. carry one mark each.

Q1. If **A** and **B** are square matrices of order  $4 \times 4$  such that  $\mathbf{A} = 5\mathbf{B}$  and  $|\mathbf{A}| = \alpha \cdot |\mathbf{B}|$ , then  $\alpha$  is

(A) 5

(B) 25

(C)625

(D) None of these

**Q2.** The particular integral of the differential equation  $(D^3 - D)y = e^x + e^{-x}$ ,  $D = \frac{d}{dx}$  is

(A)  $\frac{1}{2}(e^x + e^{-x})$ 

(B)  $\frac{1}{2}x(e^x + e^{-x})$ 

(C)  $\frac{1}{2}x^2(e^x + e^{-x})$ 

(D)  $\frac{1}{2}x^2(e^x - e^{-x})$ 

Q3. If  $x(t) = \frac{d}{dt} \{te^{-t} u(t)\}$ , then laplace transform of x(t) is

 $(A) \frac{1}{s(s+1)^2}$ 

 $(B)\frac{s}{(s+1)^2}$ 

 $(C)\frac{e^{-s}}{s+1}$ 

 $(D)\frac{e^{-s}}{(s+1)^2}$ 

**Q4.** Consider the x(t) as shown in fig. Q4. The FT of x(t) is

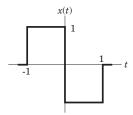


Fig Q4

(A)  $\frac{2\sin\omega-2}{\omega}$ 

(B)  $\frac{2\cos\omega-2}{j\omega}$ 

(C)  $2j\omega\cos\omega$ 

(D)  $2j\omega\sin\omega$ 

Q5. If a resistor of  $10 \Omega$  is placed in parallel with voltage source in the circuit of fig. Q5, the current i will be

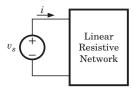


Fig Q5

(A) increased

(B) decreased

(C) unchanged

(D) It is not possible to say

- **Q6.** The current carrying capacity of a 1 W, 4 M $\Omega$  resistor used in radio receiver is
  - (A) 0.5 kA

(B) 2 kA

(C) 2 mA

- (D) 0.5 mA
- **Q7.** The equation governing the diffusion of neutral atom is
  - (A)  $\frac{\partial N}{\partial t} = D \frac{\partial^2 N}{\partial x^2}$

(B)  $\frac{\partial N}{\partial x} = D \frac{\partial^2 N}{\partial t^2}$ 

(C)  $\frac{\partial^2 N}{\partial t^2} = D \frac{\partial N}{\partial x}$ 

- (D)  $\frac{\partial^2 N}{\partial x^2} = D \frac{\partial N}{\partial t}$
- **Q8.** The p-type substrate in a monolithic circuit should be connected to
  - (A) any dc ground point
  - (B) the most negative voltage available in the circuit
  - (C) the most positive voltage available in the circuit
  - (D) no where, i.e. be floating
- **O9.** Consider the List I and List II

List I (Oscillator)

List II (Characteristic)

- P. Colpitts Oscillator
- Q. Phase shift Oscillator
- R. Tunnel diode Oscillator
- S. Relaxation Oscillator

- 1. RC Oscillator
- 2. LC Oscillator
- 3. Negative resistance Oscillator
- 4. Sweep Circuits

- The correct match is
- P Q R S
  (A) 1 2 3 4
  (B) 2 1 3 4
  (C) 1 2 4 3
- (D) 2 1 4 3

# Multiple Choice Questions

# **GATE**

Electronics & Communications

Ву

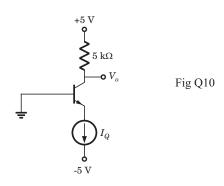
R. K. Kanodia

Price 425.00

Pages 602

Published by **NODIA & COMPANY** 

**Q10.** For the circuit shown in fig. Q10,  $V_{CB} = 0.5 \text{ V}$  and  $\beta = 100$ . The value of  $I_Q$  is



(A) 1.68 mA

(B) 0.909 mA

(C) 0.134 mA

- (D) None of the above
- **Q11.** A four-variable switching function has minterms  $m_6$  and  $m_9$ . If the literals in these minterms are complemented, the corresponding minterm numbers are
  - (A)  $m_3$  and  $m_0$
  - (B)  $m_9$  and  $m_6$
  - (C)  $m_2$  and  $m_0$
  - (D)  $m_6$  and  $m_9$
- Q12. The diode logic circuit of fig. Q12 is a

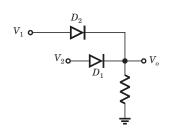


Fig Q12

(A) AND

(B) OR

(C) NAND

- (D) NOR
- **Q13.** The even part of a function x[n] = u[n] u[n-4] is

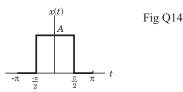
(A) 
$$\frac{1}{2} \{1 + \delta[n] - u[n-4] - u[-n-4] \}$$

(B) 
$$\frac{1}{2} \{ u[n+3] - u[n-4] + \delta[n] \}$$

(C) 
$$\frac{1}{2} \{u[n] + u[-n] - u[n-4] - u[-n-4] \}$$

(D) Above all

#### The trigonometric Fourier series for the waveform shown in fig Q14 will be **O14.**



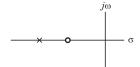
(A) 
$$\frac{A}{2} + \frac{2A}{\pi} (\sin t - \frac{1}{3} \sin 3t + \frac{1}{5} \sin 5t....)$$

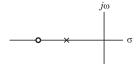
(B) 
$$\frac{A}{2} + \frac{2A}{\pi} (\cos t - \frac{1}{2} \cos 2t + \frac{1}{3} \cos 3t....)$$

(C) 
$$\frac{A}{2} + \frac{2A}{\pi} (\cos t - \frac{1}{3} \cos 3t + \frac{1}{5} \cos 5t....)$$

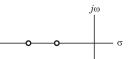
(D) 
$$\frac{A}{2} + \frac{2A}{\pi} (\sin t + \cos t + \frac{1}{3} \sin 3t + \frac{1}{3} \cos 3t \dots)$$

Q15. The poll–zero configuration of a phase–lead compensator is given by

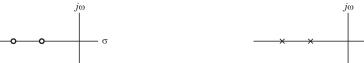




(A)



(B)



(C)

(D)

Q16. While designing controller, the advantage of pole–zero cancellation is

- (A) The system order is increased
- (B) The system order is reduced
- (C) The cost of controller becomes low
- (D) System's error reduced to optimum levels

# GATE ECE By R. K. Kanodia

MCQs: The book contains only solved Multiple choice questions (MCQ) which is the main requirement of the GATE exam. Each and every problem has its complete solution. We understand the fact that theoretical studies should be done from the standard book, that one has studied for the semester exams and thus one should use the same material to understand the concepts of the same. We have deliberately excluded theoretical matter in the book so as not to mislead the students. However, wherever needed, satisfactory explanation of the formula has been included in the solution.

Q17. Assertion (A): PSK is inferior to FSK.

Reason (R): PSK require less bandwidth than FSK.

Choose correct option:

- (A) Both A and R individually true and R is the correct explanation of A.
- (B) Both A and R individually true and but R is not the correct explanation of A.
- (C) A is true but R is false
- (D) A is false
- Q18. In a certain telemetry system, the measured values are converted to digital form. The digital values can then be transmitted via FSK (binary or quaternary), or BPSK or QPSK systems. Out of these the best noise immunity can be obtained with

(A) binary FSK

(B) quaternary FSK

(C) BPSK

(D) QPSK

**Q19.** An antenna, when radiating, has a highly directional radiation pattern. When the antenna is receiving, its radiation pattern

(A) is more directive

(B) is less directive

(C) is the same

- (D) exhibits no directivity at all
- **Q20.** The beamwidth between first null of uniform linear array of N equally spaced (element spacing = d) equally excited antenna is determined by

(A) N alone and not by d

(B) d alone and not by N

(C) the ratio N/d

(D) the product Nd

Questions Q21. to Q75. carry two marks each.

Q21. If 
$$\mathbf{A} = \begin{bmatrix} 0 & -\tan\frac{\alpha}{2} \\ \tan\frac{\alpha}{2} & 0 \end{bmatrix}$$
 then  $(\mathbf{I} - \mathbf{A}) \cdot \begin{bmatrix} \cos\alpha & -\sin\frac{\alpha}{2} \\ \sin\alpha & \cos\alpha \end{bmatrix}$  is equal to

 $(A) \mathbf{I} + \mathbf{A}$ 

(B)  $\mathbf{I} - \mathbf{A}$ 

(C) I + 2A

- (D) I 2A
- **Q22.** For what value of  $x \left( 0 \le x \le \frac{\pi}{2} \right)$ , the function  $y = \frac{x}{(1 + \tan x)}$  has a maxima?
  - (A)  $\tan x$

(B) 0

(C)  $\cot x$ 

- (D)  $\cos x$
- Q23. The value of  $\int_{0}^{2a} \frac{f(x)}{f(x) + f(2a x)} dx$  is
  - (A) 0

(B) 1

(C) a

- (D) 2a
- **Q24.** The integrating factor for the differential equation  $(x^3 + xy^4)dx + 2y^3dy = 0$  is given by
  - (A)  $e^{-x}$

(B)  $e^{x^2}$ 

(C)  $e^x$ 

- (D)  $e^{-x^2}$
- Q25.  $\int_{c} \frac{\cos \pi z}{z 1} dz = ? \text{ where c is the circle } |z| = 3$ 
  - (A)  $i2\pi$

(B)  $-i2\pi$ 

(C)  $i6\pi^2$ 

- (D)  $-i6\pi^2$
- **Q26.** If 3 is the mean and  $\frac{3}{2}$  is the standard deviation of a binomial distribution, then the distribution is
  - $(A)\left(\frac{3}{4} + \frac{1}{4}\right)^{12}$

 $(B)\left(\frac{1}{2} + \frac{3}{2}\right)^{12}$ 

 $(C)\left(\frac{4}{5} + \frac{1}{5}\right)^{60}$ 

(D)  $\left(\frac{1}{5} + \frac{4}{5}\right)^5$ 

# GATE ECE By R. K. Kanodia

**Levels of MCQs**: The Multiple choice questions included in the guide are in a conceptually evolving method, allowing the student to progress from one level of complexity to another but always aiding in understanding the basic foundation of the subject. Thus, the MCQs gradually and scientifically advance from the basic level to a more complex level, helping in the systematic understanding of the problem rather than an abrupt one.

**Q27.** For the differential equation  $dy/dx = x - y^2$  given that

x: 0		0.2	0.4	0.6	
<i>y</i> :	0	0.02	0.0795	0.1762	

Using Milne predictor–correction method, the y at next value of x is

(A) 0.2498

(B) 0.3046

(C) 0.4648

(D) 0.5114

**Q28.** The inverse Fourier transform of  $X(j\omega) = \frac{d}{d\omega} \left( \frac{4\sin 4\omega \sin 2\omega}{\omega} \right)$  is

- (A)  $t^2 2e^{-t}$  rect  $\{2(t-4)\}$
- (B)  $t^2 + 2e^{-t}$  rect (2(t+4))
- (C)  $-t\{\text{rect}(2t+8)+\text{rect}(2t-8)\}$
- (D)  $t\{\text{rect}(2t+4) \text{rect}(2t-8)\}$

**Q29.** The time signal x(t) corresponding to  $X(s) = s \frac{d^2}{ds^2} \left( \frac{1}{s^2 + 9} \right) + \frac{1}{s + 3}$  is

(A) 
$$\left(e^{-3t} + \frac{2t}{3}\sin 3t + \frac{t^2}{9}\cos 3t\right)u(t)$$

(B)  $(e^{-3t} + 2t\sin 3t + t^2\cos 3t)u(t)$ 

(C) 
$$\left(e^{-3t} + \frac{2t}{3}\sin 3t + t^2\cos 3t\right)u(t)$$

(D)  $(e^{-3t} + t^2 \sin 3t + 2t \cos 3t)u(t)$ 

**Q30.** The incidence matrix of a graph is as given below

$$\mathbf{A} = \begin{bmatrix} 1 & 0 & 0 & 0 & 1 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 & -1 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & -1 & 1 & -1 \\ 0 & 0 & 0 & 1 & 0 & 0 & -1 & 0 \end{bmatrix}$$

The number of possible tree are

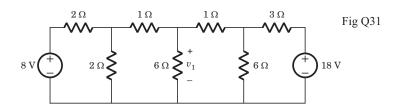
(A) 40

(B) 70

(C) 50

(D) 240

**Q31.** In the fig Q31 the value of  $v_1$  is



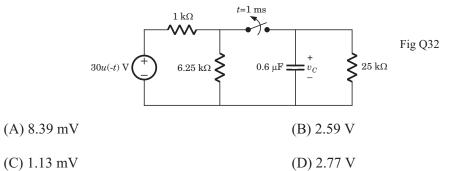
(A) 6 V

(B) 7 V

(C) 8 V

(D) 10 V

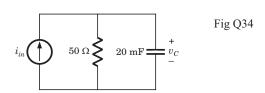
Q32. In the circuit of fig. Q32 the 30 V source has been applied for a long time. The switch is opened at t = 1 ms. At t = 4 ms the  $v_c$  (4 ms) is



- Q33. For a *RLC* series circuit  $R = 20\Omega$ , L = 0.6 H, the value of C will be
  - [CD =critically damped, OD =over damped, UD =under damped].

	CD	OD	UD
(A)	$C = 6 \mathrm{mF}$	C > 6  mF	C < 6 mF
(B)	$C = 6 \mathrm{mF}$	C < 6 mF	$C > 6 \mathrm{mF}$
(C)	C > 6  mF	$C = 6 \mathrm{mF}$	C < 6 mF
(D)	C < 6 mF	C = 6  mF	C >6 mF

**Q34.** In the circuit shown in Fig. Q34  $v(0^-) = 8 \text{ V}$  and  $i_{in}(t) = 4\delta(t)$ . The  $v_C(t)$  for  $t \ge 0$  is



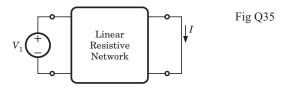
(A)  $164e^{-t}$  V

(B)  $208e^{-t}$  V

(C)  $208(1-e^{-3t})$  V

(D)  $164e^{-3t}$  V

Q35. In the circuit shown in fig. Q35, when the voltage  $V_1$  is 10 V, the current I is 1 A. If the applied voltage at port-2 is 100 V, the short circuit current flowing through at port 1 will be



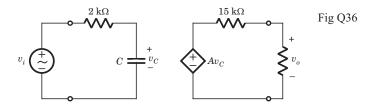
(A) 0.1 A

(B) 1 A

(C) 10 A

(D) 100 A

Q36. The network function of circuit shown in fig.Q36 is  $H(\omega) = \frac{V_o}{V_1} = \frac{4}{1 + j0.01\omega}$ 



The value of the *C* and *A* is

(A)  $10 \, \mu F$ , 6

(B)  $5 \mu F$ , 10

(C)  $5 \mu F$ , 6

- (D)  $10 \mu F$ , 10
- Q37. In germanium( $n_i = 2.4 \times 10^{13} \text{ cm}^{-3}$ ) semiconductor at T = 300 K, the acceptor concentrations is  $N_a = 10^{13} \text{ cm}^{-3}$  and donor concentration is  $N_d = 0$ . The thermal equilibrium concentration  $p_0$  is
  - (A)  $2.97 \times 10^9$  cm<sup>-3</sup>
  - (B)  $2.68 \times 10^{12}$  cm  $^{-3}$
  - (C)  $2.95 \times 10^{13}$  cm<sup>-3</sup>
  - (D)  $2.4 \, \text{cm}^{-3}$
- Q38. A silicon  $(n_i = 1.5 \times 10^{10} \text{ cm}^{-3})$  pn junction at T = 300 K has  $N_d = 10^{14} \text{ cm}^{-3}$  and  $N_a = 10^{17} \text{ cm}^{-3}$ . The built-in voltage is
  - (A) 0.63 V

(B) 0.93 V

(C) 0.026 V

- (D) 0.038 V
- Q39. The maximum electric field in reverse-biased silicon pn junction is  $|E_{\text{max}}| = 3 \times 10^5 \text{ V/cm}$ . The doping concentration are  $N_d = 4 \times 10^{16} \text{ cm}^{-3}$  and  $N_a = 4 \times 10^{17} \text{ cm}^{-3}$ . The magnitude of the reverse bias voltage is
  - (A) 3.6 V

(B) 9.8 V

(C) 7.2 V

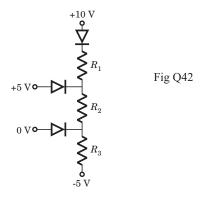
(D) 12.3 V

- **Q40.** A ideal *n*-channel MOSFET has parameters  $\mu_n = 525 \text{ cm}^2/\text{V} \text{s}$ ,  $V_{TN} = 0.75 \text{ V}$ ,  $t_{ox} = 400 \text{ A}^\circ$ . When MOSFET is biased in the saturation region at  $V_{GS} = 5 \text{ V}$ , the required rated current is  $I_{D(sat)} = 6 \text{ mA}$ . The required ratio W/L is
  - (A) 14.7

(B) 11.2

(C) 9.61

- (D) 7.2
- **Q41.** For a *n* channel enhancement-mode MOSFET the parameters are  $V_{TN} = 0.8 \text{ V}$ ,  $k'_n = 8 \mu \text{A/V}^2$  and W/L = 5. If the transistor is biased in saturation region with  $I_D = 0.5 \text{ mA}$ , then required  $v_{GS}$  is
  - (A) 1.68 V
  - (B) 2.38 V
  - (C) 4.56 V
  - (D) 3.14 V
- **Q42.** The cutin voltage for each diode in fig. Q42 is  $V_{\gamma} = 0.6$  V. Each diode current is 0.5 mA. The value of  $R_1$ ,  $R_2$  and  $R_3$  will be respectively

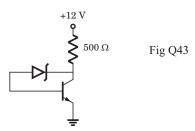


(A)  $10 \text{ k}\Omega$ ,  $5 \text{ k}\Omega$ ,  $2.93 \text{ k}\Omega$ 

(B)  $6 \text{ k}\Omega$ ,  $3 \text{ k}\Omega$ ,  $3.43 \text{ k}\Omega$ 

(C) 5 k $\Omega$ , 6 k $\Omega$ , 4.933 k $\Omega$ 

- (D)  $6 \text{ k}\Omega$ ,  $8 \text{ k}\Omega$ ,  $6.43 \text{ k}\Omega$
- **Q43.** In the circuit of fig. Q43 Zener voltage is  $V_Z = 5$  V and  $\beta = 100$ . The value of  $I_{CQ}$  and  $V_{CEQ}$  are



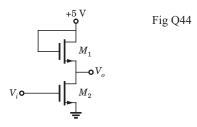
(A) 12.47 mA, 43 V

(B) 12.47 mA, 5.7 V

(C) 10.43 A, 5.7 V

(D) 10.43 A, 43 V

**Q44.** The transistors in the circuit of fig. Q44 have parameter  $V_{TN} = 0.8 \,\text{V}$ ,  $k_n' = 40 \,\mu\text{A} / \,\text{V}^2$  and  $\lambda = 0$ . The width-to-length ratio of  $M_2$  is  $\left(\frac{W}{L}\right)_2 = 1$ . If  $V_o = 0.10 \,\text{V}$  when  $V_i = 5 \,\text{V}$ , then  $\left(\frac{W}{L}\right)_1$  for  $M_1$  is



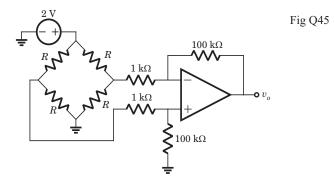
(A) 47.5

(B) 28.4

(C) 40.5

(D) 20.3

Q45. In the circuit of fig. Q45 the CMRR of the op-amp is 60 dB. The magnitude of the  $v_o$  is



(A) 1 mV

(B) 100 mV

(C) 200 mV

(D) 2 mV

**Q46.** If the X and Y logic inputs are available and their complements  $\overline{X}$  and  $\overline{Y}$  are not available, the minimum number of two-input NAND required to implement  $X \oplus Y$  is

(A)4

(B) 5

(C) 6

(D) 7

**Q47.** There are four Boolean variables  $x_1$ ,  $x_2$ ,  $x_3$  and  $x_4$ . The following function are defined on sets of them

$$f(x_3, x_2, x_1) = \Sigma m(3,4,5)$$
  

$$g(x_4, x_3, x_2) = \Sigma m(1,6,7)$$
  

$$h(x_4, x_3, x_2, x_1) = fg$$

Then  $h(x_4, x_3, x_2, x_1)$  is

(A)  $\Sigma$ m(3, 12, 13)

(B)  $\Sigma$ m(3, 6)

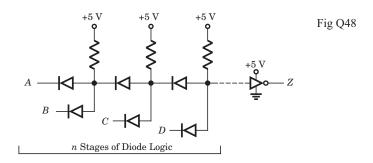
(C)  $\Sigma$ m(3, 12)

(D) 0

#### GATE ECE By R. K. Kanodia

**Unit Division**: Each unit has been further sub-divided into separate chapters and not clustered together. Thus the non-combination of all the problems in a single unit makes the reader, to remain focused and able to manage his time during his preparation.

**Q48.** The ideal inverter in fig. Q48 has a reference voltage of 2.5 V. The forward voltage of the diode is 0.75 V. The maximum number of diode logic circuit, that may be cascaded ahead of the inverter without producing logic error, is



- (A) 3
- (C) 5 (D) 9
- **Q49.** Consider the following set of 8085 μP instruction

MVI A, BYTE1
RLC
MOV B, A
RLC
RLC
ADD B

If BYTE1 = 07H, then content of accumulator, after the execution of program will be

(A) 46H

(B) 70H

(C) 38H

- (D) 68H
- **Q50.** Consider the execution of the following instruction by 8085 μp

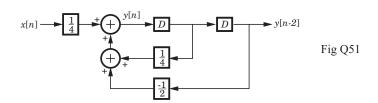
MVI H, 01FFH SHLD 2050H

After execution the contents of memory location 2050H, 2051H and registers H, L will be respectively

- (A) 01H, FFH, FFH, 01H
- (B) FFH, 01H, FFH, 01H
- (C) FFH, 01H, 01H, FFH
- (D) 01H, FFH, 01H, FFH

# GATE ECE By R. K. Kanodia

# **Q51.** The system shown in fig. Q51 is



(A) Stable and causal

(B) Stable but not causal

(C) Causal but unstable

(D) unstable and not causal

# **Q52.** The transfer function of a system is given as

$$H(z) = \frac{2\left(z + \frac{1}{2}\right)}{\left(z - \frac{1}{2}\right)\left(z - \frac{1}{3}\right)}.$$

Consider the two statements

**Statement(i)**: System is causal and stable.

**Statement(ii)**: Inverse system is causal and stable.

The correct option is

(A) (i) is true

(B) (ii) is true

(C) Both (i) and (ii) are true

(D) Both are false

**Q53.** A causal LTI filter has the frequency response  $H(j\omega)$  shown in fig. Q53. For the input signal  $x(t) = e^{-jt}$ , output will be

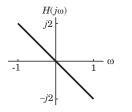


Fig Q53

(A) 
$$-2ie^{-jt}$$

(B) 
$$2ie^{-j}$$

(C) 
$$4\pi j e^{-jt}$$

(D) 
$$-4\pi j e^{-jt}$$

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**Q54.** The numeric value of  $a = \sum_{n=0}^{\infty} n \left(\frac{1}{4}\right)^n$  will be

$$(A)\frac{16}{9}$$

(B) 
$$\frac{9}{4}$$

(C) 
$$\frac{4}{9}$$

(D) 
$$\frac{9}{16}$$

**Q55.** Each of two sequence x[n] and y[n] has a period N = 4. The FS coefficient are

$$X[0] = X[3] = \frac{1}{2}X[1] = \frac{1}{2}X[2] = 1$$
 And  $Y[0], Y[1], Y[2], Y[3] = 1$ 

The FS coefficient Z[k] for the signal z[n] = x[n]y[n] will be

(B) 
$$6|k|$$

(C) 
$$6^{|k|}$$

(D) 
$$e^{j\frac{\pi}{2}k}$$

Q56. A Routh table is shown below. The location of pole on RHP, LHP and imaginary axis are

S 7	1	2	-1	-2	
s <sup>5</sup>	1	2	-1	-2	
S <sup>5</sup>	s <sup>5</sup> 3		-1		
s <sup>4</sup> 1		-1	-8		
S <sup>3</sup>	$s^3$ 7				
$s^2 - 18$		-21			
$s^1$	-9				
S 0	-21				

(D) None of the above

**Q57.** The open-loop transfer function of a *ufb* control system is

$$G(s) = \frac{K(1+2s)(1+4s)}{s^2(s^2+2s+8)}$$

The position, velocity and acceleration error constants are respectively

(A) 
$$0, 0, 4K$$

(B) 0, 
$$4K$$
,  $\infty$ 

(C) 
$$\infty$$
,  $\frac{K}{8}$ , 0

(D) 
$$\infty$$
,  $\infty$ ,  $\frac{K}{8}$ 

**Q58.** The forward-path transfer function of a *ufb* system is

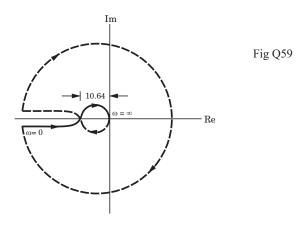
$$G(s) = \frac{K(s+1)(s+2)}{(s+5)(s+6)}$$

The break points are

	Break-in	Breakaway		
(A)	-1.563	-5.437		
(B)	-5.437	-1.563		
(C)	-1.216	-5.743		
(D)	-5.743	-1.216		

Q59. The Nyquist plot of a system is shown in fig. Q59. The open-loop transfer function is

$$G(s)H(s) = \frac{4s+1}{s^2(s+1)(2s+1)}$$

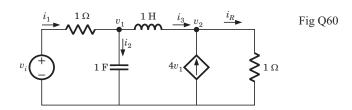


The no. of poles of closed loop system in RHP are

- (A) 0 (B) 1
- (C) 2 D) 4

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**Q60.** For the network shown in fig. Q60. The output is  $i_R(t)$ . The state space representation is



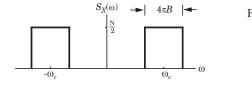
$$(A) \begin{bmatrix} \dot{v}_1 \\ \dot{i}_3 \end{bmatrix} = \begin{bmatrix} 1 & -1 \\ -3 & 1 \end{bmatrix} \begin{bmatrix} v_1 \\ i_1 \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix} v_i, i_R = \begin{bmatrix} 4 & 1 \end{bmatrix} \begin{bmatrix} v_1 \\ i_3 \end{bmatrix}$$

$$(\mathbf{B}) \begin{bmatrix} \dot{v}_1 \\ \dot{i}_3 \end{bmatrix} = \begin{bmatrix} -1 & -1 \\ -3 & -1 \end{bmatrix} \begin{bmatrix} v_1 \\ i_3 \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix} v_i, i_R = \begin{bmatrix} 4 & 1 \end{bmatrix} \begin{bmatrix} v_1 \\ i_3 \end{bmatrix}$$

$$(C) \begin{bmatrix} \dot{v}_1 \\ \dot{v}_2 \end{bmatrix} = \begin{bmatrix} 1 & -3 \\ 1 & 6 \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \end{bmatrix} + \begin{bmatrix} 1 \\ -1 \end{bmatrix} v_i, i_R = \begin{bmatrix} 1 & 4 \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \end{bmatrix}$$

$$(D) \begin{bmatrix} \dot{v}_1 \\ \dot{v}_2 \end{bmatrix} = \begin{bmatrix} 1 & 3 \\ -1 & 6 \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \end{bmatrix} + \begin{bmatrix} 1 \\ -1 \end{bmatrix} v_i, i_R = \begin{bmatrix} 1 & 4 \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \end{bmatrix}$$

**Q61.** The power spectral density of a bandpass white noise n(t) is N/2 as shown in fig. Q61. The value of  $n^2$  is



(A) NB

(B) 2NB

(C)  $2\pi NB$ 

(D)  $\frac{NB}{\pi}$ 

Q62. In a receiver the input signal is  $100 \,\mu\text{V}$ , while the internal noise at the input is  $10 \,\mu\text{V}$ . With amplification the output signal is 2 V, while the output noise is 0.4 V. The noise figure of receiver is

(A) 2

(B) 0.5

(C) 0.2

(D) None of the above

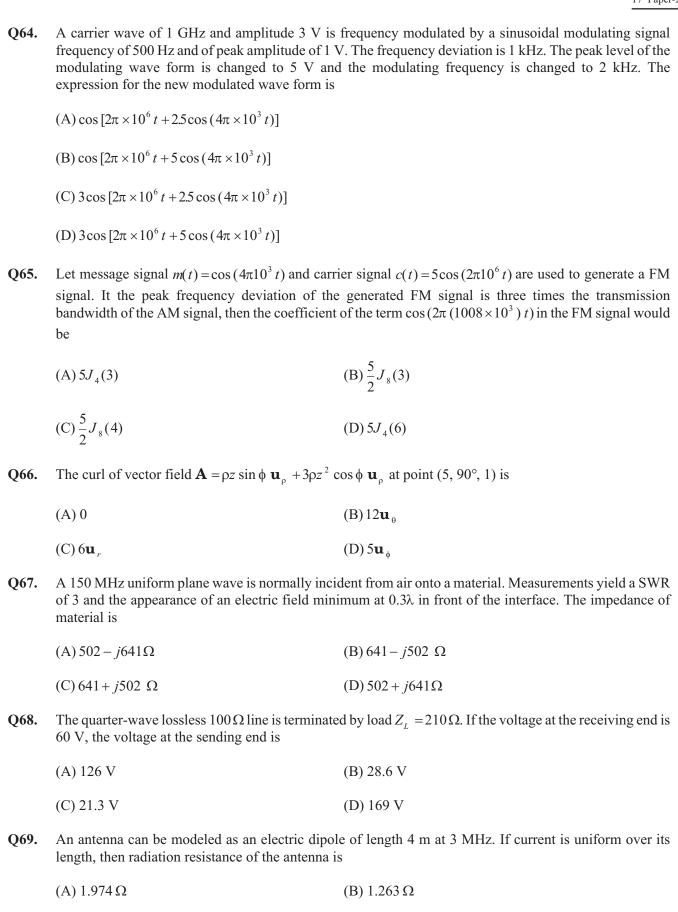
Q63. 12 signals each band-limited to 5 kHz are to be transmitted over a single channel by frequency division multiplexing. If AM-SSB modulation guard band of 1 kHz is used, then the band width of the multiplexed signal will be

(A) 131 kHz

(B) 81 kHz

(C) 121 kHz

(D) 71 kHz



(D)  $2.693 \Omega$ 

(C)  $2.186 \Omega$ 

**Q70.** An array comprises two dipoles that are separated by half wavelength. If the dipoles are fed by currents, that are 180° out of phase with each other, then array factor is

(A) 
$$\sin\left(\frac{\pi}{4}\cos\theta + \frac{\pi}{4}\right)$$

(B) 
$$\cos\left(\frac{\pi}{4}\cos\theta + \frac{\pi}{2}\right)$$

(C) 
$$\cos\left(\frac{\pi}{2}\cos\theta + \frac{\pi}{2}\right)$$

(D) 
$$\sin\left(\frac{\pi}{2}\cos\theta + \frac{\pi}{2}\right)$$

#### **Common Data Questions**

### **Common Data for Questions Q.71-73:**

For the circuit shown in fig. Q71-73 transistor parameters are  $V_{TN} = 2 \text{ V}$ ,  $K_n = 0.5 \text{ mA} / \text{V}^2$  and  $\lambda = 0$ . The transistor is in saturation.

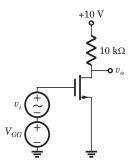


Fig Q71-73

- **Q71.** If  $I_{DQ}$  is to be 0.4 mA, the value of  $V_{GSQ}$  is
  - (A) 5.14 V

(B) 4.36 V

(C) 2.89 V

(D) 1.83 V

- **Q72.** The values of  $g_m$  and  $r_o$  are
  - (A)  $0.89 \text{ mS}, \infty$

(B) 0.89 mS, 0

(C) 1.48 mS, 0

- (D) 1.48 mS,  $\infty$
- **Q73.** The small signal voltage gain  $A_{\nu}$  is
  - (A) 14.3

(B) -14.3

(C) - 8.9

(D) 8.9

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**Variety**: The book carries in it a large variety of problems. The words of one of the senior educators of a reputed coaching institute bear testimony to the fact wherein he comments that "We can't expect so much variety of problems in a single book available in the market."

#### **Common Data for Questions Q74-75:**

Consider three continuous-time periodic signals whose Fourier series representation are as follows.

$$x_1(t) = \sum_{k=0}^{100} \left(\frac{1}{3}\right)^k e^{-jk\frac{2\pi}{50}t}, \qquad x_2(t) = \sum_{k=-100}^{100} \cos k\pi \ e^{-jk\frac{2\pi}{50}t}, \qquad x_3(t) = \sum_{k=-100}^{100} j \sin \left(\frac{k\pi}{2}\right) e^{-jk\frac{2\pi}{50}t}$$

- Q74. The even signals are
  - (A)  $x_2(t)$  only
  - (B)  $x_2(t)$  and  $x_3(t)$
  - (C)  $x_1(t)$  and  $x_3(t)$
  - (D)  $x_1(t)$  only
- Q75. The real valued signals are
  - (A)  $x_1(t)$  and  $x_2(t)$
  - (B)  $x_2(t)$  and  $x_3(t)$
  - (C)  $x_3(t)$  and  $x_1(t)$
  - (D)  $x_1(t)$  and  $x_3(t)$

Linked Answer Questions: Q76. to Q85. carry two marks each.

#### Statement for Linked Answer Questions: Q76. and Q77:

The parameters of an n –channel enhancement-mode MOSFET are  $V_{TN}=0.8 \text{ V}$ ,  $W=64 \, \mu\text{m}$ ,  $L=4 \, \mu\text{m}$ ,  $t_{ox}=450 \, \text{A}^{\circ}$ ,  $\mu_n=650 \, \text{cm}^2/\text{V}-\text{s}$ .

- **Q76.** The conduction parameter  $K_n$  is
  - (A)  $0.8 \,\text{mA/V}^2$
  - (B)  $0.8 \,\mu\text{A/V}^2$
  - (C)  $0.4 \,\text{mA/V}^2$
  - (D)  $0.4 \,\mu\text{A/V}^2$
- **Q77.** If  $V_{GS} = V_{DS} = 3 \text{ V}$ , then current  $I_D$  is
  - (A) 1.94 mA
  - (B) 2.87 mA
  - (C) 5.68 mA
  - (D) 3.84 mA

### Statement for Linked Answer Questions: Q78 and Q79:

The 8-bit left shift register and D-flip-flop shown in fig. Q78-79 is synchronized with same clock. The D flip-flop is initially cleared.

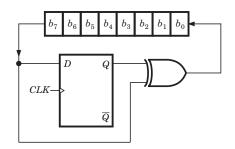


Fig Q78-79

- **Q78.** The circuit act as
  - (A) Binary to 2's complement converter
  - (B) Binary to Gray code converter
  - (C) Binary to 1's complement converter
  - (D) Binary to Excess-3 code converter
- Q79. If initially register contains byte B7, then after 4 clock pulse contents of register will be
  - (A)73

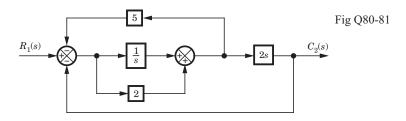
(B) 72

(C) 7E

(D) 74

## Statement for Linked Answer Questions: Q80 and Q81:

A block diagram is shown in fig. Q80-81.



- **Q80.** The transfer function for this system is
  - (A)  $\frac{2s(2s+1)}{2s^2+3s+5}$

(B) 
$$\frac{2s(2s+1)}{2s^2+13s+5}$$

(C) 
$$\frac{2s(2s+1)}{4s^2+13s+5}$$

(D) 
$$\frac{2s(2s+1)}{4s^2+3s+5}$$

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- **Q81.** The pole of this system are
  - (A)  $-0.75 \pm j1.39$

(B) -0.41, -6.09

(C) -0.5, -1.67

(D)  $-0.25 \pm j0.88$ 

# Statement for Linked Answer Questions: Q82 and Q83:

Ten telemetry signals, each of bandwidth 2 kHz, are to be transmitted simultaneously by binary PCM. The maximum tolerable error in sample amplitudes is 0.2% of the peak signal amplitude. The signals must be sampled at least 20% above the Nyquist rate. Framing and synchronizing requires an additional 1% extra bits.

- Q82. The minimum possible data rate must be
  - (A) 272.64 kbits/sec

(B) 436.32 kbits/sec

(C) 936.64 kbits/sec

- (D)None of the above
- **Q83.** The minimum transmission bandwidth is
  - (A) 218.16 kHz

(B) 468.32 kHz

(C) 136.32 kHz

(D) None of the above

#### Statement for Linked Answer Questions: Q84 and Q85:

In an air-filled waveguide, a TE mode operating at 6 GHz has

$$E_y = 15\sin\left(\frac{2\pi x}{a}\right)\cos\left(\frac{\pi y}{b}\right)\sin\left(\omega t - 12z\right)V/m$$

- **Q84.** The cutoff frequency is
  - (A) 4.189 GHz

(B) 5.973 GHz

(C) 8.438 GHz

(D) 7.946 GHz

- **Q85.** The intrinsic impedance is
  - (A)  $35.72 \Omega$

(B)  $3978 \Omega$ 

(C)  $1989 \Omega$ 

(D)  $71.44\Omega$ 

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E : Expressive
L : Less Erroneous
I : Individualistic
T : Targeted approach
E : Exhaustive content

# **Answers Paper-2**

1.	(C)	2.	(B)	3.	(B)	4.	(B)	5.	(C)
6.	(A)	7.	(A)	8.	(B)	9.	(B)	10.	(B)
11.	(B)	12.	(B)	13.	(D)	14.	(C)	15.	(A)
16.	(B)	17.	(C)	18.	(B)	19.	(C)	20.	(D)
21.	(A)	22.	(D)	23.	(C)	24.	(B)	25.	(B)
26.	(A)	27.	(B)	28.	(C)	29.	(C)	30.	(A)
31.	(A)	32.	(D)	33.	(A)	34.	(B)	35.	(C)
36.	(C)	37.	(C)	38.	(A)	39.	(C)	40.	(A)
41.	(B)	42.	(A)	43.	(B)	44.	(D)	45.	(B)
46.	(A)	47.	(A)	48.	(A)	49.	(A)	50.	(C)
51.	(A)	52.	(C)	53.	(B)	54.	(C)	55.	(A)
56.	(A)	57.	(D)	58.	(A)	59.	(C)	60.	(B)
61.	(B)	62.	(A)	63.	(D)	64.	(C)	65.	(D)
66.	(D)	67.	(C)	68.	(A)	69.	(B)	70.	(B)
71.	(C)	72.	(A)	73.	(C)	74.	(A)	75.	(B)
76.	(C)	77.	(A)	78.	(B)	79.	(C)	80.	(C)
81.	(C)	82.	(B)	83.	(A)	84.	(B)	85.	(B)

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Publishers, who claim to be the partners in the progress of students, use cheap tactics to sell their material to them. A publisher, famous for providing books on GATE, which are incidentally also erroneous & copied, has also imitated our book this year. They have gone to the extent this year and introduced a revised edition of their book on GATE on Elect. & Comm., which also includes a large number of Multiple Choice Questions [MCQ]. All the MCQ's & their solutions have been copied from our book [GATE by RK Kanodia]. This is condemnable act of plagiarism. They are trying to restrict our book from reaching the students, by giving high incentives to retailers & distributors. They are using all possible unfair means of the publishing field, to make money and monetary exploit students, yet the book fails to reach the students.

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Kindly note that our publication GATE-ECE by RK Kanodia, has the following features that make it an excellent study material in comparison to other books available on the GATE exam:

- MCQs: The book contains only solved Multiple choice questions (MCQ) which is the main requirement of the GATE exam. Each and every problem has its complete solution. We understand that theoretical studies should be done from the standard book, that one has studied for the semester exams and thus one should use the same material to understand the concepts of the same. We have deliberately excluded theoretical matter in the guide book so as not to mislead the students. However, wherever needed, satisfactory explanation of the formula has been included in the solution.
- Adherence to Pattern: All Multiple choice questions are strictly according to the GATE pattern. Every problem selected and included in the book is a model problem for the preparation of the exam which would thus prepare and equip the students better. Kindly note, that the standard of Multiple choice questions and their solution in every unit is much better than the ones available in a famous series of problems & solutions as far as GATE is concerned.
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