Time: 3 Hours

B. Tech Degree V Semester (Supplementary) Examination May 2006

EC/EB 503 ADVANCED MICROPROCESSORS AND MICROCONTROLLERS

(2002 Admissions onwards)

Maximum Marks: 100

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Ī.	(a)	List the differences between 8085 and 8086 microprocessor.	(4)
	(b)	Draw the internal architecture of 8086 processor and explain each block.	(10)
	(c)	Explain different types of interrupts in 8086 microprocessor.	(6)
II.	(a)	OR Neatly draw block diagram showing maximum mode configuration of 8086	
	(4)	microprocessor. Explain the organization in detail.	(10)
	(b)	With suitable examples explain various addressing modes for the 8086 instructions.	(10)
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Ш.	(a)	Explain various flow control instruction in 8086 microprocessor.	(10)
	(b)	Write a program to check whether a number is prime or not.	(10)
		OR	
IV.	(a)	Explain the following instructions:	
		(i) SCASB (ii) AAS	
		(iii) LEA (iv) TEST	
		(v) XLAT	(10)
	(b)	Describe the following program development tools:	
		(i) Linker (ii) Locator	(10)
		(iii) Debugger (iv) Emulator.	(10)
V.	(a)	Explain how the virtual addresses are translated to physical addresses by 80386	
v .	(a)	processor.	(10)
	(b)	Describe the architecture of an 80486 microprocessor. What is the enhancement in a	(10)
	(-)	cache memory system in 80486 compared to 80386?	(10)
		OR	
VI.	(a)	What is descriptor? Explain different types of descriptors in 80386 processor.	(10)
	(b)	How are tasks in 80386 protected from each other?	(10)
VII.	(a)	Explain the working of different functional units of the Pentium Processor.	(10)
V 11.	(a) (b)	Give architectural differences between Pentium – III and Pentium – IV microprocessors.	(4)
	(c)	Explain different properties of RISC processors.	(6)
	(4)	OR	(0)
VIII.	(a)	How dynamic branch prediction is implemented in Pentium Processor?	(10)
	(b)	Explain the use of instruction and data caches in Pentium.	(4)
	(c)	Compare Pentium and Pentium Pro architecture.	(6)
IV	7.3		
IX.	(a)	Explain the following instructions: (i) XCHD (ii) XRL	
		(i) XCHD (ii) XRL (iii) SWAP (iv) DJNZ	(8)
	(b)	Explain the different types of interrupts in MCS – 51.	(6)
	(c)	Briefly explain the USB. (universal serial bus)	(6)
	(~)	OR	(0)
X.	(a)	Explain with examples the various addressing modes available in the 8051	
	()	micro controller.	(10)
	(b)	Explain how an LCD can be interfaced with the 8051 micro controller.	(10)
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