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M.Sc. I Semester Examination, May 2006

COMPUTER SCIENCE

Paper 1.3

(Microprocessor and Microcontrollers)

Time: 3 Hours]

[*Max. Marks* : 100

Answer all questions.

Section A – (Marks: $8 \times 5 = 40$)

- 1. Construct a clocked RS Flip-Flop using logic gates and explain its working with necessary Truth table.
- 2. Explain the successive approximation A/D converter.
- 3. Discuss various types of storage memory used in micro processors.
- 4. Explain the instruction cycle of 8085.
- 5. Draw the timing diagram for memory read operation in the minimum mode 8086 and explain.
- 6. Mention various types of addressing modes for 8086.
- 7. Discuss the evolution of micro controllers.
- 8. Write a note on Hard disk controllers.

Section B – (Marks: $4 \times 15 = 60$)

9. (a) (i) Discuss different types of counters.

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(ii) Explain the construction and working of a four bit binary counter with necessary circuit diagram and Truth table.

Or

(b) (i) Define uninterrupted power supply.

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(ii) Explain the working of offline and online ups with necessary block diagrams.

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10. (a)	Exp	plain the Architecture of micro processor 8085 with a neat bloc 2008 with a neat bloc 2008 of UNIMOD	k diagram. 10 + 5
(d)	(i) (ii)	Explain various addressing modes used for 8085. Explain shift and Rotate instructions of 8085.	7 8
11. (a)	Ex	plain different types of Assembles directives and operators used Or	d in 8086.
(b)	Discuss the operation of programmable peripheral interface 8255 with a neat block diagram.		
12. (a)	(i) (ii)	Draw the block diagram of microcontroller 8051. Explain CPU and port registers of 8051.	
		e instruction cycle of 8085. TO	
(b)	(i)	Explain the Timer special function registers.	editated 6
	(ii)	What are the different interrupts used in 8051. Explain the interrupt special function registers.	talges bas 3
		re evolution of ratero controllers.	