L.I.C.D

Con. 5316-09.

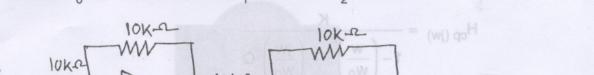
(REVISED COURSE)

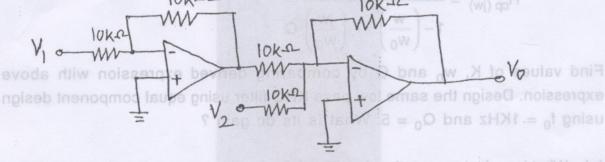
230001.30.

(3 Hours) [Total Marks: 100 Linear Integrated Circuits & Design

Question No. 1 is compulsory. polanA to equi eno una to prixiow

- (2) Attempt any four questions out of remaining six questions.
- (3) Assume suitable data if required and state it clearly.
 - (4) Figures to the right indicate full marks.
- (a) Calculate V_0 for the circuit for $V_1 = 5V$ and $V_2 = 2V$. If the viscos bins 1.





- (b) A 741C op-amp is used as an inverting amplifier with a gain of 50. The voltage gain Vs frequency curve of 741C is flat upto 20 KHz. What maximum peak to peak input signal can be applied without distorting the output? Slew rate for 741C is 0.5 V/us.
- Define following terms pertaining to Phase Lock Loop (PLL):
 - 5

- (i) Free running frequency
- (ii) Capture range
- (iii) Lock range.
- Explain the current boosting achieved in 723 IC.
- 5
- Prove with proper expression that, what produces more offset voltage at the 2. (a) 10 output; input offset current or input bias current? Discuss various compensating techniques for input offset voltage.
 - (b) With the help of op-amp model explain the slew rate limitation? Also explain 10 various method of increasing slew rate?
- (a) In the Astable multivibrator using IC 555, $R_A = 2.2 \text{ K}\Omega$. $R_B = 6.9 \text{ KW}$ and 3. 10 C = 0.01 μF. Calculate thigh, tlow, free running frequency and duty cycle. Derive the relation used.
 - What is the difference between normal rectifier and precision rectifier? With (b) proper derivation and circuits explain the working of full wave precision rectifier.

- Compare different types of ADCs based on their working principle. Explain working of any one type of Analog to Digital Converter. Managed (1) 8.8.M
 - In a free running multivibrator using op-amp derive the expression of free running frequency. State that how maximum operating frequency is determine.
- Draw circuit diagram of low pass KRC filter. Derive expression for transfer function 5. 20

$$H_{cp (jw)} = \frac{K}{1 - \left(\frac{w}{w_0}\right)^2 + \left(\frac{jw}{w_0}\right) Q}$$

Find values of K, wo and Q by comparing derived expression with above expression. Design the same low pass KRC filter using equal component design using $f_0 = 1$ KHz and $Q_0 = 5$. What is its dc gain?

- (a) What is an Instrumentation Amplifier? Derive the output gain of Instrumentation 6. Amplifier using three op-amp. State its characteristics.
 - (b) Using IC 7805 regulator. Design a 1amp current source.
- Write short notes on any three :-7.
 - Sample and Hold Circuit (a)
 - Peak Detector (b)
 - Switched Capacitor Filter (c)
 - V-F Converter Using Op-amp (d)
 - State Variable Filter. (e)

10

10

20