V-Ex-1-09-E-Scan-82 T-E- (COM) Sem VI (R) 28/5/09	
Con. 3084-09. Systems Programming 3 p.m. to (REVISED COURSE) 3 P.m. to VR-5:	6 p.p. 325
(3 Hours) [Total Marks :	100
<ul> <li>N.B.: (1) Question No. 1 is compulsory.</li> <li>(2) Attempt any four questions out of remaining six questions.</li> <li>(3) Figures to the right indicate full marks.</li> </ul>	
<ol> <li>(a) Explain with neat flow charts working of single pass assembler, with database used.</li> <li>(b) What are the features provided by a macro ? Explain with example.</li> </ol>	10 10
2. (a) Construct the LALR parsing table for the following grammar. $S \rightarrow S$ $S \rightarrow CC$ $C \rightarrow cC \mid d$	10
(b) Differentiate between linkage editor and linking loader.	10
<ul><li>3. (a) Explain storage allocation and error handling phase of a compiler.</li><li>(b) Explain the design of Direct Linking Loader.</li></ul>	10 10
4. (a) Describe the various forms of intermediate code used by compilers.	10
(b) What is binding? Explain static and dynamic binding.	10
<ul> <li>5. (a) Write short notes on :-</li> <li>(i) SPARC Assembler</li> <li>(ii) Text Editors.</li> </ul>	10
(b) Explain with the help of memory, data formats, registers, instruction format, addressing modes of traditional CISC machines.	10
6. (a) Explain the LR parsers with suitable examples.	10
(b) Explain with syntax directed translation.	10
7. Write notes on any two :	20
(i) LEX and Y ACC	
(ii) Operator precedence parsing	
(iii) Macro assemblers.	