

[TURN OVER

Con. 5003-CD-5817-07.

4. Determine and derive expressions for differential mode gain, common mode gain and CMRR for the circuit shown in **figure**. Assume early voltage $|V_A| = 100 V$ for n-MOSFET and $|V_A| = 50 V$ for P-MOSFET, for calculations of drain resistance of a transistor. Find the Q point of main transistors Q_1 and Q_2 and draw the frequency response nature.



 $K_{n} = K_{p} = 312 \ \mu A/V^{2}$

$$|V_{TP}| = V_{Tn} = 3 V$$

- 5. (a) For OP-AMP 741, explain following terms and give typical magnitudes in each case :----
 - (i) Input bias current
 - (ii) CMRR
 - (iii) Slew rate
 - (iv) Output resistance
 - (v) Power Supply Rejection Ratio.
 - (b) Draw typical Bode plots for one, two and three pole amplifiers (both gain and phase) a explain how stability of amplifier can be determined from Bode plot.
- 6. Design a two stage RC coupled CS amplifier using mid-point biasing to satisfy following specification $|A_v| \ge 15$, $V_o = 3 V$, $R_i > 1.2 M\Omega$.

Use JFET BFW11.

Determine $A_{\rm V},\,R_{\rm i}$ and $R_{\rm o}$ for designed amplifier.

- 7. Write short notes on any three of the following :---
 - (a) Compression of LC oscillator with RC oscillator.
 - (b) Advantages and disadvantages of negative feedback in amplifiers.
 - (c) Nyquist stability criteria.
 - (d) Concept of virtual ground in OP-AMP.