Reg.	No.	

Karunya University

(Declared as Deemed to be University under Sec.3 of the UGC Act, 1956)

End Semester Examination – November / December 2008

Subject Title: ELECTRON DEVICES Time: 3 hours
Subject Code: EC201 Maximum Marks: 100

Answer ALL questions $PART - A (10 \times 1 = 10 MARKS)$

1.	How do	vou increase	the	conductivity	z of i	intrinsic	semico	nducto	orʻ

- 2. What are the charge carriers found in P type semiconductor?
- 3. Define emitter efficiency.
- 4. In common base configuration with active region, the collector junction is biased in the ______ direction and the emitter junction in the _____ direction.
- 5. Emitter follower has ______ transistor amplifier configuration.
- 6. Which transistor amplifier configuration has lowest current gain?
- 7. Which type MOSFET is called normally off MOSFET?
- 8. Draw the schematic symbol of SCR.
- 9. What are tunnel diodes?
- 10. Draw the symbol of varactor diode.

$PART - B (5 \times 3 = 15 MARKS)$

- 11. What is avalanche breakdown?
- 12. Write the Eber's Moll equation for emitter and collector currents.
- 13. Define the various h parameters of bipolar junction transistor.
- 14. The intrinsic stand off ratio of UJT is 0.65. If the inter base resistance is 7.5 Kohms. Determine R_1 and R_2 .
- 15. Write short notes on opto coupler and mention its advantages.

$\underline{PART - C \ (5 \times 15 = 75 \text{ MARKS})}$

- 16. a. Show that in an intrinsic semiconductor the Fermi level lies midway between the conduction and valance band. (7)
 - b. Find the conductivity and resistivity of an intrinsic semiconductor at room temperature. It is given that (8)

$$\begin{array}{ll} n_i = 2.5 \ x \ 10^{13} \ cm^{-3} \ ; & \mu_n = 3800 \ cm^2 \ / \ sV \\ \mu_p = 1800 \ cm^2 \ / \ sV & e = 1.6 \ x \ 10^{-19} \ C \\ & (OR) \end{array}$$

- 17. Derive the condition of dynamic equilibrium for the density of charge carriers for the continuity equation. Also explain the three special cases of continuity equation.
- 18. Draw and explain the static characteristics of a Common Base transistor configuration. (OR)
- 19. Explain the basic structure of different bipolar transistor and the various transistor current components.

20.	a.	Explain the small signal low frequency T equivalent circuit of a common base configuration.	(10)					
	b.	Compare CE, CB and CC transistor configuration. (OR)	(5)					
21.		Oraw the hybrid – Pi model common emitter transistor and derive the values of the various omponents in terms of h – parameters.						
22.	a. b.	With neat diagram, explain the operation, V-I and transfer characteristics of N- charact	(10) (5)					
22	_	(OR)	` /					
23.	Exp	plain the construction, working and applications of a UJT with neat sketches.						
24	a.	What is zener effect? Explain the functions of a zener diode and its characteristics. Mention its applications.	(10)					
	b.	Mention the applications of DIAC and TRIAC. (OR)	(5)					
25.	Des a.	cribe the construction, working and application of the following devices. Photo Diode b. Light Emitting Diode	(8+7)					