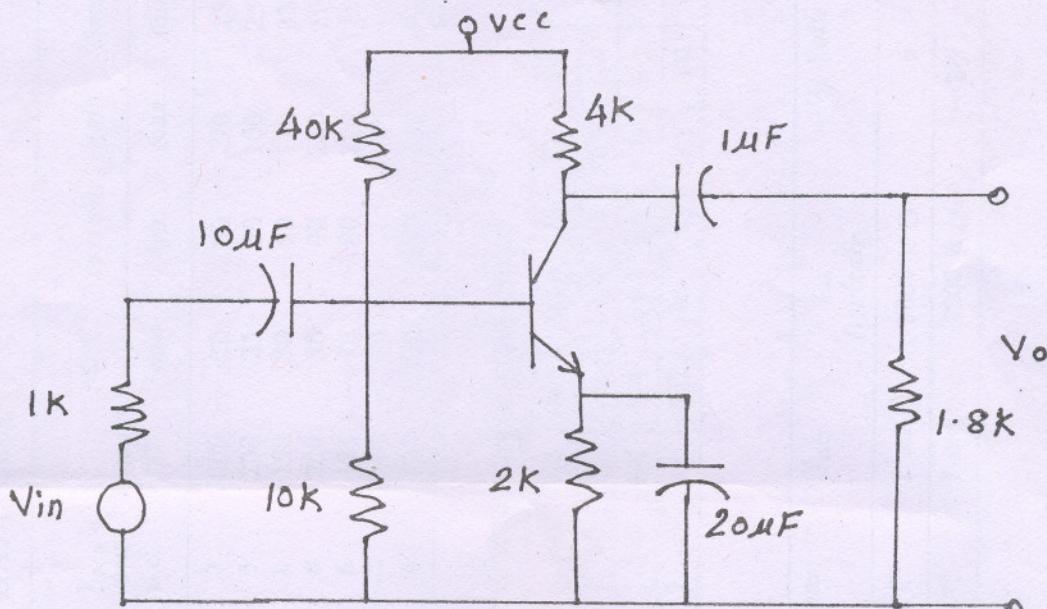


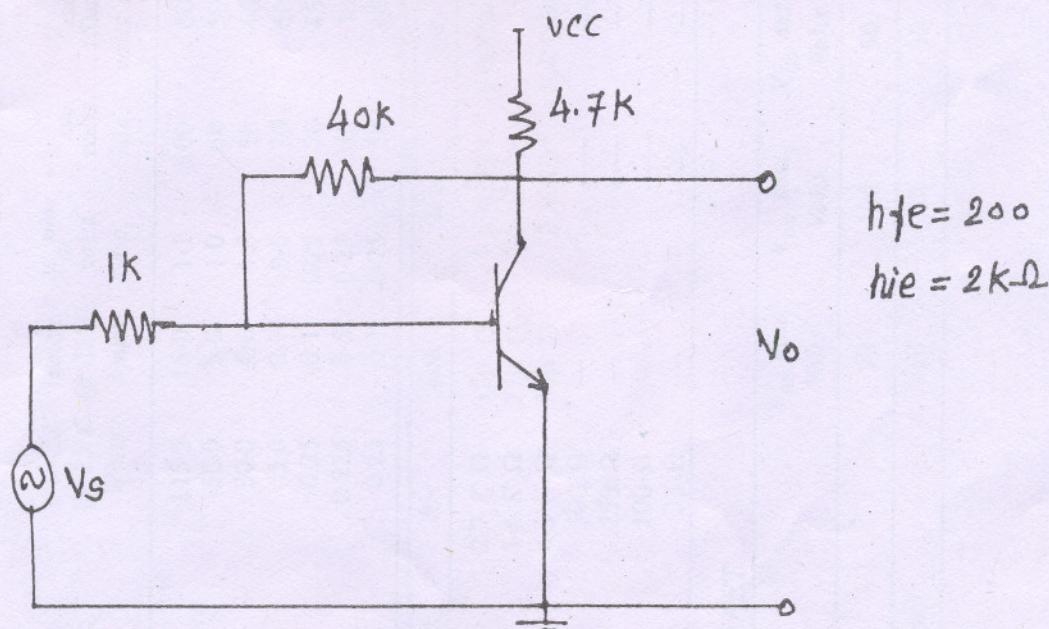
- N.B. (1) Question No.1 is **compulsory**.  
 (2) Answer any four out of remaining **six** questions.  
 (3) Assumption made should be **clearly** stated.

1. Design a two stage RC coupled BJT amplifier to meet the following specifications :— 20
- $A_v \geq 5000$ ,  $S_{IC0} \leq 10$ ,  $f_L = 20$  Hz,  $V_o = 2$  V and  $VCC = 15$  V.
- Calculate  $A_v$ ,  $R_i$  and  $R_o$  of the designed circuit.
  - What would be the voltage gain of the designed circuit if both bypass capacitors are removed ?
2. (a) Draw the equivalent circuit of FET amplifier at high frequency. Derive the expression for upper cutoff frequency. 10  
 (b) Find the lower cutoff frequency for the given amplifier and hence draw low frequency response plot. 10



$$h_{fe} = 200 \text{ and } h_{ie} = 2\text{k}\Omega.$$

3. (a) Explain working and analysis of transformer coupled Class A Power Amplifier. 10  
 (b) Design a large signal class B power amplifier to provide 5 W output to the 4 ohm load. 10
4. (a) For the following circuit, calculate  $A_{vf}$ ,  $R_{if}$  and  $R_{of}$  and also identify the type of feedback :— 10



- (b) Using suitable block diagrams explain various types of negative feedback circuits. 10

5. (a) Derive the expression for frequency of RC phase shift oscillator using BJT. 10  
(b) Design Op-amp based Wein-bridge oscillator which generates 10 KHz frequency. 10
6. (a) Derive an expression for the Differential voltage gain and differential input resistance of a dual input balanced output differential amplifier. 10  
(b) Draw circuit diagram using op-amps to realize  $V_o = 5V_1 + 7V_2 - 3V_3$ . 10
7. Write short notes on any four :— 20  
(a) Cascode Amplifier  
(b) Ideal characteristics of Opamp  
(c) Role of Level shifter circuit in Opamp  
(d) Crystal oscillator  
(e) Opamp as a Integrator.

**DBEC DATA SHEET**

Transistor type	Pdmax @ 25°C Watts	Icmax @ 25°C Amps	V <sub>CE</sub> <sup>(sat)</sup> volts d.c.	V <sub>CBO</sub> volts d.c.	V <sub>CEO</sub> (Sus) volts d.c.	V <sub>CER</sub> (Sus) volts d.c.	V <sub>CEx</sub> volts d.c.	V <sub>BEO</sub> volts d.c.	T <sub>j</sub> max °C	D.C. min	current typ.	gain max.	Small min.	Signal typ.	h <sub>fe</sub> max.	V <sub>BE</sub> max.	θ <sub>je</sub> °C/W	Derate above 25°C W/C
N 3055	115.5	15.0	1.1	100	60	70	90	7	200	20	50	70	15	50	120	1.8	1.5	0.7
ECN 055	50.0	5.0	1.0	60	50	55	60	5	200	25	50	100	25	75	125	1.5	3.5	0.4
ECN 149	30.0	4.0	1.0	50	40	—	—	8	150	30	50	110	33	60	115	1.2	4.0	0.3
ECN 100	5.0	0.7	0.6	70	60	65	—	6	200	50	90	280	50	90	280	0.9	35	0.05
BC147A	0.25	0.1	0.25	50	45	50	—	6	125	115	180	220	125	220	260	0.9	—	—
2N 525(PNP)	0.225	0.5	0.25	85	30	—	—	—	100	35	—	65	—	45	—	—	—	—
BC147B	0.25	0.1	0.25	50	45	50	—	6	125	200	290	450	240	330	500	0.9	—	—
Transistor type	hie	hoe	hre	θja														
BC 147A	2.7 K Ω	18 μV	1.5 × 10 <sup>-4</sup>	0.4°C/mw														
2N 525 (PNP)	1.4 K Ω	25 μV	3.2 × 10 <sup>-4</sup>	—														
BC 147B	4.5 K Ω	30 μV	2 × 10 <sup>-4</sup>	0.4°C/mw														
ECN 100	500 Ω	—	—	—														
ECN 149	250 Ω	—	—	—														
ECN 055	100 Ω	—	—	—														
2N 3055	25 Ω	—	—	—														

**BFW 11—JFET MUTUAL CHARACTERISTICS**

-V <sub>GS</sub> volts	0.0	0.2	0.4	0.6	0.8	1.0	1.2	1.6	2.0	2.4	2.5	3.0	3.5	4.0
I <sub>DS</sub> max. mA	10	9.0	8.3	7.6	6.8	6.1	5.4	4.2	3.1	2.2	2.0	1.1	0.5	0.0
I <sub>DS</sub> typ. mA	7.0	6.0	5.4	4.6	4.0	3.3	2.7	1.7	0.8	0.2	0.0	0.0	0.0	0.0
I <sub>DS</sub> min. mA	4.0	3.0	2.2	1.6	1.0	0.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

**V-Channel JFET**

Type	V <sub>DS</sub> max. Volts	V <sub>DG</sub> max. Volts	V <sub>GS</sub> max. Volts	P <sub>d</sub> max. @25°C	T <sub>j</sub> max. °C	I <sub>DSS</sub>	g <sub>mo</sub> (typical)	-V <sub>P</sub> Volts	r <sub>d</sub>	Derate above 25°C	θ <sub>ja</sub>
2N3822	50	50	50	300 mW	175°C	2 mA	3000 μV	6	50 KΩ	2 mW/°C	0.59°C/mW
BFW 11 (typical)	30	30	30	300 mW	200°C	7 mA	5600 μV	2.5	50 KΩ	—	0.59°C/mW