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# SATHYABAMA UNIVERSITY

(Established under section 3 of UGC Act, 1956)

Course & Branch: B.E-EEE

Title of the Paper: Computer Aided Design

Max. Marks: 80

Sub. Code: 414507

Time: 3 Hours

Date: 20/11/2010

Session: FN

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PART - A

(10 X 2 = 20)

Answer ALL the Questions

1. What are the basic features of Matlab?
2. What is simulink?
3. Write the Matlab analysis for RLC circuit.
4. Draw the different wave forms for DFT and FFT.
5. Draw the characteristics realization of diodes.
6. Write the Matlab function for poles and zeros.
7. Draw the block diagram of design flow of VHDL.
8. What are the two main data types used in VHDL?
9. What is meant by overloading?
10. What is the function of attributes?

PART – B  
Answer All the Questions

(5 x 12 = 60)

11. Explain different data types.  
(or)
12. Explain the different plotting commands.
13. Explain the Matlab analysis for Nodal analysis.  
(or)
14. Explain the Matlab analysis for two port networks.
15. Explain the zener diode characteristics realization using Matlab function.  
(or)
16. Explain the Matlab function for frequency response.
17. Describe the 1-bit full adder using the dataflow style.  
(or)
18. What are the two kinds of delays that can be specified in a procedural assignment Statement? Elaborate using an example.
19. What are the different types of operators?  
(or)
20. Explain different generics and configurations used in VHDL.