B. Tech Degree V Semester (Supplementary) Examination July 2009

EC/EI 505 MICRO ELECTRONICS AND INTEGRATED CIRCUITS

(2006 Scheme)

Time: 3 Hou	Maximum Marks :	100
	PART – A (Answer ALL questions) (8 x 5 =	40)
I. (a) (b) (c) (d) (e) (f) (g) (h)	List the characteristics of an ideal op-amp. Explain current to voltage converter using op-amp. Explain a peak detector using neat sketches. Draw the transfer characteristics of a Schmitt trigger and explain. What is hysterics? Explain the current limiting technique in a 723 voltage regulator. List the important specifications of ADC/DAC. Compare monolithic and hybrid IC's and also mention about its application area. Explain the fabrication of monolithic bipolar npn transistor.	
	PART – B $(4 \times 15 =$	· 60)
II.	Draw the circuit diagram of an instrumentation amplifier. Derive the expression for its output voltage. What are the advantages of instrumentation amplifier. OR	(15)
III (a) (b)	What is meant by input bias current in an op-amp? How it is compensated?	(6) (9)
IV. (a) (b)	Explain the concept of simulated inductance with neat diagram.	(8) (7)
V. (a) (b)		(8) (7)
VI.	Explain a 3 bit flash type analog to digital converter. Discuss its advantages and disadvantages.	(15)
VII. (a) (b)	OR What are the features of a 555 timer IC. Draw its functional block diagram and explain. What o/p voltage would be produced by a 4 bit DAC whose o/p voltage range is 0 to 10V and input binary number is 0111?	(11) (4)
VIII.	Discuss thin film and thick film technology. Explain its advantages and applications. OR	(15)
IX.	<u> </u>	(15)

