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Code: AE26 **Subject: POWER ELECTRONICS DECEMBER 2007** Time: 3 Hours Max. Marks: 100

NOTE: There are 9 Questions in all.

Question 1 is compulsory and carries 20 marks. Answer to Q. 1. must be written in the space provided for it in the answer

Q.1	Ch	noose the correct or best alternati	ve in the following:	(2x10)
	a.	A triac can be turned on by applying	ng	
		(A) a negative signal to gate.		
		(B) positive signal to gate.	144.	
		<ul><li>(C) either positive or negative sign</li><li>(D) None of the above</li></ul>	iai to gate.	
	b.	o. An UJT exhibits negative resistance		
		(A) before peak point.		
		(B) between peak and valley point		
		(C) any point between peak and v	alley.	
		( <b>D</b> ) both ( <b>A</b> ) & ( <b>C</b> )		
	c. The objective of connecting a resistance and capacitance across			ance and capacitance across gate
		(A) Over voltage	<b>(B)</b> dv/dt	
		(C) Noise signals	(D) Over currents	
	d.	Secondary breakdown occurs in		
		<b>(A)</b> BJT	(B) MOSFET	
		(C) BJT but not in MOSFET	( <b>D</b> ) both MOSFET and BJ	Γ
	e. In a dc choppers, per unit ripple is maximum when the duty cycle is			
		<b>(A)</b> 0.2	<b>(B)</b> 0.5	
		<b>(C)</b> 0.7	<b>(D)</b> 0.9	
	f.	A cycloconverter is		
		(A) ac-dc converter	(B) dc-ac converter	
		(C) ac-ac converter	<b>(D)</b> dc-dc converter	
	g. A converter with provision for both the output voltage and current be either positive or negative is called			e either positive or negative is called
		(A) full converter.	(B) dual converter.	
		<b>(C)</b> two quadrant converter.	<b>(D)</b> semi converter.	
	ŀ	h. A single phase converter is fed by a source having an inductance L. If load current is Io, the decrease in output voltage due to overlap is		
		(A) ©LI <sub>o</sub> /2π	( <b>B</b> ) <sup>ωLI</sup> <sub>o</sub> /4π	
		(C) ωLI <sub>0</sub> /π	( <b>D</b> ) ωLI <sub>o</sub> /3π	

i. The methods of voltage control in three phase and single phase inverters are the same.

(B) False

(A) True

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A full converter operates in four quadrants

(A) True

(B) False

## Answer any FIVE Questions out of EIGHT Questions. Each question carries 16 marks.

a. What is thyristor? Enumerate the various mechanisms by which thyristor can be triggered into conduction. What are the necessary **Q.2** conditions for turning on of an SCR? Discuss. (10)

b. It is required to operate 250A SCR in parallel with 350A SCR with their respective on state voltage drops of 1.6V and 1.2V. Calculate the value of resistance to be inserted in series with each SCR so that they share the total load of 600A in proportion to their current ratings.

(10)Q.3 a. Explain inverting mode of converters with suitable diagrams.

b. A 230V, 1kW resistance load is connected across a fully controlled single phase bridge converter. If the load is to be delivered a power of 800W, find

- (i) output voltage,
- (ii) firing angle and
- (iii) load current (rms value).

**(6)** 

a. Explain various steps to be carried out while designing chopper circuit. **Q.4 (7)** 

b. A dc chopper has an input of 200V and average output voltage of 150V. Load resistance is  $10\Omega$ . Find

- duty cycle, (i)
- (ii) average and rms load currents and
- (iii) effective input resistance.

(9)

Q.5 a. What is meant by commutation of SCR? How are commutation methods classified?

**(8)** 

b. What is unijunction transistor? Explain its configuration and characteristics.

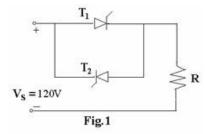
(8)

Q.6 Draw circuit diagram and wave shapes of input voltage, output voltage and load current in a single phase full wave ac regulator feeding purely resistive load? (10)

b. The single phase full wave controller shown in Fig.1 supplies a resistive load of  $R=10\Omega$ . The input rms voltage  $V_s$  is 120V, 60Hz. The delay angles of thyristors are equal:  $\alpha_1 = \alpha_2 = \pi/2$ . Determine

- the rms output voltage, (i)
- (ii) the input power factor
- the average current of thyristor (iii)

(6)



0.7 a. Explain the principle of cycloconverter using simple diagram. **(8)** 

b. Explain circulating current mode of cycloconverter.

**(8)** 

a. Draw the diagram of a three phase bridge inverter circuit and explain its working. What are the two modes of operations? How 0.8 these two modes are obtained? (10)

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b. A single phase full bridge inverter has purely resistive load and its voltage is controlled by multiple pulse width modulation technique. The width of each pulse is 30° and each half cycle has 5 pulses. If input voltage is 200V find (i) rms value of output voltage,

- (ii) pulse width to maintain the output power constant if input voltage is increased by 10%,
- (iii) maximum possible input voltage if maximum possible pulse width is 33°. (6)
- Q.9 a. Draw and explain block diagram for microprocessor based speed control of (i) dc motor and (ii) induction motor.(8)
  - b. A 3-phase half controlled thyristor bridge with 400V, 3 phase 50 Hz supply is feeding a separately excited dc motor. The motor parameters are: rated current 50A, resistance 0.1Ω, back emf constant 0.3V/rpm.
    - (i) calculate no load speed if no load current is 5A and firing angle is 30°, (ii) also find firing angle to obtain a speed of 1600 rpm at rated current. (8)