Enrolment No.\_\_\_\_

## **GUJARAT TECHNOLOGICAL UNIVERSITY**

M. E. I<sup>ST</sup> Semester–Remedial Examination – July- 2011

Subject code: 710701

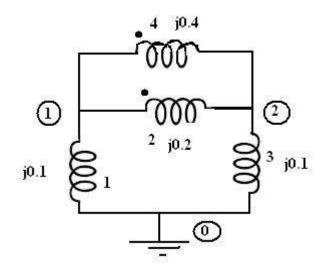
Subject Name: Power System Modelling and Simulation

## Date:07/07/2011

Time: 10:30 am – 01:00 pm Total Marks: 60

## **Instructions:**

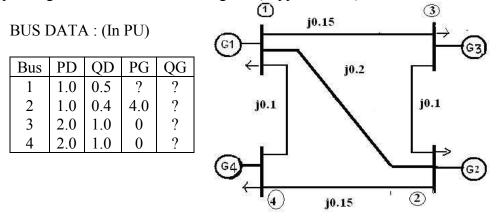
- 1. Attempt all questions.
- 2. Make suitable assumptions wherever necessary.
- 3. Figures to the right indicate full marks.
- Q.1 (a) Define following network matrices : Basic incidence matrix (A), Basic Loop 06 matrix (B), Basic cutest matrix (C) and Branch Path Incidence matrix (K).
  - (b) Derive following relations :  $Y_{bus} = A [y] A^{T}$ , using primitive admittance 06 matrix 'y' and network topology.
- Q.2 (a) How Z<sub>bus</sub> algorithm is used to add Link in to the existing partial network. 06 Derive all the equations used in the algorithm. The added element may be mutual coupled and may be connected to a reference node.
  - (b) For a given network, considering node-0 as reference node, find bus impedance 06 matrix using  $Z_{bus}$  algorithm. Elements 2 & 4 are mutually coupled having mutual reactance j0.1 per unit. Add elements as per order given by number shown in figure below.



OR

- (b) List all the assumptions made in short circuit (fault) analysis. And justify each 06 of them. Also state applications of short circuit analysis.
- Q.3 (a) Draw the flowchart for FDLF method for 'n' bus power system having both PV 06 and PQ buses. State all assumptions made in decoupled load flow method to obtain FDLF algorithm.
  - (b) Write a short note on concept of 'optimal Power flow'. Explain 'Gradient 06 method' OR 'Linear programming method' for optimal power flow.

- Q.3 (a) Explain 'Approximate load flow method' or 'Direct load flow method' for 'n' 06 bus system. State all the assumption made to derive equations and justify each of them.
  - (b) Consider 4 bus system shown below, where in line reactance are indicated in 06 per unit. Line resistances are negligible. The magnitude of all four-bus voltages is specified to be 1.0 per unit. Injected bus powers (S) are specified below. Considering bus-1 as slack bus find out only real power line flows and reactive power generation at each bus using DC (Approximate) load flow method.



Q.4	<b>(a)</b>	Explain 'Generation shift factor' and 'line outage distribution factor' for security of power system.	06
	<b>(b)</b>	Explain performance index (PI). How it is useful for contingency selection?	06
		OR	
Q.4	<b>(a)</b>	Discuss effect of traveling waves on short circuited transmission line.	06
	<b>(b)</b>	Explain Bewles Lattice diagram and state its usefulness in studying	06
		performance of traveling waves on power system.	
Q.5	<b>(a)</b>	What is 'State Estimation'? State application of state estimation in power system.	06
	<b>(b)</b>	Explain Maximum Likehood Weighted Least Squares Estimation.	06
		OR	
Q.5	<b>(a)</b>	Explain sparsity techniques and its advantages. State any one method to store	06
		sparse matrix in computer.	
	<b>(b)</b>	Compare backward Euler's method and trapezoidal method in context to	06

\*\*\*\*\*