Roll No.

Total Pages: 2

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BT-5/D07 COMPUTER HARDWARE DESIGN PAPER - ECE-303E

Time: 3 Hrs.

Maximum Marks: 100

Note: Attempt any five questions.

- Describe in brief the following terms :
 - a. Register Transfer Language
 - b. Micro-operation
 - c. Registers
 - d. Register Transfer
 - e. Control Function

20

a. Find the solution of the given expression using Booth's algorithm (in tabular form):

10

- b. Perform the following operations using 2's complement representation :
 - i. (-53) + (+27)

10

- Explain the following types of memory :
 - a. Cache memory
 - b. Associative memory

20

- 4. a. Write short notes on :
 - i. Cross bar network

(5th sem. Electronics)

15

		ii. Hypercube network			
		iii. Tree network	10		
	b.	Write short note on 'Pipelining"	10		
5.	a.	Write short notes on :			
1		i. Subroutine			
		ii. Macros			
		iii. Tristate Buffer	12		
	b.	Explain Program Interrupt and its operation with d	iagram.		
			8		
3.	a.	Write short notes on :			
		i. Hardwired control			
		ii. Microprogram control	10		
	b.	Describe the microinstruction format for the	control		
		memory.	. 10		
	a.	es per			
		instruction is 2.0 when all memory accesses hit in th			
		cache. The only data acesses are loads and stores and			
	0.0	these total 40% of the instruction. If the miss pe	nalty is		
		25 clockcycles and miss rate is 2%, how much			
	Y.	would the machine be if all instructions were cache hits?			
			10		
	b.	Write short note on Magnetic Disc Memories	10		
		Describe in detail the different types of modes	of data		
		transfer.	20		

2,80	different modes of their propagation ? Explain	n ground		
	waves.	15		
b.	b. What waves are used for intercontinental			
	cation? Why ?	. 5		
a.	. How does ionosphere limits the communicatio	n to outer		
	space ?	5		
b.	Write short notes on the following:			
	i. Maximum usable frequency			
	ii. Skip distance			
	iii. Critical frequency	15		