BT-5/D09

MICROPROCESSOR AND INTERFACING

Paper: ECE-311(E)

Time : Three Hours		[Maximum marks: 10

Note: Attempt five questions in all, selecting at least one question from each unit.

UNIT-I

- What are Von-Neumann architecture of computers? Give 1. the examples and explain technological trends in microprocessors development.
 - Define what is a Tristate logic. What do you mean by tristating of buses?
 - Write the features and specifications of microprocessor 8086.
- (a) Draw the functional block diagram and explain the 2. features of clock generator 8284 used with 8086.
 - What is the role of Pointer and Index register in 8086? How they can used, explain with the help of examples. 6
 - Explain the programming model of 8086.

UNIT-II

- Explain the function of the following pins: 3. POW10 OF NMI, TEST, DEN BHE, Ready.
 - (b) Describe the operation performed by the following instruction:
 - (i) MOV [SI], BX.
 - (ii) AND BX, [SI].
 - (iii) AAA.
 - (iv) IMUL CL.
 - (v) SBB BX, CX.
- Write an 8086 assembly language program to complement the LSB of flag register using LAHF and SAHF instructions.
 - Explain the different addressing modes of 8086 with the 10 help of suitable examples.

UNIT-III

Draw and explain the timing waveforms for read operation of 8086 in minimum mode.

	(b)	Compare the features of DRAM and SRAM.	5
5.	(c) (a)	Explain the concept of Refreshing of DRAM. Explain the features and functions of DRAM controll Explain the features and functions of DRAM controll TMS-4500 with the help of its functional block diagram	er
	(4)	TMS-4500 with the help of the	10
	(b)	Explain in detail the features and functions of different types of Memory devices.	nt 10
		the interfacing of PPI 8255 to	the
7.	(a)	Sketch and explain the intermediate and sold microprocessor in minimum node. Define an Interrupt and give the differen types	of
	(b)	Define an Interrupt and give	8
8.	W	. I at notes on any two of the tollowing	10
0.	(a)	Description and illerracing	10
	(b	V-termint vector table.	10
	(c	c in of 8 x x key Doard.	•