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Roll N	lo Total Pages :
ů.	952
×81 0	BT-5/D08
	MICROPROCESSOR AND INTERFACING
	PAPER - ECE-311E
Time :	3 Hrs. Maximum Marks : 10
Note:	Attemps any five questions by selecting at least on
ĒĐ,	question from each unit.
2000	UNIT-I
1. a.	Explain the overview of microcomputer structure an
	operation with the help of a block diagram of a simpl
	microcomputer. 7
b.	Explain the technological trends in microprocesso
	development.
с.	Explain Intel family tree.
2. a.	Explain the special purpose of Pointer and Index register
	with the help of suitable examples.
b.	Differentiate between Minimum and Maximum mode
	of operation of 8086.
C.	Explain the functions of IC 8284 in 8086 based systems

UNIT-II

3. a. What do you mean by an Instruction format ? Explain

examples:

(5th sem. Electronics)

ADC, LEA, PUSH, INC, JNZ

the following instructions with the help of suitable

b. What is the use of assembler directives and macros.

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		explain the following with suitable examples :	
		ASSUME, SEGMENT	10
4.	а.	Write a delay loop which produces a delay of 50	0 μ sec
		on an8086 with a 5 MHz clock.	10
	b.	Write a program that adds a profit factor PROF	IT = 1
		to each out of six elements in COST array and	store
		the result in PRICES array.	10
		UNIT-III	
5.	a.	What are the advantages of DRAM over SRAM?	Explair
		the concept of interfacing and refreshing of DRA	AM with
		the help of a functional block diagram of DRAM.	. 12
	b.	What are the different functions of an address de	coder?
		Explain with the help of an example.	8
6.	a.	Draw and explain the timing waveforms for	r read
	N	operation of 8086 in minimum mode.	14
JYYI	b.	What are the different types of memory devices as	/ailable
		and their advantages ?	6
		UNIT - IV	
7.		Explain the operation, characteristics, specific	ations,
		applications and interfacing of D/A converters with	n 8086.
			20
8.		Write short notes on any two:	
	i.	DMA controller interfacing with 8086	10
	ii.	8255 description and Interfacing with 8086	10
	iii.	Interfacing alphanumeric displays	10