

Code No: RR310201

III B.Tech I Semester(RR) Supplementary Examinations, November 2010

COMPUTER ORGANIZATION

(Common to Electrical & Electronics Engineering, Electronics & Communications Engineering, Electronics & Instrumentation Engineering and Instrumentation & Control Engineering)

Time: 3 hours

Max Marks: 80

Answer any FIVE Questions  
All Questions carry equal marks

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1. Describe all optional PCI signal lines with designation and type. [16]
2. Discuss about the IEEE standard for binary floating point arithmetic [16]
3. NOOP instruction has no effect on the CPU state other than incrementing the program counter. Suggest some uses of this instruction with examples. [16]
4. (a) List and describe different Motorola 88000 bit-field instructions.  
(b) Discuss about register management in Motorola 88000 [8+8]
5. (a) Explain different techniques of cache mapping function  
(b) what are the merits and demerits of each. [10+6]
6. (a) Discuss about possible DMA configurations.  
(b) What is vectored interrupts.  
(c) Discuss about 68000 interrupt structure. [6+4+6]
7. (a) Differentiate between impact and non-impact printers.  
(b) Elaborate on mechanical printers.  
(c) Discuss about joysticks which use potentiometers as position transducers. [5+5+6]
8. (a) Discuss about exception in multiple execution unit pipelined processors with examples.  
(b) Discuss about dispatch and superscalar operation in multiple execution unit pipelined processors [8+8]

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