## DISTANCE EDUCATION

## B.C.A. DEGREE EXAMINATION, DECEMBER 2010. ELECTRONIC DEVICES AND DIGITAL CIRCUITS <br> (2003 onwards)

Time : Three hours
Maximum : 100 marks
Answer any FIVE questions.

1. (a) What is Excess-3 code? Why it is important?
(b) Write short notes on ASCII code.
(c) Convert the following :
(i) $(5 A 8)_{16}=?_{2}$
(ii) $\quad(555)_{10}=?_{8}$
(iii) $(741)_{8}=?_{2}$
(iv) $(7400)_{8}=?_{10}$.
2. (a) "NOR gate is a universal building block" - Justify. (10)
(b) What are the basic operations available in digital system? Explain with gate symbol.
3. (a) Explain the function of Full Adder circuit.
(b) State and prove Demorgan's law with truth tables.
4. (a) Simplify the function :

$$
\begin{equation*}
F(x, y, z)=\sum(0,1,3,6,7) . \tag{10}
\end{equation*}
$$

(b) Using Demorgan's theorem, show that:
(i) $\quad(A+B)^{\prime}\left(A^{\prime}+B^{\prime}\right)=0$
(ii) $A+A^{\prime} B+A^{\prime} B^{\prime}=1$.
5. (a) Define Flip-flop. With neat diagram and truth-table explain the R-S flipflop. (10)
(b) Write short notes on Right shift and left shift operations. (10)
6. (a) Design a 2 -bit shift register using JK flip flop. (10)
(b) What are the various types of counters? Explain.
7. (a) Explain the formation of the depletion region across an unbiased P-N junction. (10)
(b) What are the different configurations of BJT? Explain. (10)
8. (a) Explain the construction of N-channel JFET.
(b) Explain the four distinct regions of the output characteristics of a JFET. (10)

