## **2010 Robert Bosch Placement Paper Pattern**

Robert Bosch visited BIT (Bangalore Institute Of Technology) on 29th of jan 2010, about 150 members attended the

written test but only 16-17 were selected.

The procedure for selection was as follows

- 1)A written test
- 2)One or two technical rounds for those who are through written test
- 3)Mostly one but some times two HR rounds

The written test was quite ok, they had given 60 questions and we were suppose to answer it in 60min the test was separate for EC and NON EC students (I had taken EC test i don't know much about NON EC test)

it had the following sections (no sectional cut off)

- a) Technical (concentrate on Logic design, network analysis, analog electronics, electronic devices, power electronics)
- b) Quantitative (it was just a cake walk, the questions were from 10th maths (AP, GP) and nothing was difficult)
- c) Verbal (one antonym, one synonym, arranging sentences in order to make the paragraph meaningful, reading comprehension)

Once this was done around 60 were short listed for the technical round (it was 2:1 that is one student and 2 interviewers :) ,common

for both EC and NON EC people)

In this for each student the interview was about 1hour, they mostly concentrated on clippers, clampers etc, (analog circuits),8051 architecture

and programming, c-programming and if you have done any projects it will add up a lot in this round, they concentrate more on your projects in this case.

one more important thing, if the interviewer is good at c but if he feels that your good at 8051 then you will be asked to take one more technical round

in this case the 2nd interviewer will be asking you about 8051.(this concept in the interview is very good i liked it the most)

After this round 20 -25 were selected for HR round, and HR round was very simple and easy, the questions were like

- a) Tell me about yourself
- b) How many flights does kingfisher airlines has (answer is not important, rather the way you approach is important)......