(c) Maximum mode of 8086 processor.

Dec. Hara 170 (Compon & I.T.) sem 5 (Rev.)

Con. 5577-07.

Microprocessols

CD-6030

(REVISED COURSE)

(3 Hours)

[Total Marks: 100

N.B		Question No. 1 is compulsory. Attempt any four questions out of remaining six questions.	
1.		sign a single board computer system with following specifications— (a) 8086 processor working at 5 MHz. (b) 8087 NDP (c) 32 KB EPROM using 2764 devices. (d) 12 KB SRAM using 4KB devices. (e) Two interrupt drives 8 bit input ports. (f) Two interrupt driven 8 bit output ports. sign the system using absolute decoding technique. Show memory and I/o maps.	20
2.		Explain 8237 DMA Controller in detail. Explain the various Data Formats supported by 8087 NDP, and convert (20-875) ₁₀ into short real format.	10
).	(a) (b)	Explain Interrupt Structure & 8085 processor. Explain operating modes of 8254 PIT and draw any four with timing waveforms.	10
4.	(a) (b)	Draw block diagram of 8259 PIC and explain it's operation in brief Draw timing diagram of tack to back interrupt acknowledge bus cycles in minimum mode or 8086 processor and explain.	10
5.	(a) (b)	List and explain various string instructions and prefixes of 8086 instruction set. Show the interacing of DRAM chips using 8203 DRAM controller and explain in brief.	10
6.	(a) (b)	What do you mean by Assembler directive? Explain with example all the directives. Draw interfacing diagram of 3 PIC, cone master and two slaves connected to 8086 processor and explain the operation.	10
7.	(a)	te short notes (any two) – What is microprocessor and how it become CPU of a computer? RS 232 C serial Interface.	20