		164 Exam.)	TE((CX)	OP) V	(R)		11112109	
ws O	et 09-1	1164	Mich	mores	SOV			
Con	. 531	7-09.	P	EVISED CO	DURSE)		SP-85	552
				(3 Hour	s)		[Total Marks .	100
N.B.	: (	1) Question N	o. 1 is cor ip	ulsory.				
	(2	2) Solve any f	our quest of	ns from the	remaining six	<b>c</b> quest	ions.	
1.		ign a 8086 bas		_	_	:-		20
			processor w 64 KB using					
	,		2 KB using			^	-	
					, interrupt dev		0	
	Expl	ain the design.	Also show	the memory	y and I/o map.			
2.	(a)	Write a program			a block of data	using st	ring instructions.	8
	(b)				35 microproces	> sor with	a neat diagram.	12
0	(-)	E de la la de la constante de						10
3.	(a)	Also explain the			101	naxımur	m mode system.	10
	(b)	Show an inter	facing diagr	am of the	8086 With the		DMA controller.	10
				of Direct on	emory access	? Enur	merate with two	
		practical exam	ipies.					
4.	Exp	Explain the need of Bus arbitration. What are the various Bus arbitration schemes						20
in loosely coupled systems? Draw a multiprocessor system of Z-8086 r						-8086 modules		
	usin	g the 8289 Bus	s arbitor. Ex	plain the dia	agram.			
5.	(a)	Explain the fol	llowing With	respect to	the 8259 PIC			12
0.	(4)		Fully Nested		0200 1 10			
		(ii) Sperial	Mask Mode					
		(iii) Initializa			59			
	(h)	(iv) Function			ta bus is domi	ultiploy	ad in the 9095	8
	(b)	Draw and exp	O THE	adders/da	ia bus is demi	umpiexe	ed in the 8085.	0
6.	(a)					es of seg	gmented memory	10
	(b)	with reference				respec	t to architecture,	10
	(5)	instruction set			•	гезрее	t to aromitottare,	
7.	Writ	e short notes of						20
		11/11/	munication u					
	1		peration of t rfacing in 80					
	,		Generator	Job Systems				
	,	,	terrupts in 8	8086				
	,		re Controller		6.			