SemTIT /RWI IT. / E. D. C. / DRe 09

Con. 5208-09.

(REVISED COURSE)

SP-7442

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		(3 Hours)	[Total Marks : 100)
N.	.B.: (1) Question No. 1 is comp (2) Attempt any four question (3) Assume suitable data if r (4) Figures to the right indi	ons out of remaining six necessary.	questions.	
1.	(a) Explain the criterion for selection(b) Draw the circuit diagram output differential amplifier.	and explain the operat	•	
	(c) With the help of block diagonal (d) State and explain Barkhuse	ram, explain the operati		į
2.	(a) Explain the working of 3 expression for output vol		tion amplifier. Derive the	10
	(b) Draw the circuit diagram ar(c) Design the RC phase shift	•	•	
3.	` '		on of Integrator. What are recomed in practical circuit?	
	(b) Draw the circuit diagram an using OPAMP.	· ·	•	
4.	(a) A 6 bit DAC has an input 10	00 101 and 10V referen) Conversion Resolution		10
	(b) Design astable multivibrate			10
5.	(a) What are advantages of act		p of circuit diagram, explain	10

(b) Draw the circuit diagram and explain the operation of mono stable multivibrator. 10

6. (a) Draw the circuit diagram and explain the operation of high voltage regulator using IC 723.

(b) Differentiate between linear and switching regulator.

4 10

6

(c) Derive the expression for stability factor 's' for different BJT biasing circuits.

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7. (a) Write a program for SPICE simulation of Non-inverting amplifier.

10 10

(b) For the differential amplifier determine I_{CQ} , V_{CEQ} , A_d , A_c , C_{MRR} if $h_{ie} = 1k$,

h_{fe} = 100, V_{BE} = 0.6 V.